

A Theory of Skiplists with Applications to the Verification of Concurrent Datatypes

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Motivation

**Why do we want a decidable theory
for skip lists?**

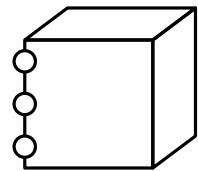
Verification of Concurrent Data-structures

Main Idea

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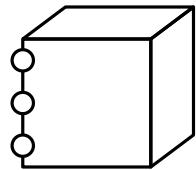
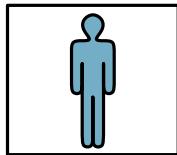
Concurrent DataStructure



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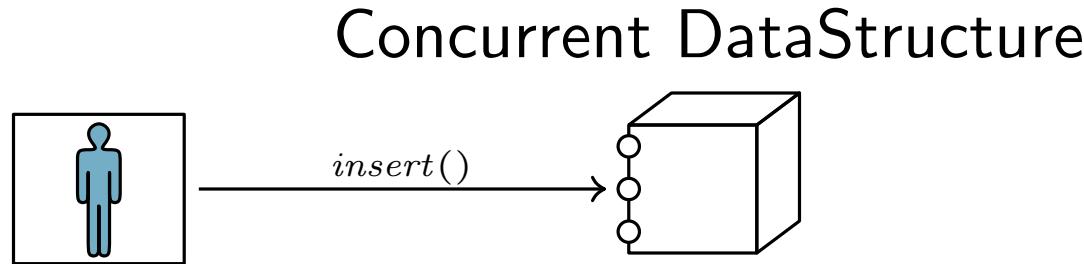
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Most General Client

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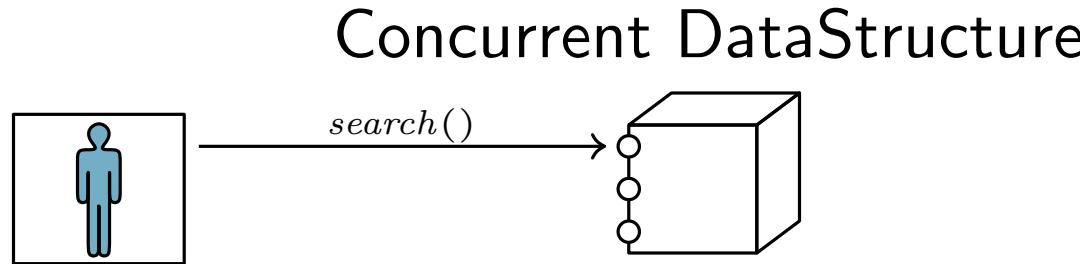
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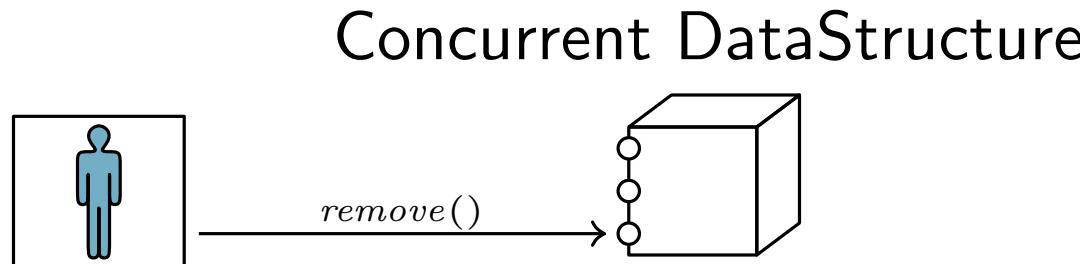
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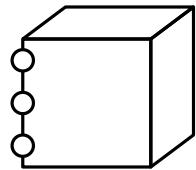
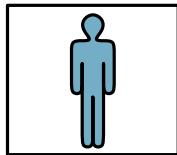


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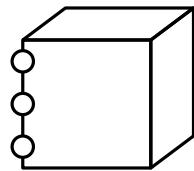


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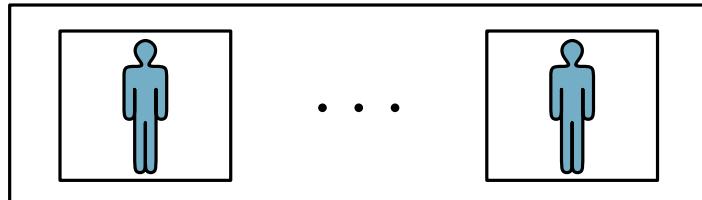
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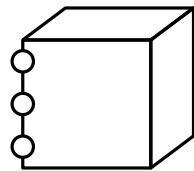
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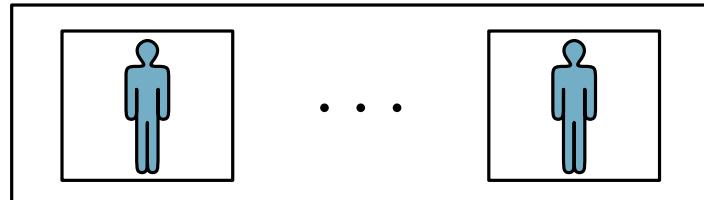
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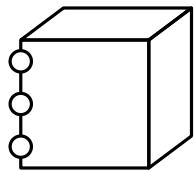


$$P[N] : P(1) \parallel \cdots \parallel P(N)$$

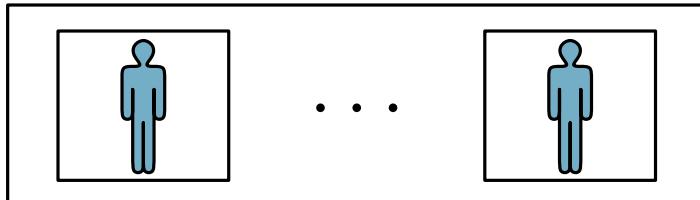
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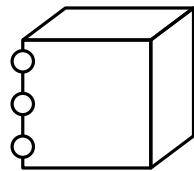
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ghost variables

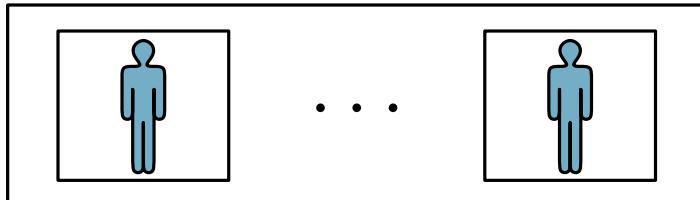
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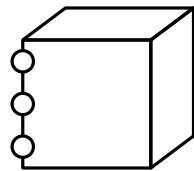
Property

$\varphi^{(k)}$

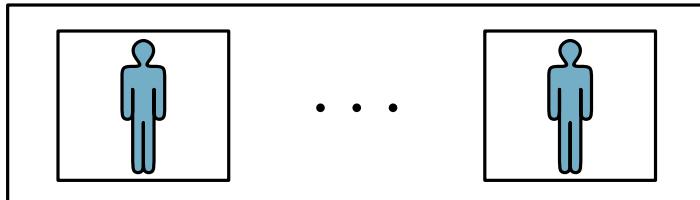
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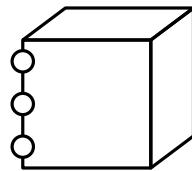
LTL ($\square, \diamond, \mathcal{U}, \dots$)

A curved arrow points from the text $\varphi^{(k)}$ up towards the LTL formula, indicating a relationship between the property and the temporal logic expression.

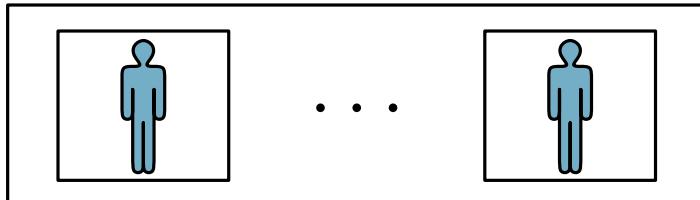
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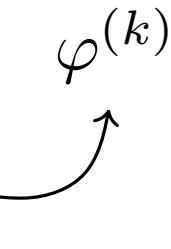
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Diagram

\mathcal{D}

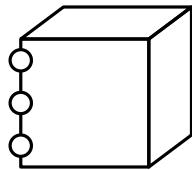
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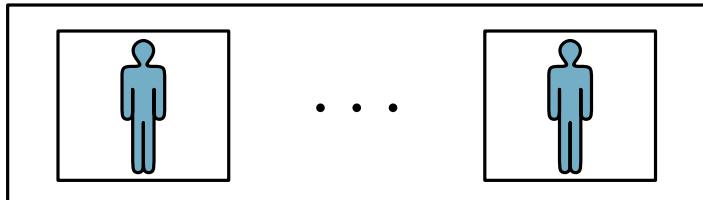
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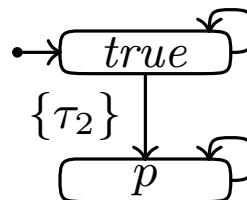


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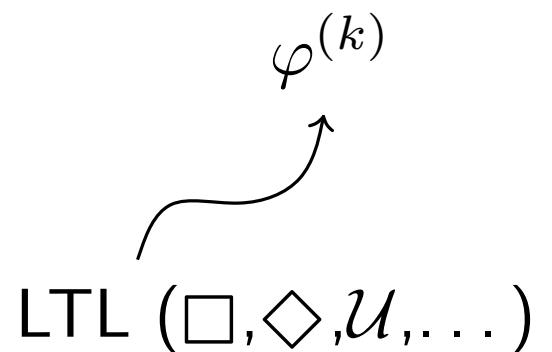
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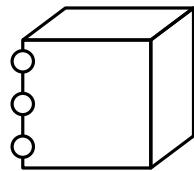
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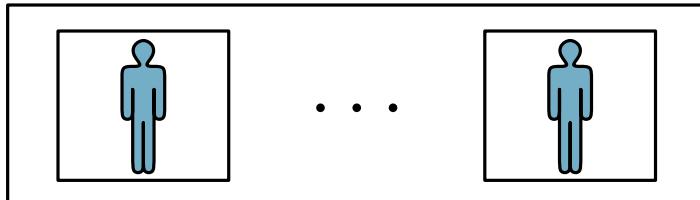
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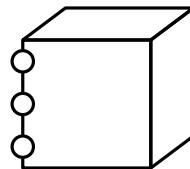
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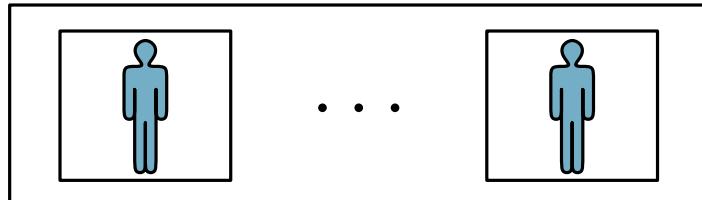
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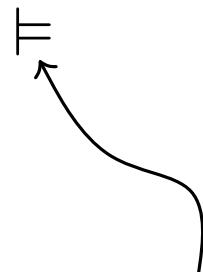
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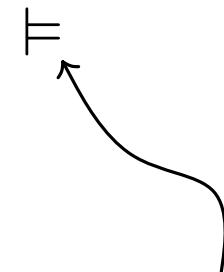


Verification Conditions:

- ▶ Initiation
- ▶ Consecution
- ▶ Acceptance
- ▶ Fairness

Property

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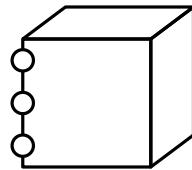


Satisfaction
(Model Checking)

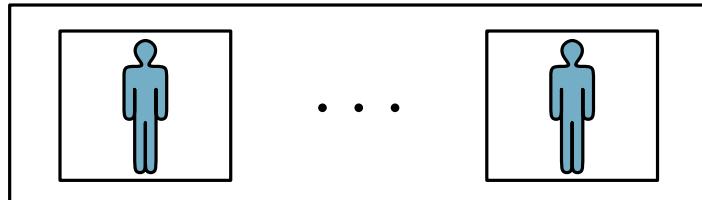
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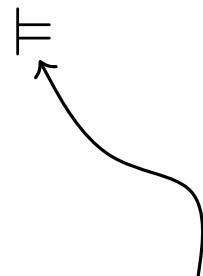


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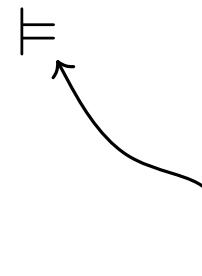
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Verification Conditions:

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Satisfaction
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Decision Procedures
(first order propositional logic)

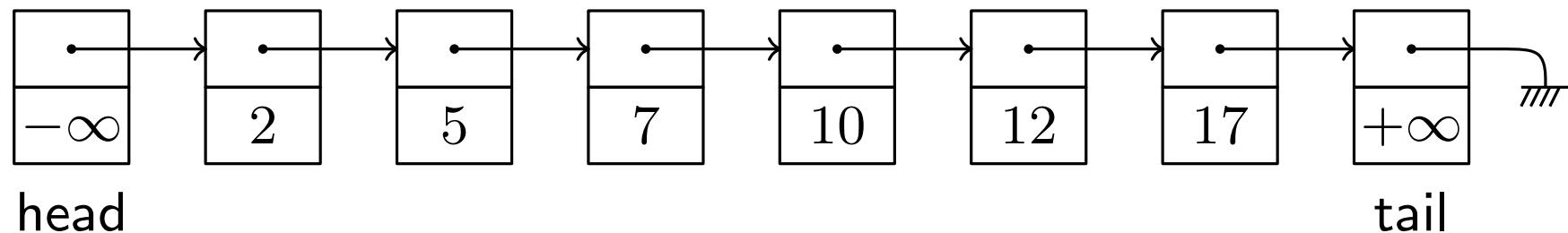
Concurrent Lock-Coupling SkipLists

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- ▶ Sorted list of elements

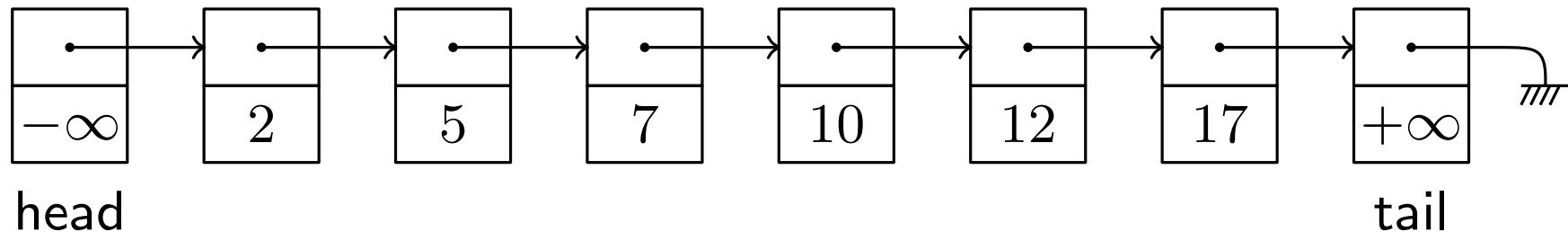
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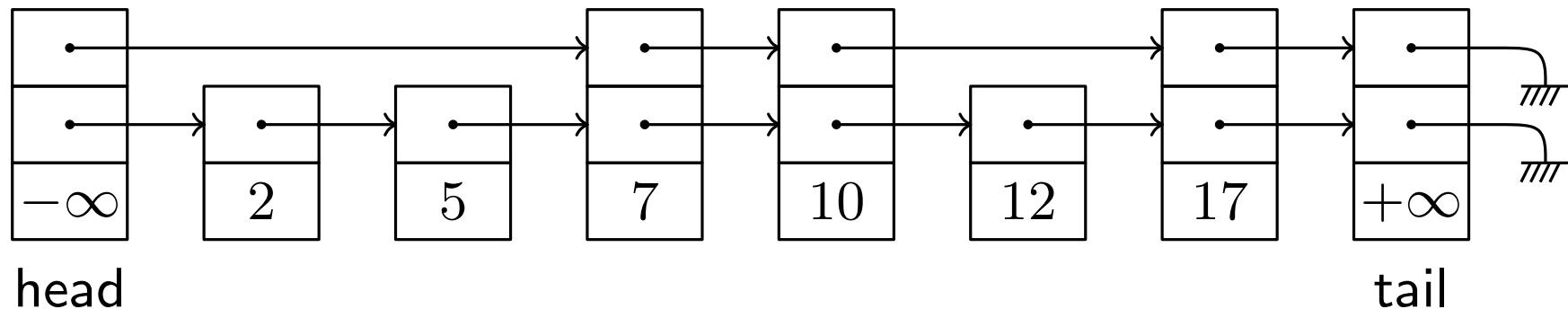
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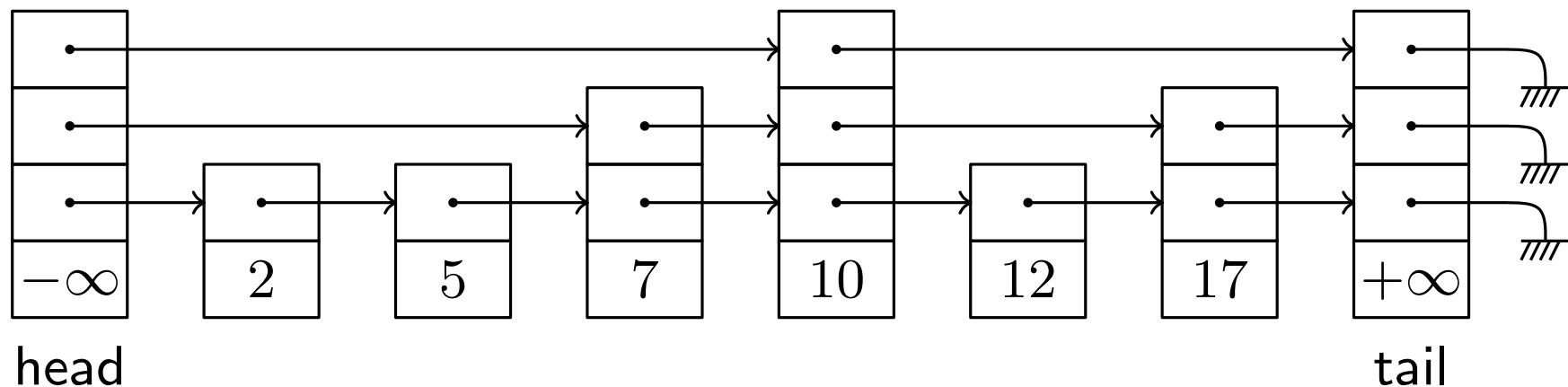
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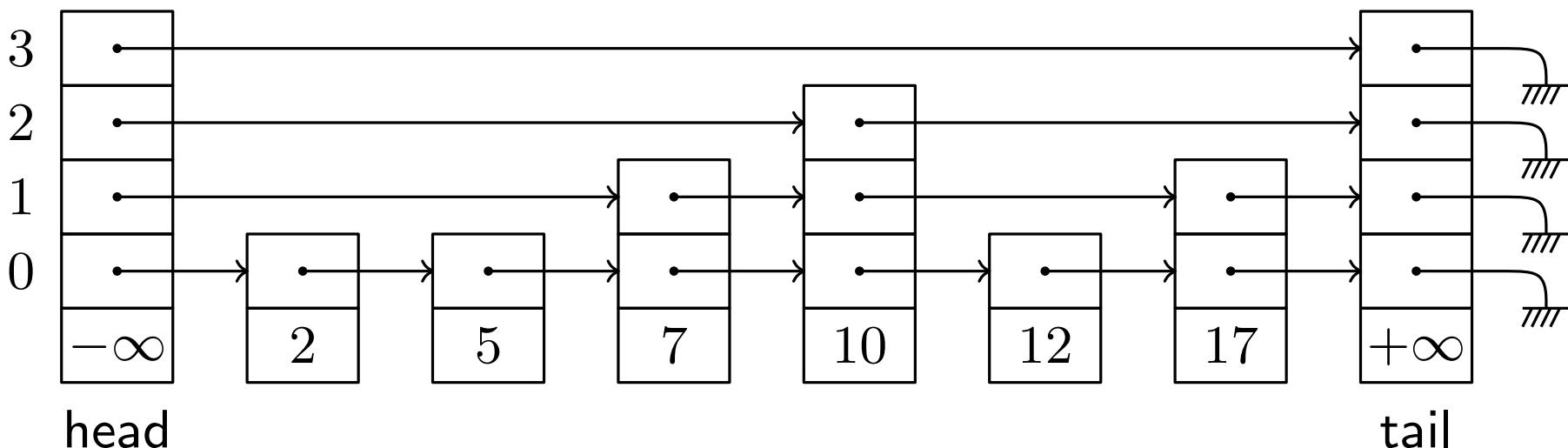
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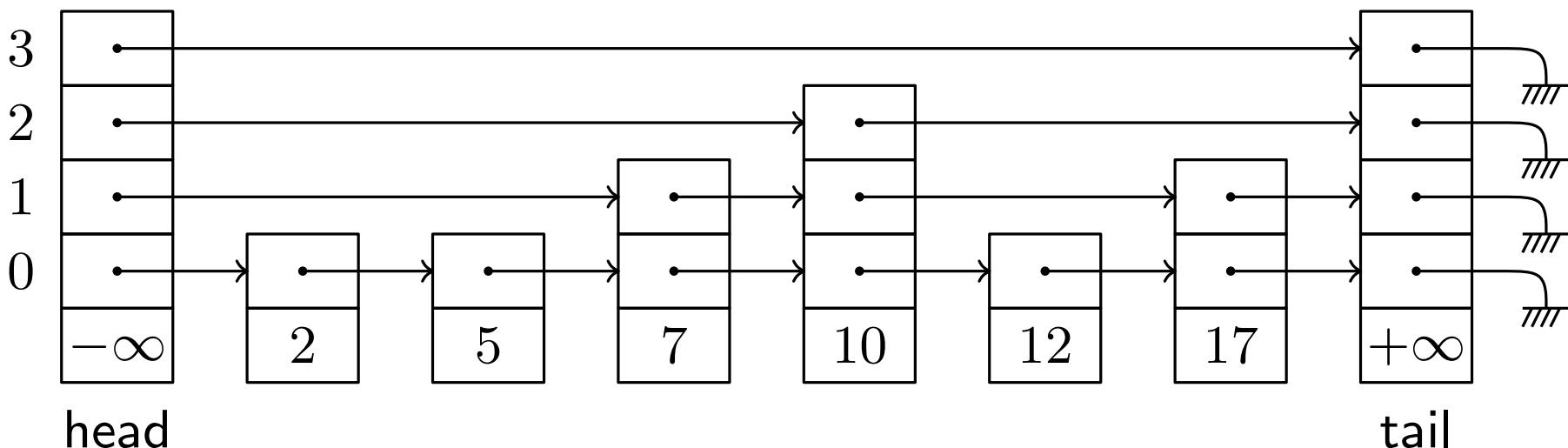
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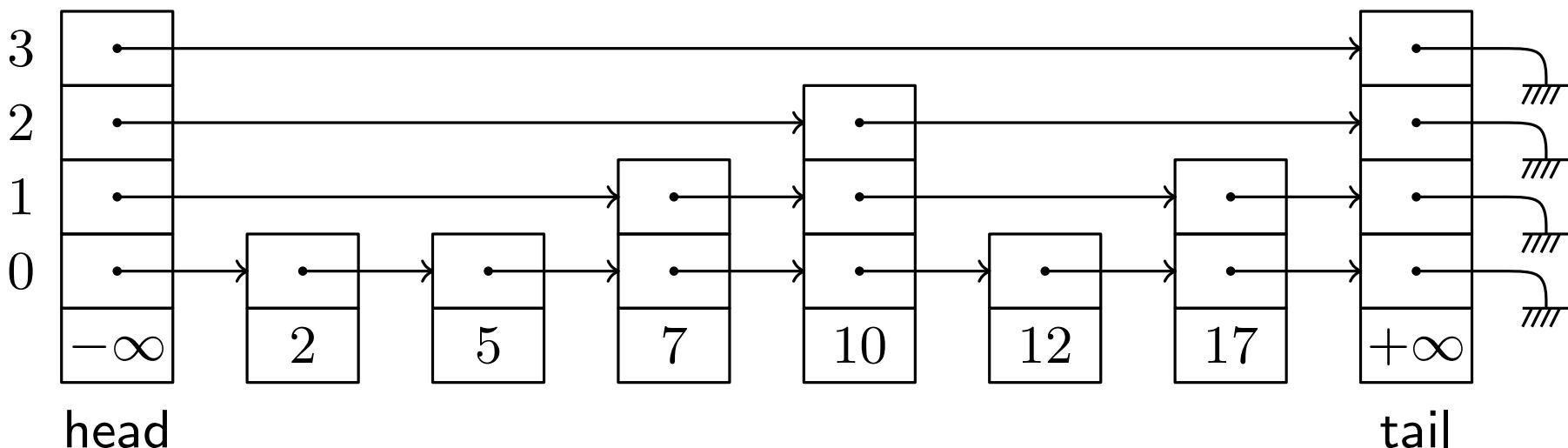
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    Node* head;          Value v;  
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Concurrent Lock-Coupling SkipLists

- ▶ Sorted list of elements
- ▶ Hierarchy of linked lists
- ▶ Efficiency comparable to balanced binary search trees

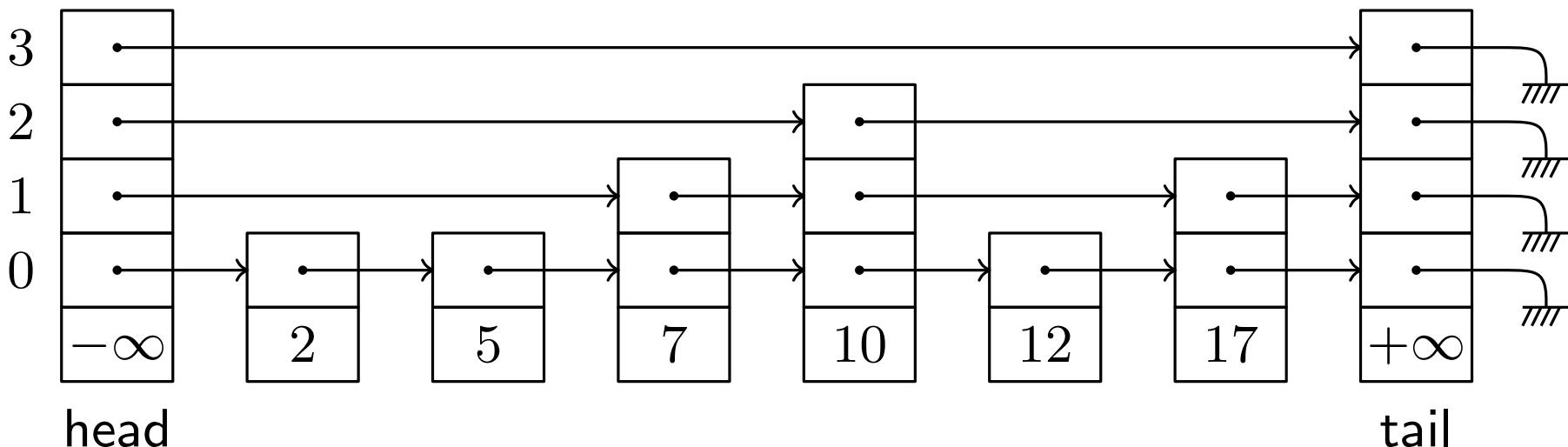
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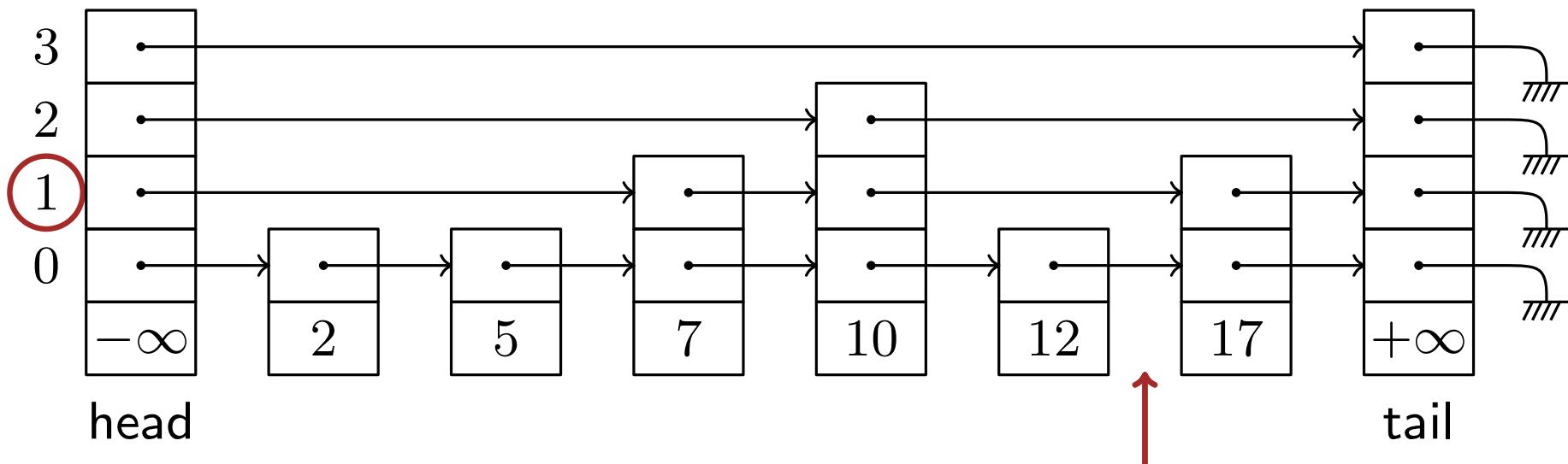
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insert(14) with height 1



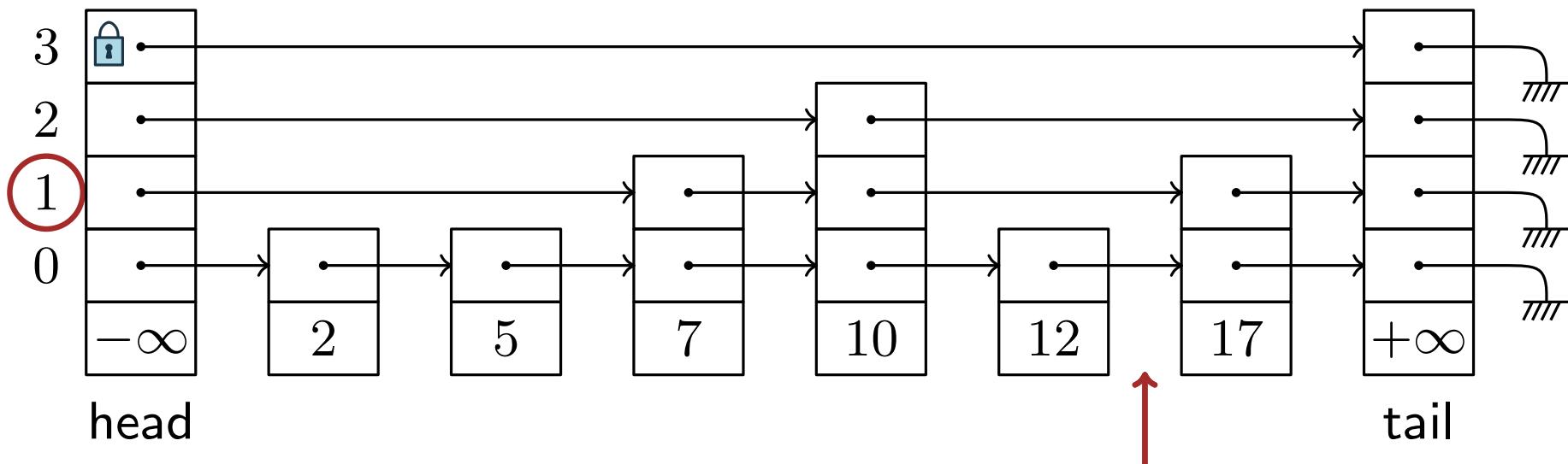
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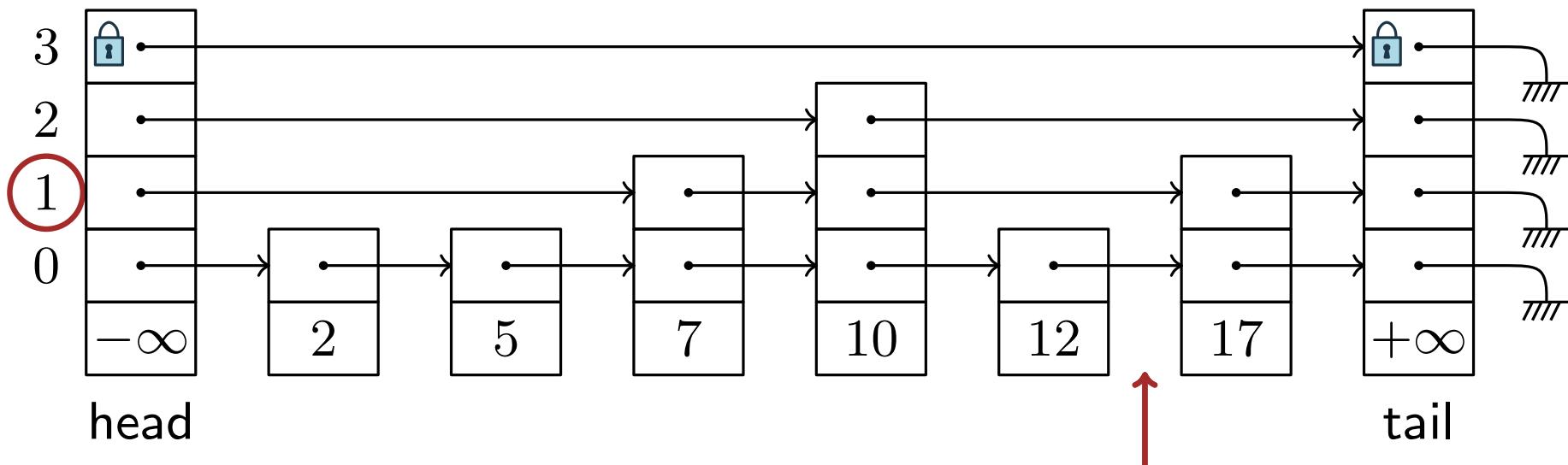
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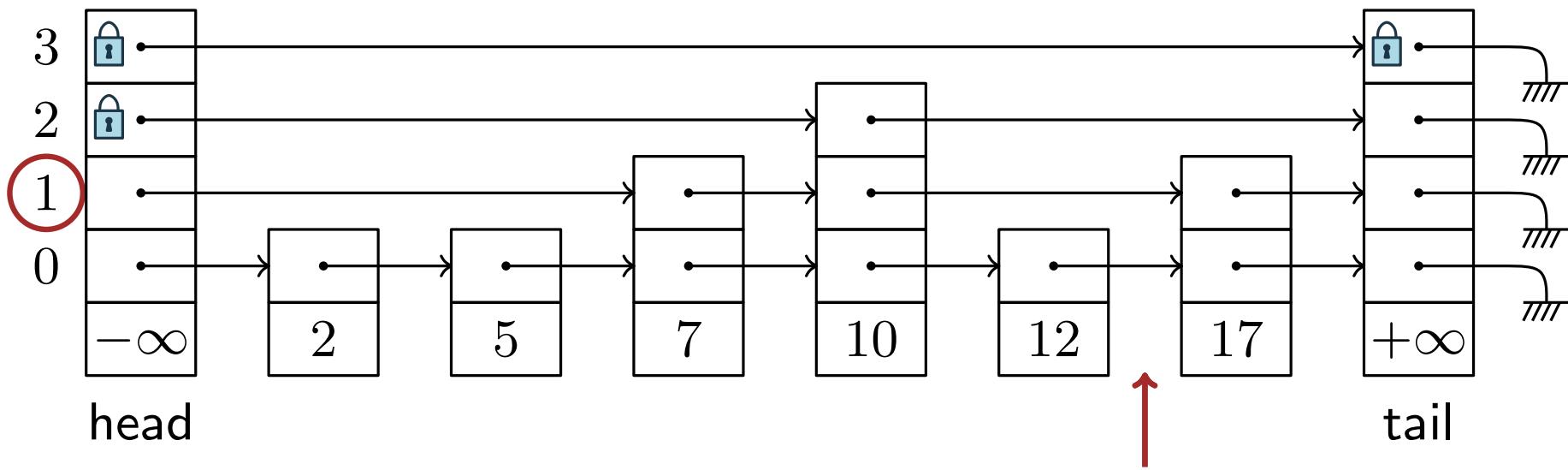
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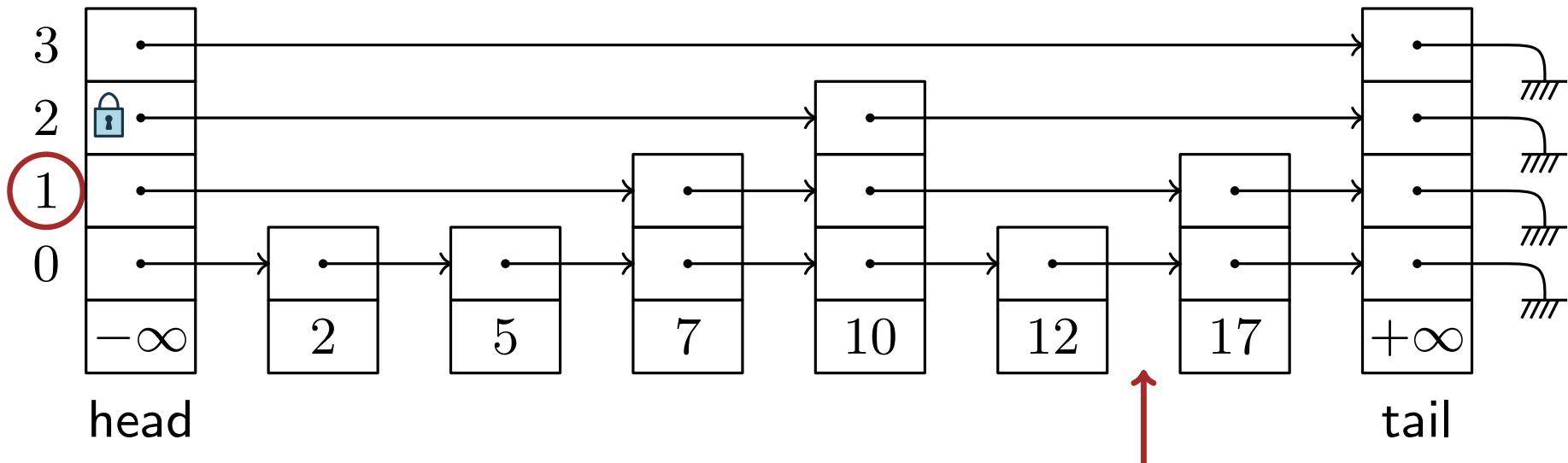
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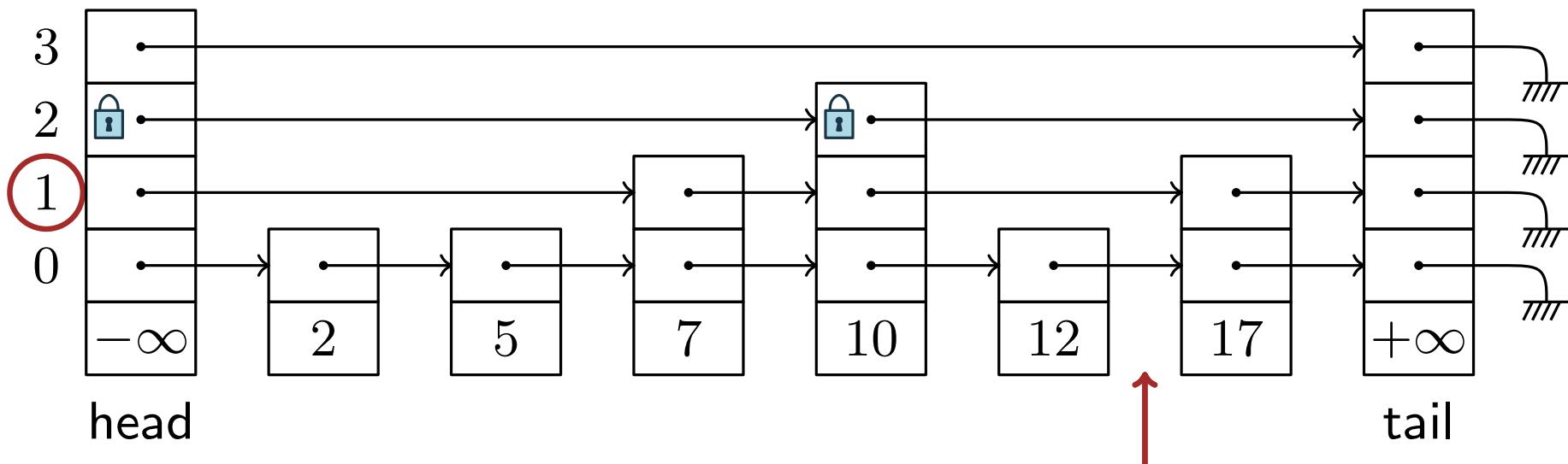
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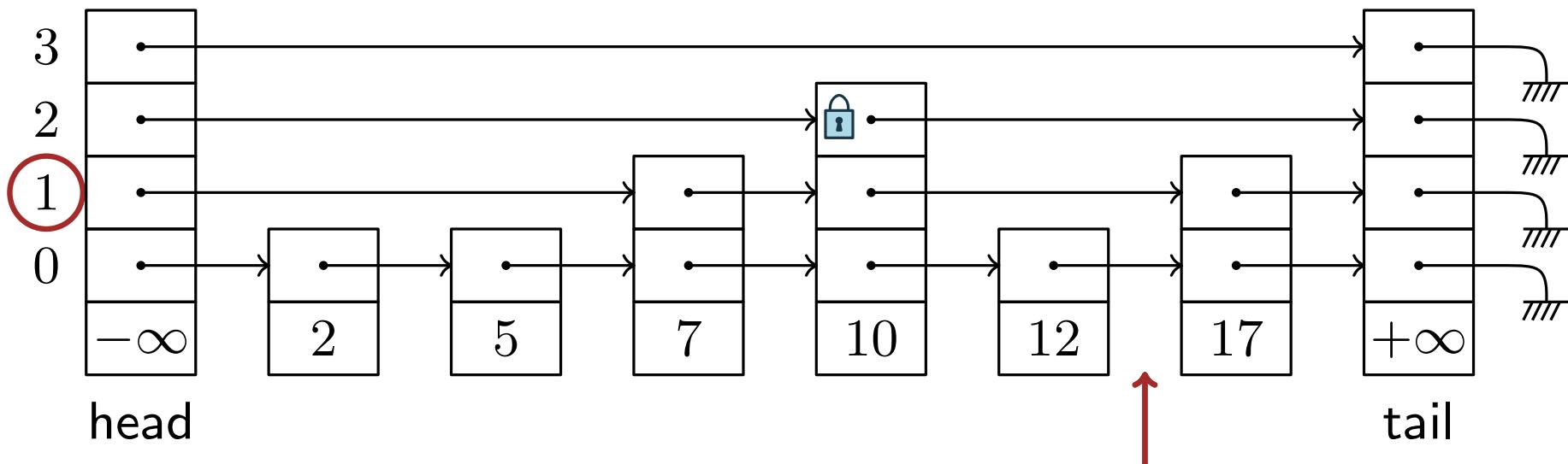
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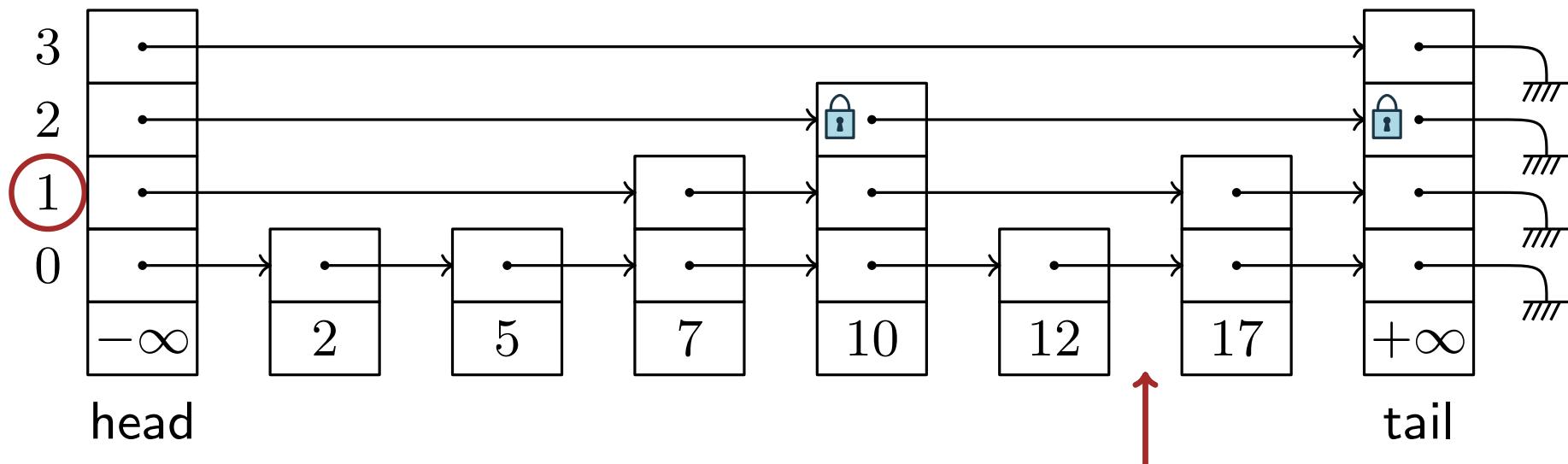
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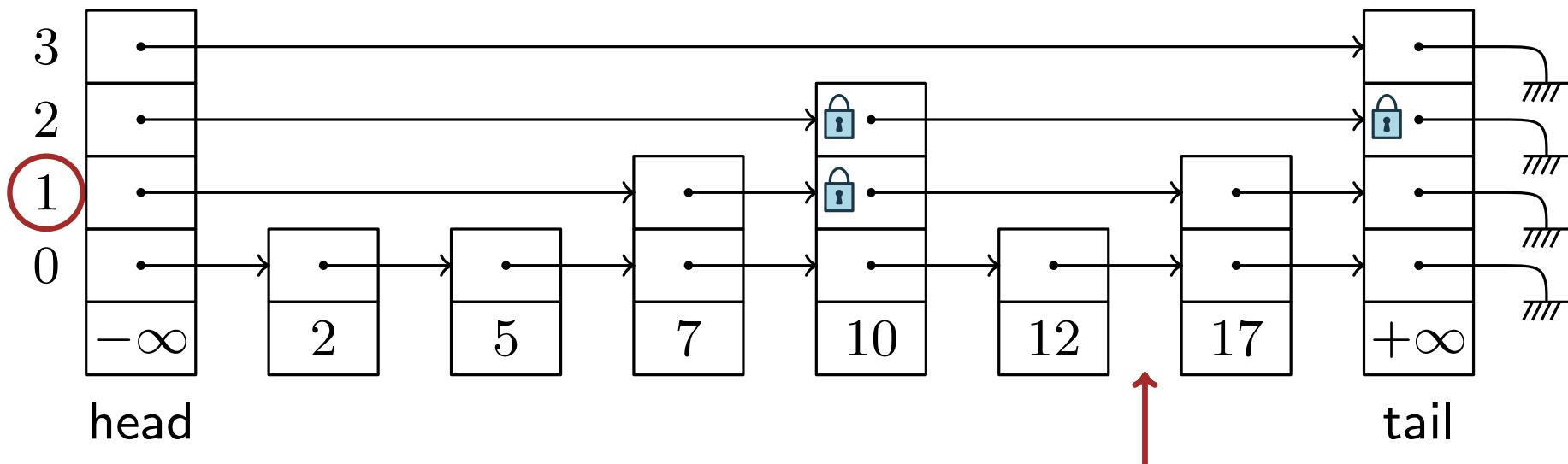
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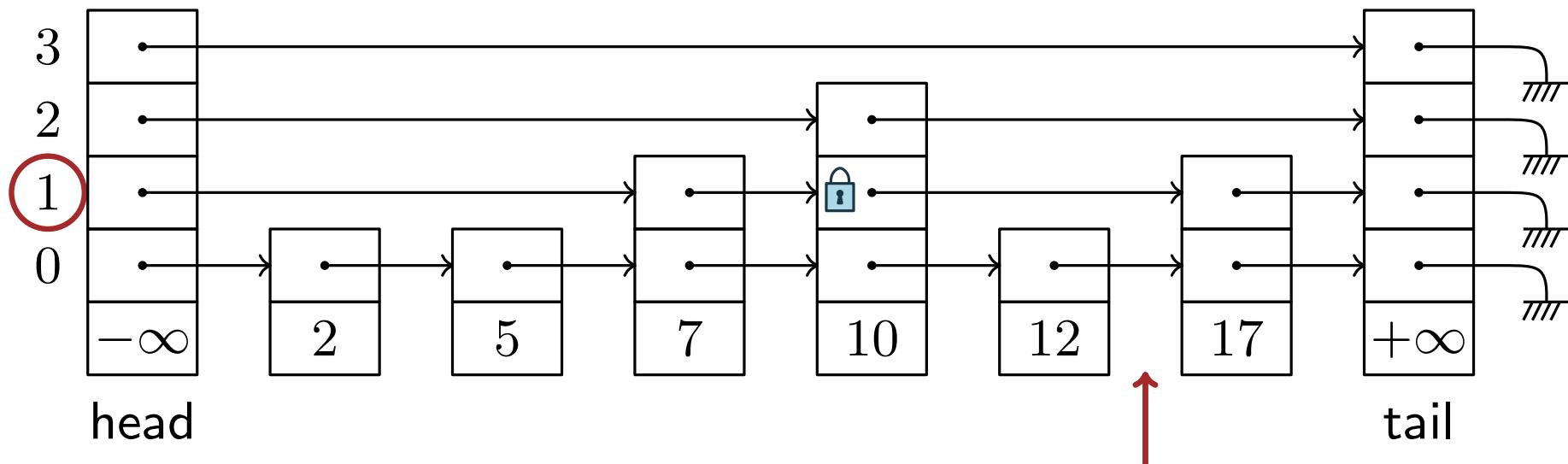
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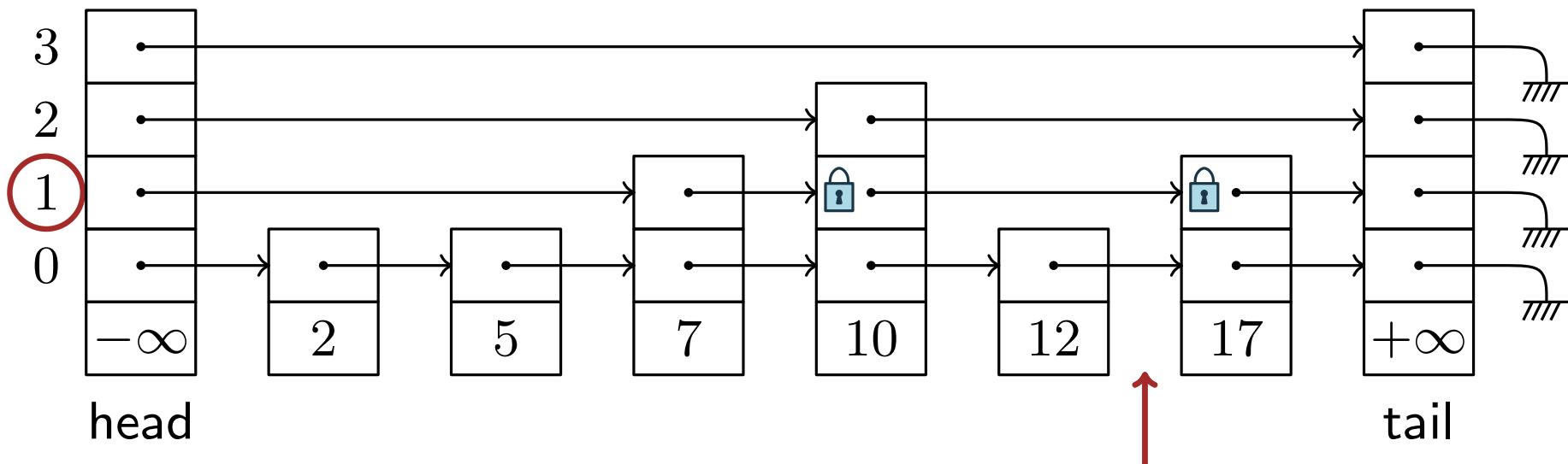
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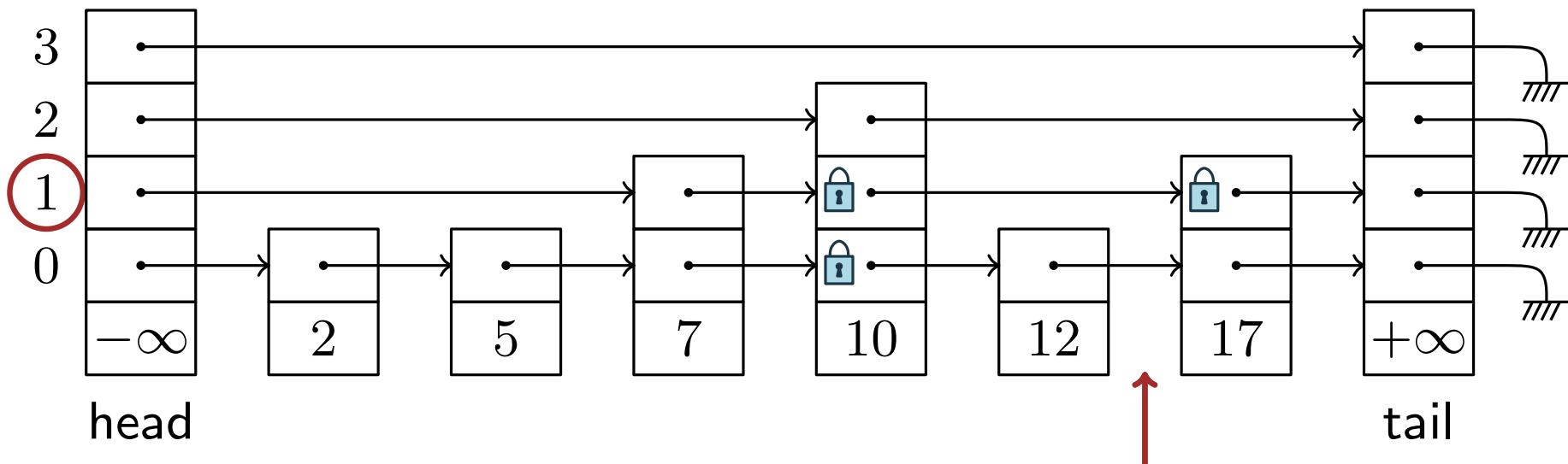
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insert(14) with height 1



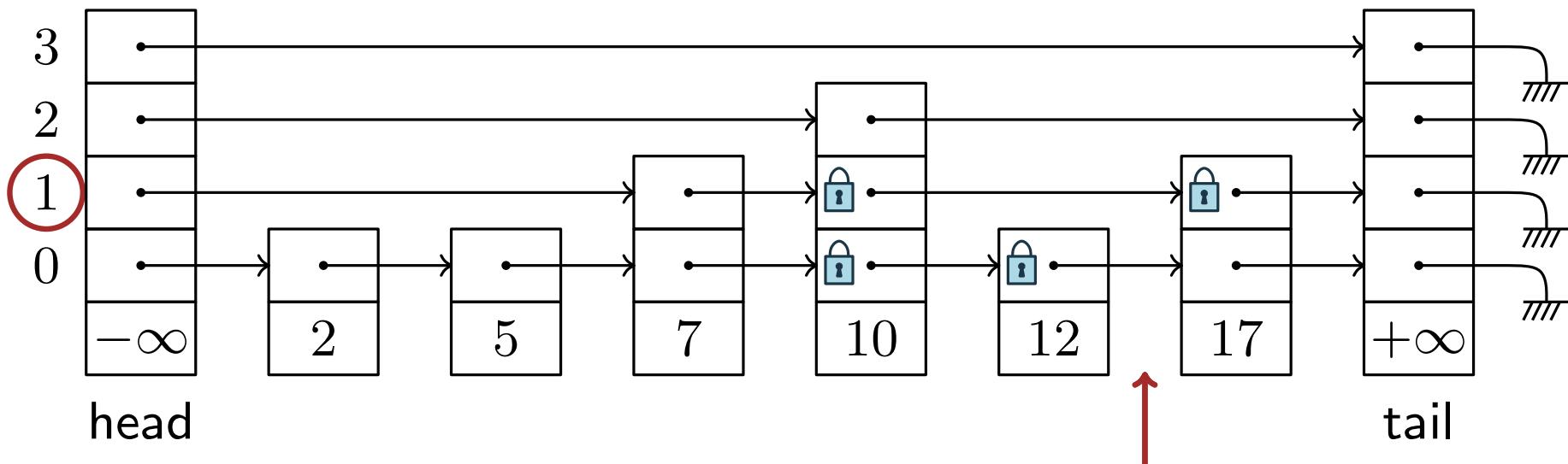
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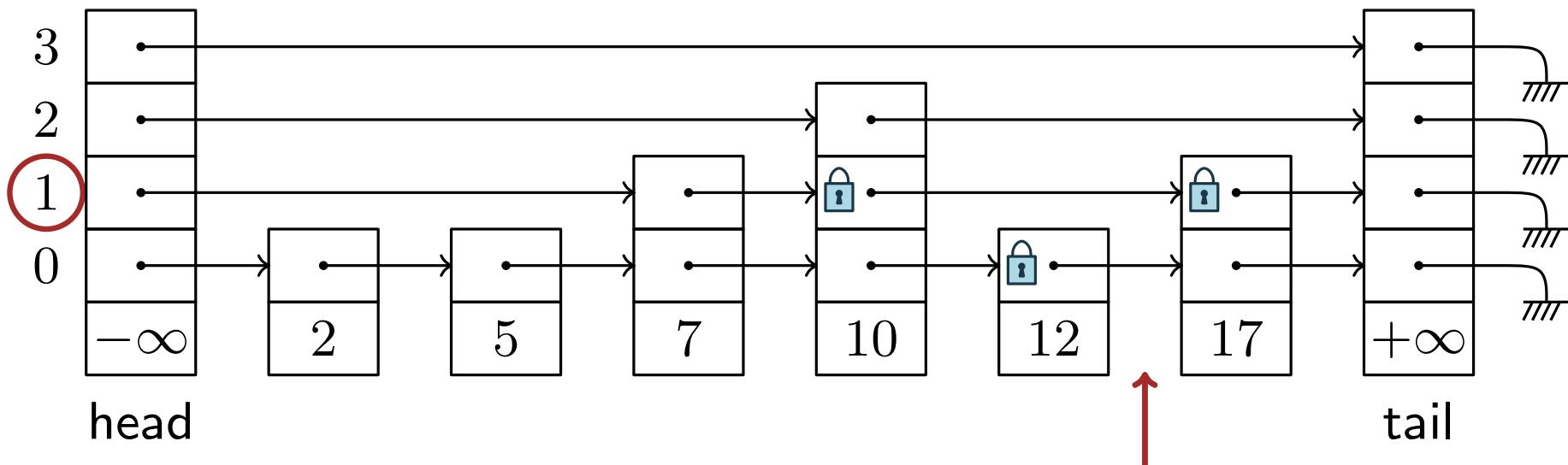
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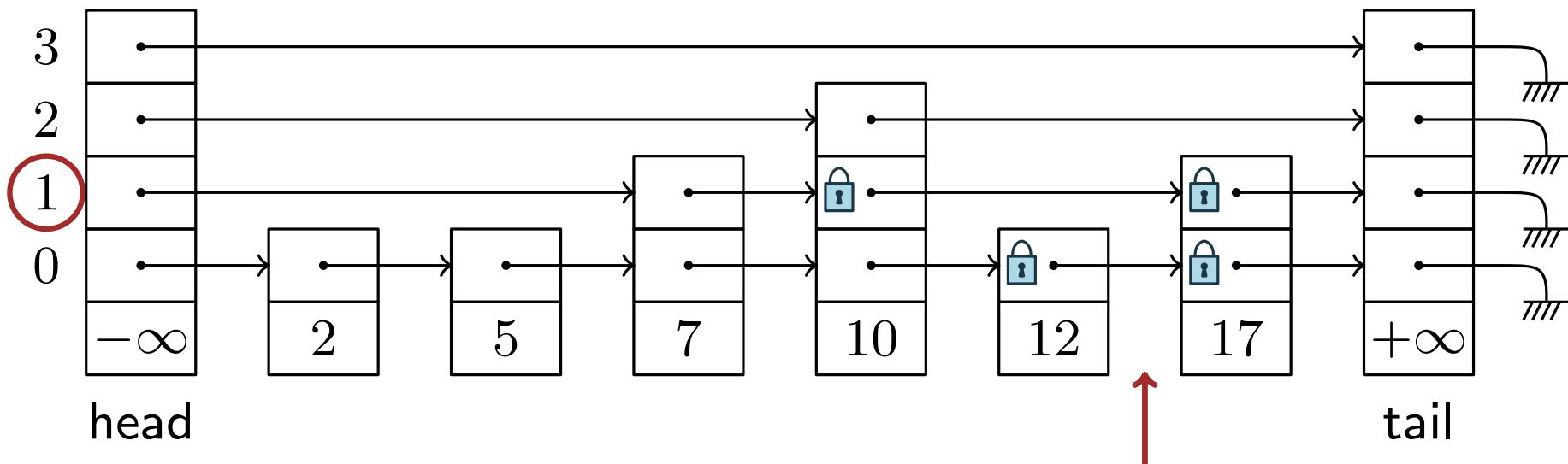
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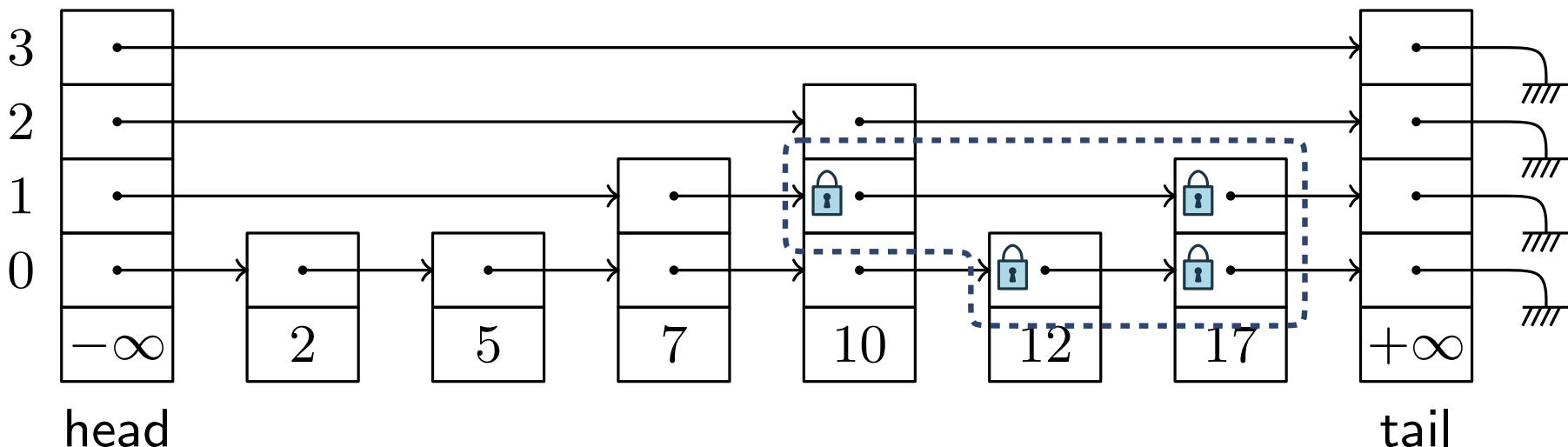
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masked regions = $2^{Node \times \mathbb{N}}$

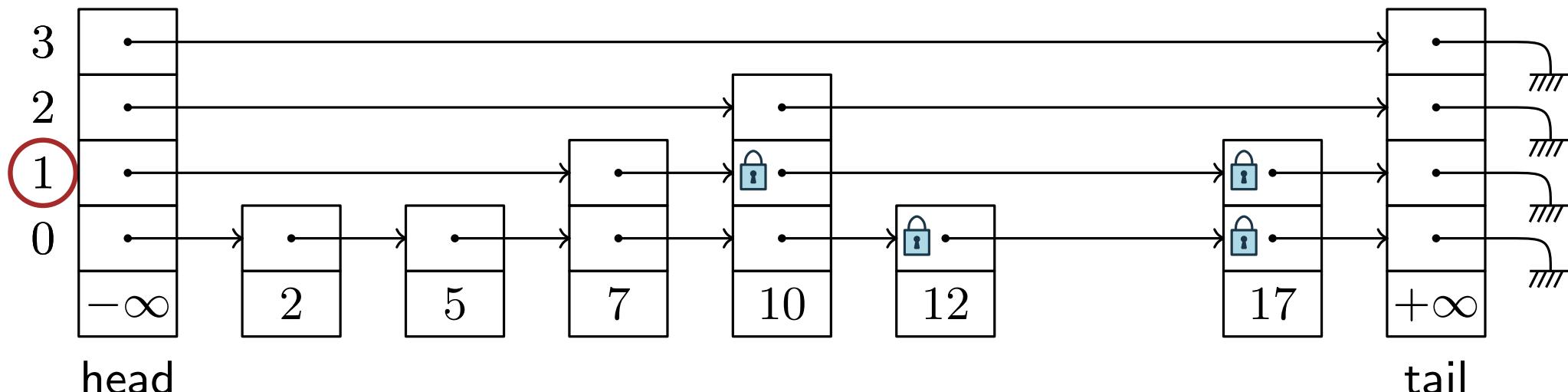
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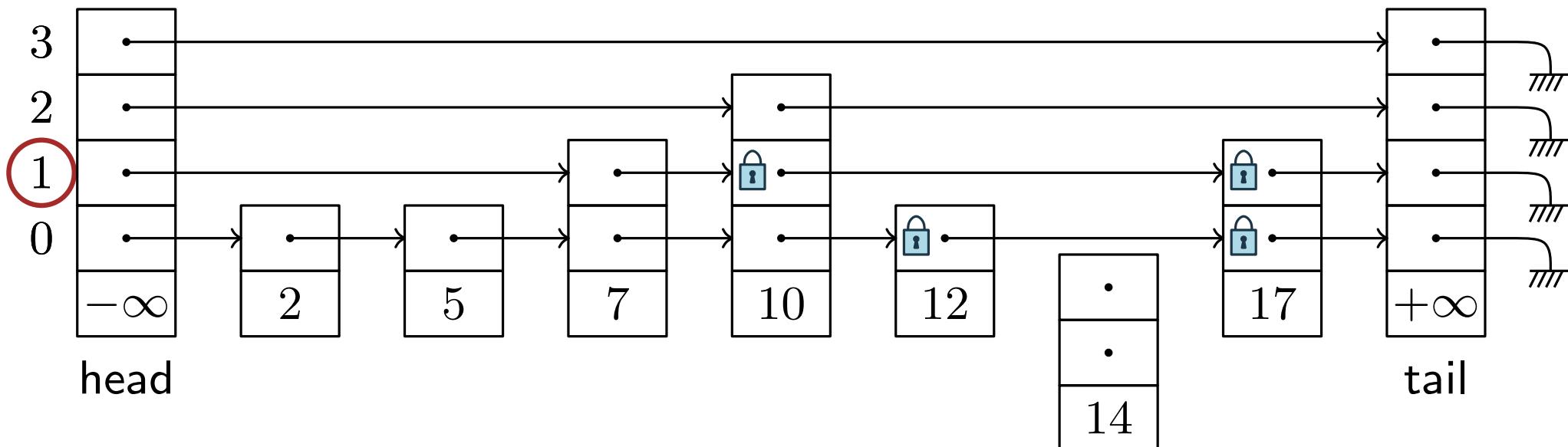
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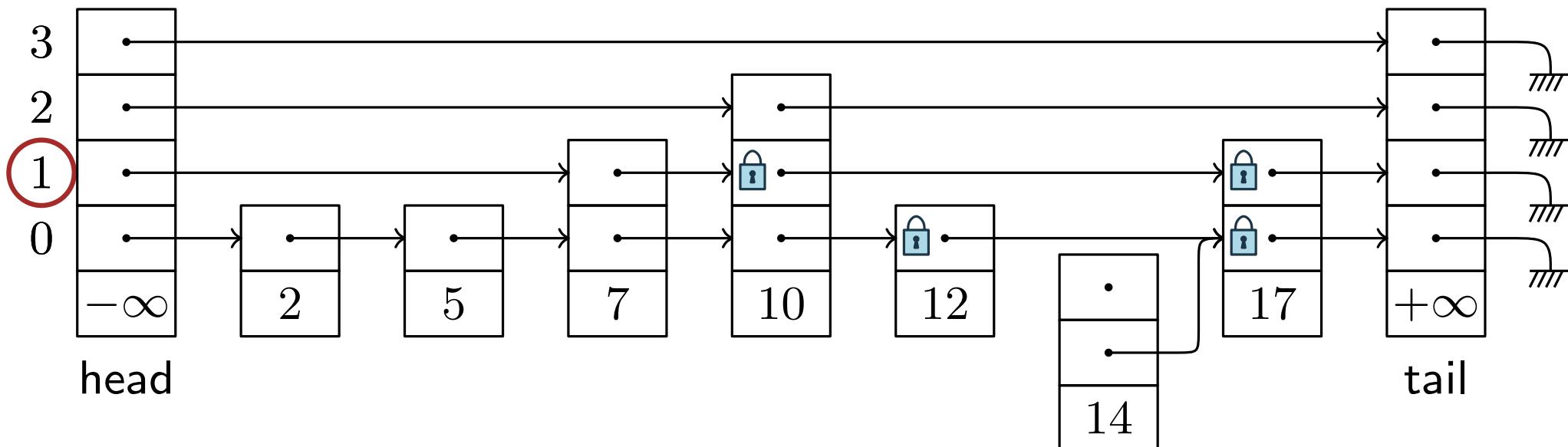
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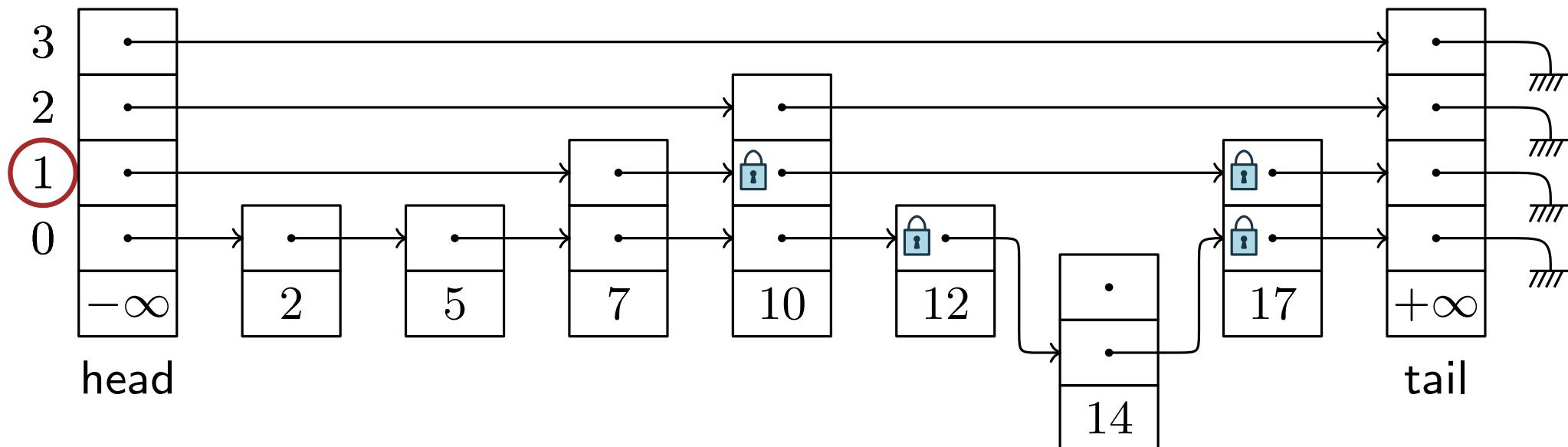
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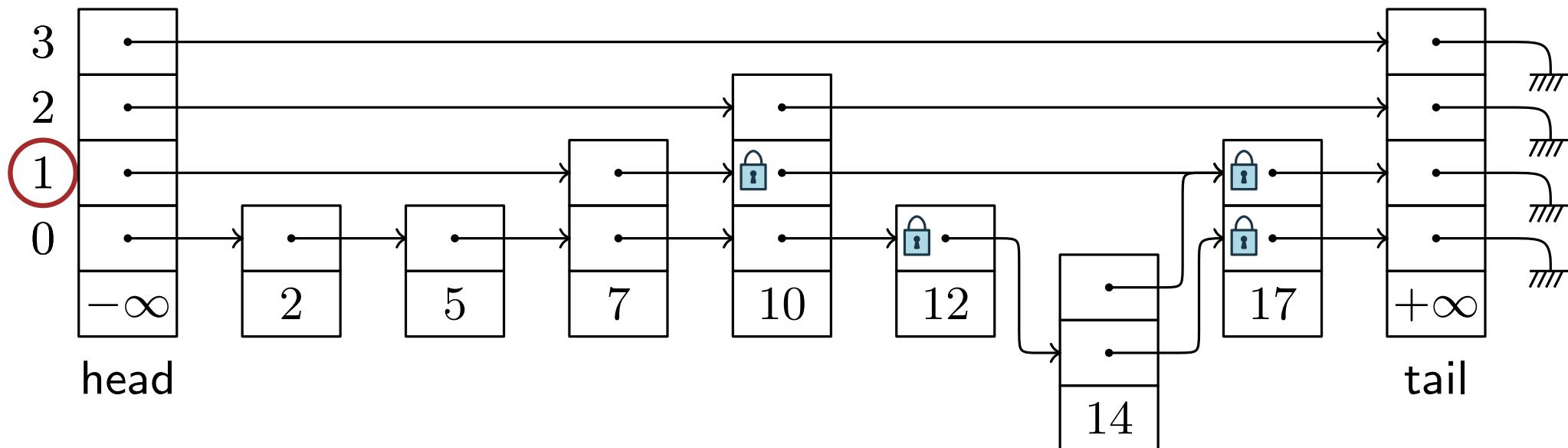
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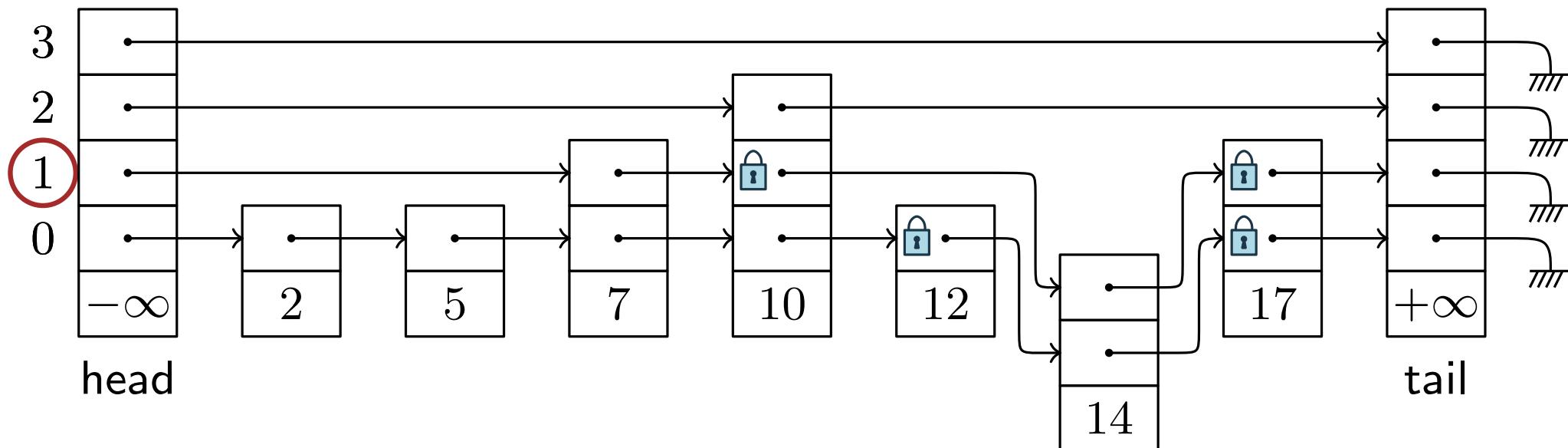
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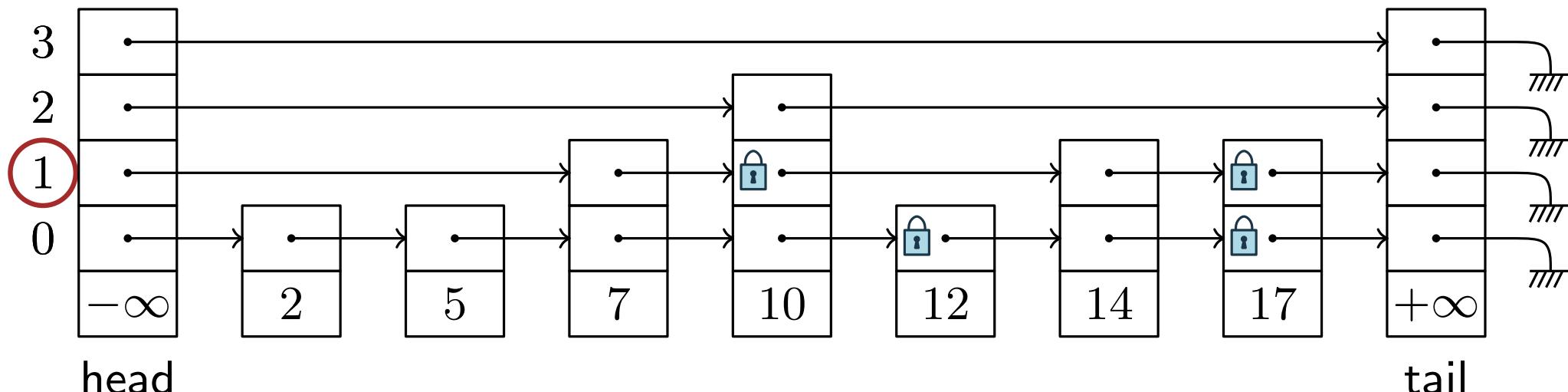
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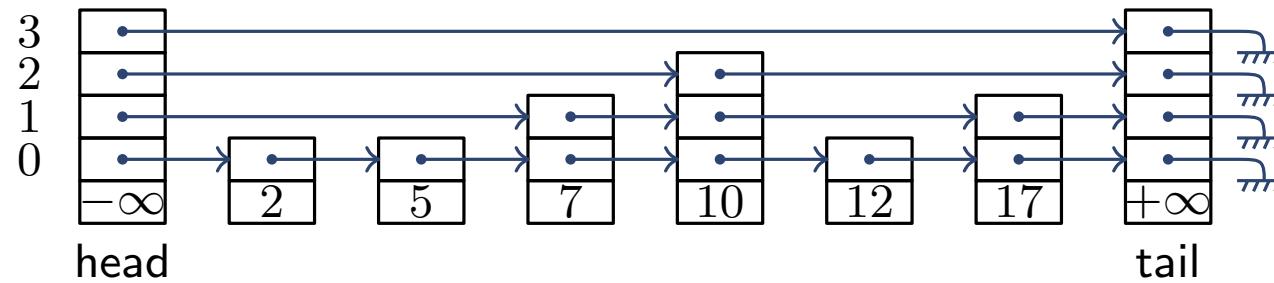
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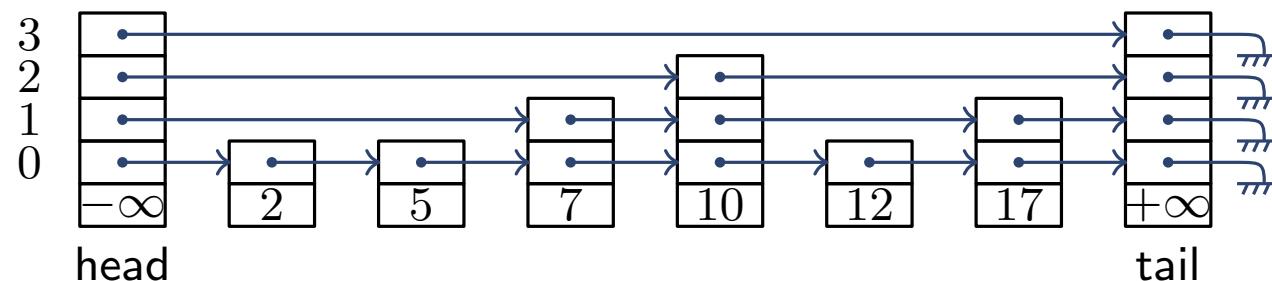
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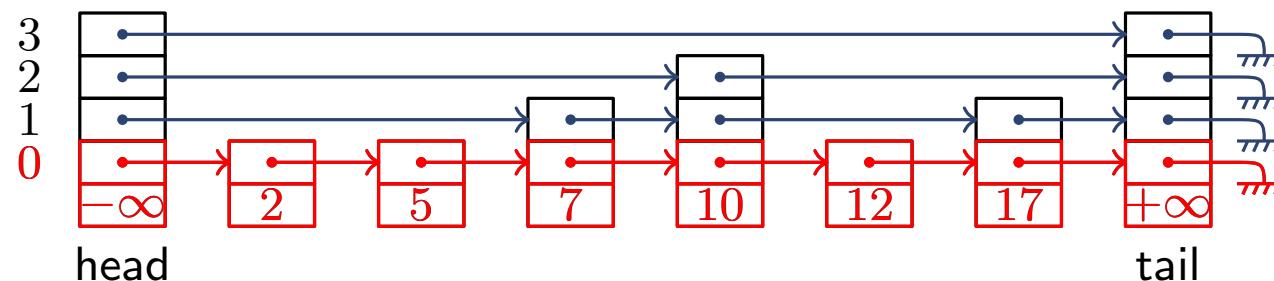
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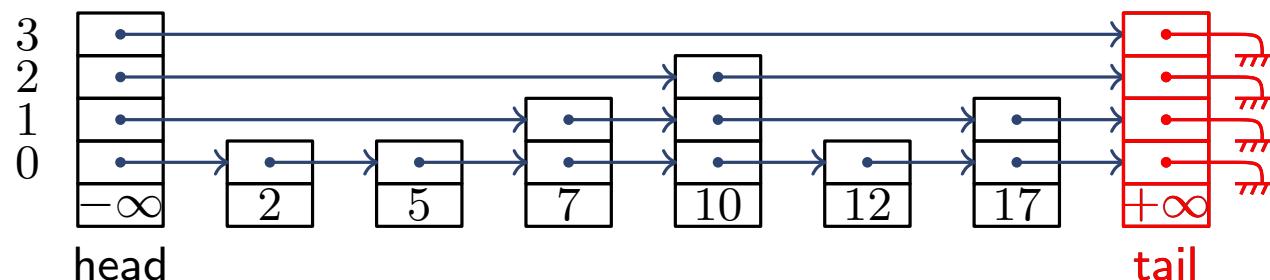
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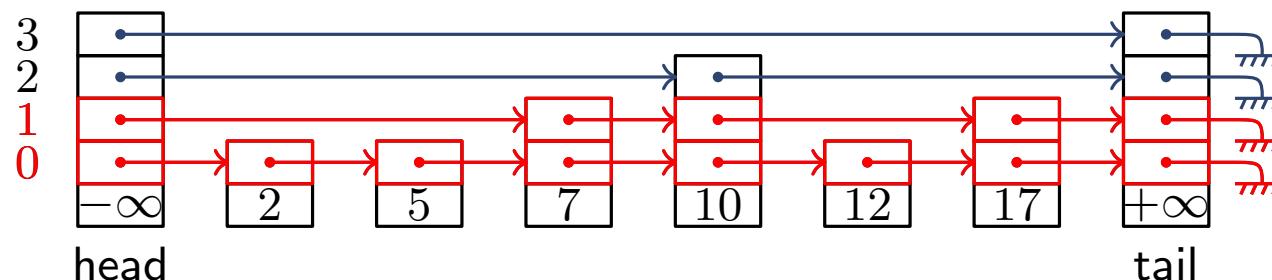
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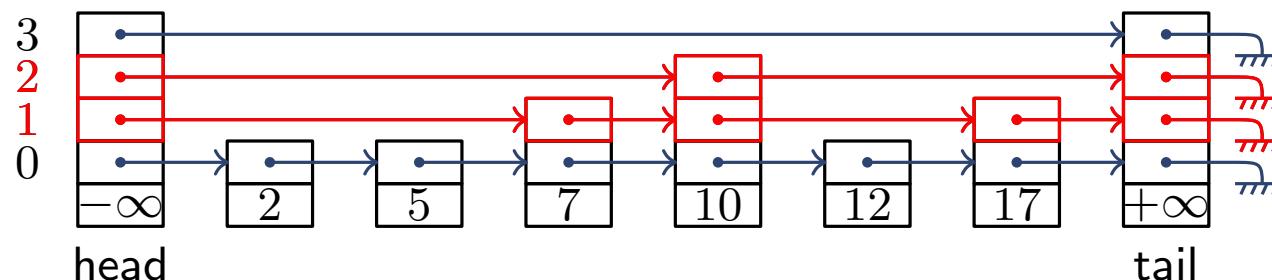
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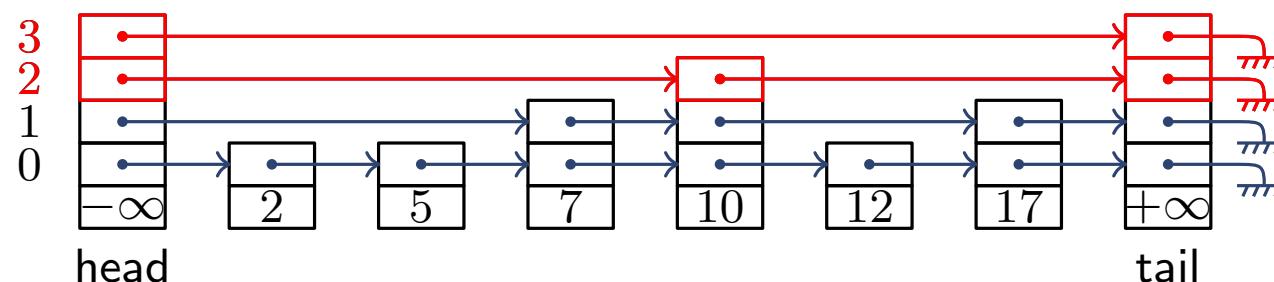
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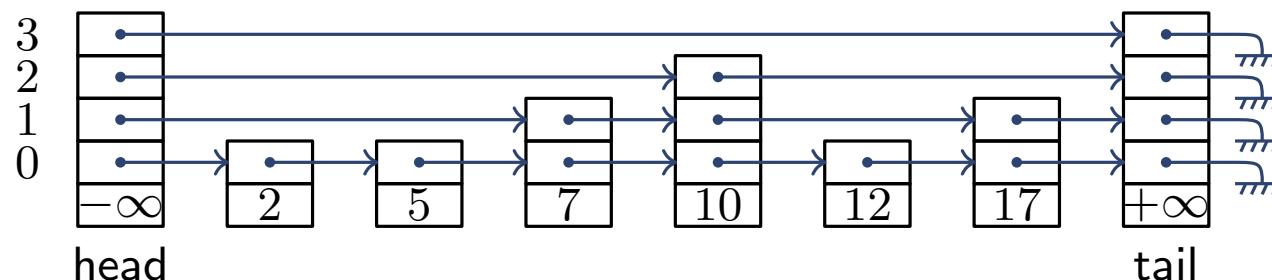


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- ▶ **Program transitions**



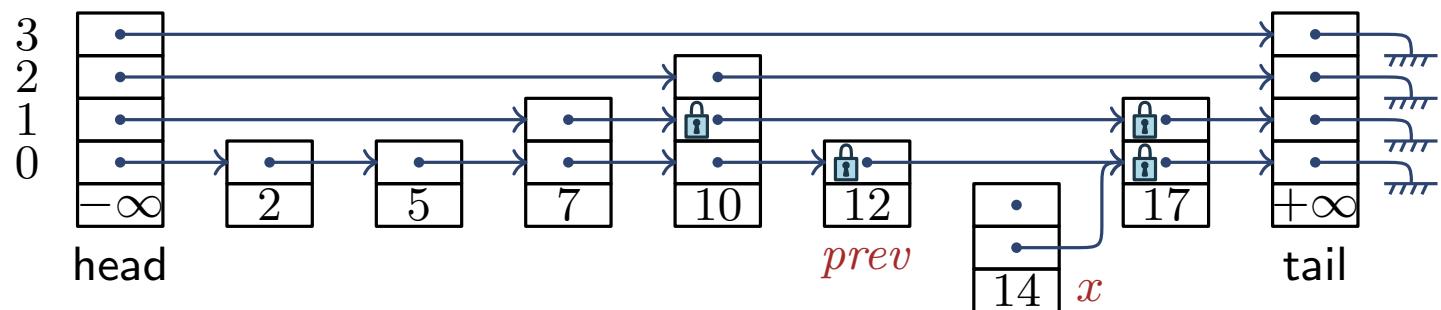
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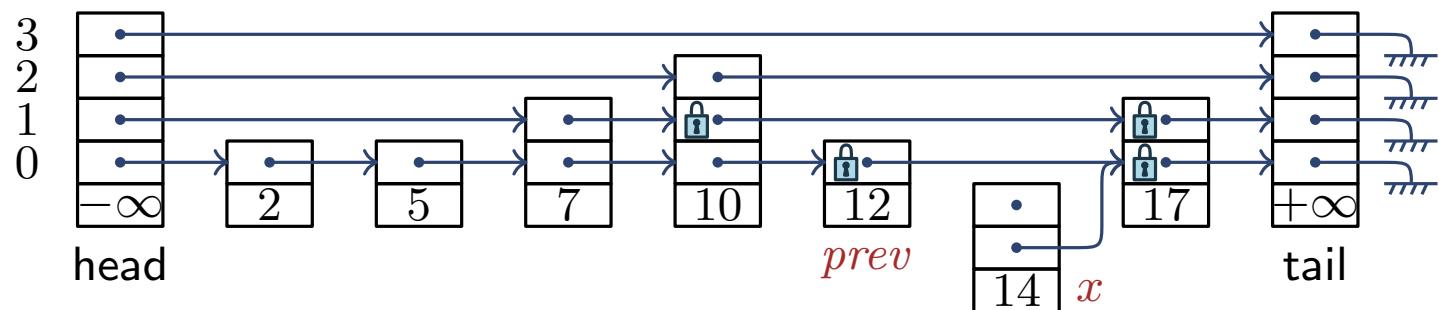
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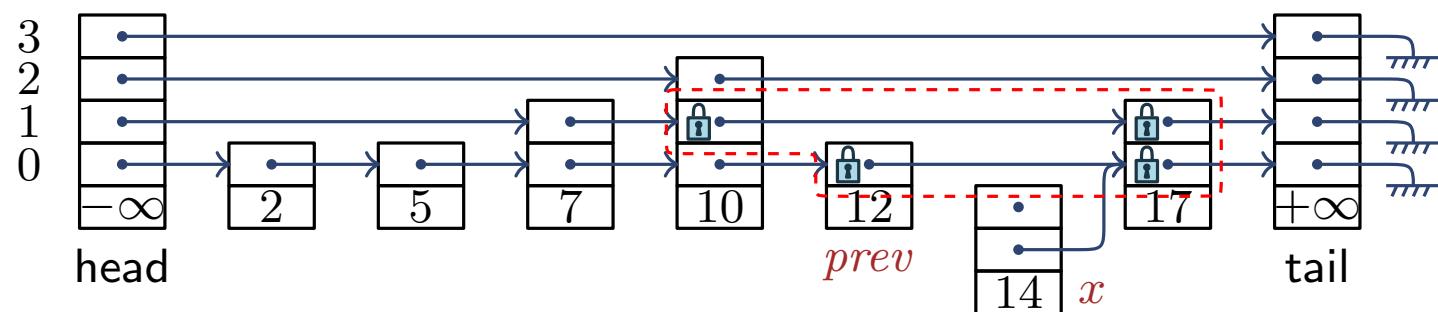
$\text{SkipList}_4(h, sl)$

$$\left(\begin{array}{l} x.key = 14 \wedge \\ prev.key < 14 \wedge \\ x.next[0].key > 14 \wedge \\ prev.next[0] = x.next[0] \wedge \\ (x, 0) \notin sl.r \wedge 0 \leq 0 \leq 3 \end{array} \right)$$

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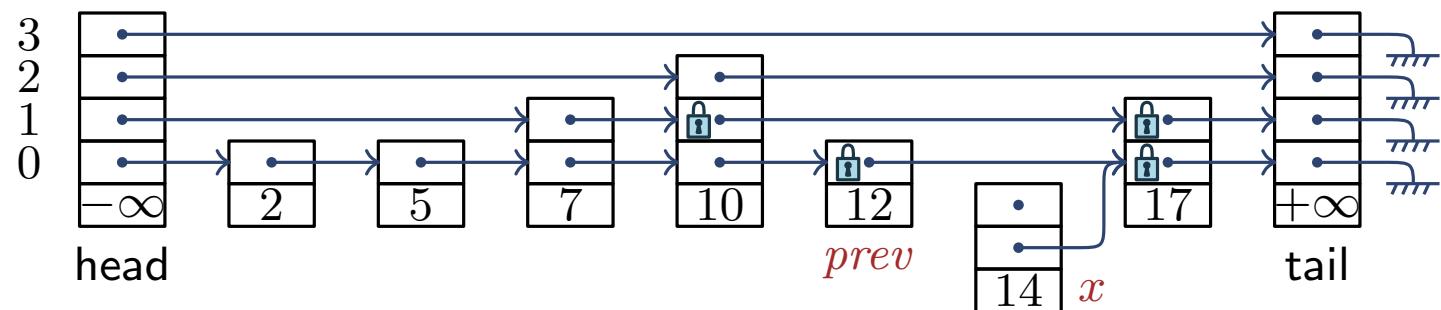
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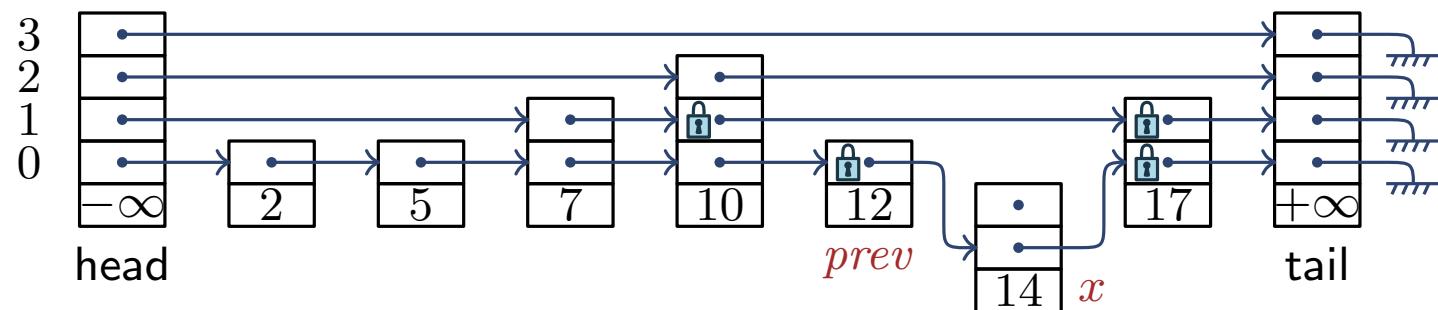
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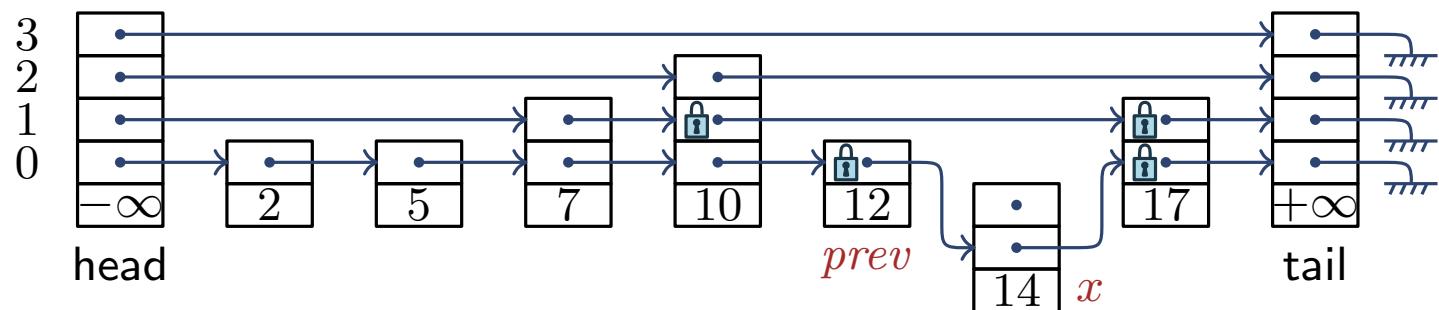
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Verification of Concurrent Skiplists

- ▶ **Skiplist shape preservation** : $\square \text{SkipList}_4(h, sl)$

$$\begin{aligned} \text{SkipList}_4(h, sl : \text{SkipList}) &\triangleq \text{OList}(h, sl, 0) \wedge \\ &\left(h[sl].tail.next[0] = \text{null} \wedge h[sl].tail.next[1] = \text{null} \right) \wedge \\ &\left(h[sl].tail.next[2] = \text{null} \wedge h[sl].tail.next[3] = \text{null} \right) \wedge \\ &\left(\text{SubList}(h, sl.head, sl.tail, 1, sl.head, sl.tail, 0) \wedge \right. \\ &\quad \text{SubList}(h, sl.head, sl.tail, 2, sl.head, sl.tail, 1) \wedge \\ &\quad \left. \text{SubList}(h, sl.head, sl.tail, 3, sl.head, sl.tail, 2) \right) \end{aligned}$$

- ▶ **Program transitions** : $SL_4(h, sl) \wedge \varphi_{aux} \wedge \rho_{36}^{[t]}(V, V') \rightarrow SL_4(h', sl')$

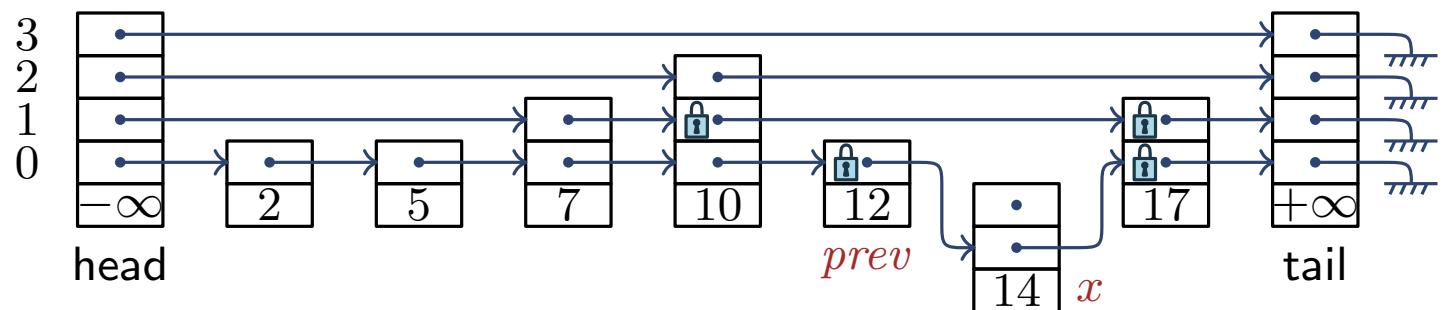
$\text{SkipList}_4(h, sl)$

$$\left(\begin{array}{lcl} x.key & = & 14 \\ prev.key & < & 14 \\ x.next[0].key & > & 14 \\ prev.next[0] & = & x.next[0] \\ (x, 0) \notin sl.r & \wedge & 0 \leq 0 \leq 3 \end{array} \right) \wedge \left(\begin{array}{lcl} at_{36}[t] & & \\ prev'.next[0] = x & \wedge & \\ at'_{37}[t] & & \\ h' = h \wedge sl = sl' & \wedge & \\ x' = x & \dots & \end{array} \right) \rightarrow \text{SkipList}_4(h', sl')$$

35: . . .

36: $prev.next[0] := x$

37: . . .



Verification of Concurrent Skiplists

- **Skiplist shape preservation** : $\square \text{SkipList}_4(h, sl)$

$$\begin{aligned} \text{SkipList}_4(h, sl : \text{SkipList}) &\triangleq \text{OList}(h, sl, 0) \wedge \\ &\left(h[sl].tail.next[0] = \text{null} \wedge h[sl].tail.next[1] = \text{null} \right) \wedge \\ &\left(h[sl].tail.next[2] = \text{null} \wedge h[sl].tail.next[3] = \text{null} \right) \wedge \\ &\left(\begin{array}{l} \text{SubList}(h, sl.head, sl.tail, 1, sl.head, sl.tail, 0) \wedge \\ \text{SubList}(h, sl.head, sl.tail, 2, sl.head, sl.tail, 1) \wedge \\ \text{SubList}(h, sl.head, sl.tail, 3, sl.head, sl.tail, 2) \end{array} \right) \end{aligned}$$

- **Program transitions** : $SL_4(h, sl) \wedge \varphi_{aux} \wedge \rho_{36}^{[t]}(V, V') \rightarrow SL_4(h', sl')$

$\text{SkipList}_4(h, sl)$

$$\left(\begin{array}{l} x.key = 14 \wedge \\ prev.key < 14 \wedge \\ x.next[0].key > 14 \wedge \\ prev.next[0] = x.next[0] \wedge \\ (x, 0) \notin sl.r \wedge 0 \leq 0 \leq 3 \end{array} \right) \wedge \left(\begin{array}{l} at_{36}[t] \wedge \\ prev'.next[0] = x \wedge \\ at'_{37}[t] \wedge \\ h' = h \wedge sl = sl' \wedge \\ x' = x \dots \end{array} \right) \rightarrow \text{SkipList}_4(h', sl')$$

reason about
locks

Verification of Concurrent Skiplists

- ▶ **Skiplist shape preservation** : $\square \text{SkipList}_4(h, sl)$

$$\begin{aligned} \text{SkipList}_4(h, sl : \text{SkipList}) &\triangleq \text{OList}(h, sl, 0) \wedge \\ &\left(h[sl].tail.next[0] = \text{null} \wedge h[sl].tail.next[1] = \text{null} \right) \wedge \\ &\left(h[sl].tail.next[2] = \text{null} \wedge h[sl].tail.next[3] = \text{null} \right) \wedge \\ &\left(\text{SubList}(h, sl.head, sl.tail, 1, sl.head, sl.tail, 0) \wedge \right. \\ &\quad \text{SubList}(h, sl.head, sl.tail, 2, sl.head, sl.tail, 1) \wedge \\ &\quad \left. \text{SubList}(h, sl.head, sl.tail, 3, sl.head, sl.tail, 2) \right) \end{aligned}$$

- ▶ **Program transitions** : $SL_4(h, sl) \wedge \varphi_{aux} \wedge \rho_{36}^{[t]}(V, V') \rightarrow SL_4(h', sl')$

$$\begin{aligned} \text{SkipList}_4(h, sl) &\\ \left(\begin{array}{lcl} x.key &=& 14 \\ prev.key &<& 14 \\ x.next[0].key &>& 14 \\ prev.next[0] &=& x.next[0] \\ (x, 0) \notin sl.r \wedge 0 \leq 0 \leq 3 \end{array} \wedge \right) \wedge & \left(\begin{array}{lcl} at_{36}[t] \\ prev'.next[0] = x \\ at'_{37}[t] \\ h' = h \wedge sl = sl' \\ x' = x \end{array} \wedge \dots \right) \rightarrow \text{SkipList}_4(h', sl') \end{aligned}$$

**reason about
thread identifiers**

Verification of Concurrent Skiplists

- ▶ **Skiplist shape preservation** : $\square \text{SkipList}_4(h, sl)$

$$\begin{aligned} \text{SkipList}_4(h, sl : \text{SkipList}) &\triangleq \text{OList}(h, sl, 0) \wedge \\ &\left(h[sl].tail.next[0] = \text{null} \wedge h[sl].tail.next[1] = \text{null} \right) \wedge \\ &\left(h[sl].tail.next[2] = \text{null} \wedge h[sl].tail.next[3] = \text{null} \right) \wedge \\ &\left(\text{SubList}(h, sl.head, sl.tail, 1, sl.head, sl.tail, 0) \wedge \right. \\ &\quad \text{SubList}(h, sl.head, sl.tail, 2, sl.head, sl.tail, 1) \wedge \\ &\quad \left. \text{SubList}(h, sl.head, sl.tail, 3, sl.head, sl.tail, 2) \right) \end{aligned}$$

- ▶ **Program transitions** : $SL_4(h, sl) \wedge \varphi_{aux} \wedge \rho_{36}^{[t]}(V, V') \rightarrow SL_4(h', sl')$

$$\begin{aligned} \text{SkipList}_4(h, sl) &\\ \left(\begin{array}{lcl} x.key &=& 14 \\ prev.key &<& 14 \\ x.next[0].key &>& 14 \\ prev.next[0] &=& x.next[0] \\ (x, 0) \notin sl.r \wedge 0 \leq 0 \leq 3 \end{array} \wedge \right) &\wedge \left(\begin{array}{lcl} at_{36}[t] \\ prev'.next[0] = x \\ at'_{37}[t] \\ h' = h \wedge sl = sl' \\ x' = x \\ \dots \end{array} \wedge \right) \rightarrow \text{SkipList}_4(h', sl') \end{aligned}$$

**reason about
program positions**

Verification of Concurrent Skiplists

- ▶ Skiplist shape preservation : $\square \text{SkipList}_4(h, sl)$

$$\begin{aligned} \text{SkipList}_4(h, sl : \text{SkipList}) &\hat{=} \text{OList}(h, sl, 0) \quad \wedge \\ &\left(h[sl].tail.next[0] = \text{null} \wedge h[sl].tail.next[1] = \text{null} \right) \quad \wedge \\ &\left(h[sl].tail.next[2] = \text{null} \wedge h[sl].tail.next[3] = \text{null} \right) \quad \wedge \\ &\left(\begin{array}{l} \text{SubList}(h, sl.head, sl.tail, 1, sl.head, sl.tail, 0) \wedge \\ \text{SubList}(h, sl.head, sl.tail, 2, sl.head, sl.tail, 1) \wedge \\ \text{SubList}(h, sl.head, sl.tail, 3, sl.head, sl.tail, 2) \end{array} \right) \end{aligned}$$

- ▶ Program transitions : $SL_4(h, sl) \wedge \varphi_{aux} \wedge \rho_{36}^{[t]}(V, V') \rightarrow SL_4(h', sl')$

$\text{SkipList}_4(h, sl)$

$$\left(\begin{array}{ll} x.key = 14 & \wedge \\ \text{prev.key} < 14 & \wedge \\ x.next[0].key > 14 & \wedge \\ \text{prev.next}[0] = x.next[0] & \wedge \\ (x, 0) \notin sl.r \wedge 0 \leq 0 \leq 3 & \end{array} \right) \wedge \left(\begin{array}{ll} at_{36}[t] & \wedge \\ \text{prev'.next}[0] = x & \wedge \\ at'_{37}[t] & \wedge \\ h' = h \wedge sl = sl' & \wedge \\ x' = x & \dots \end{array} \right) \rightarrow \text{SkipList}_4(h', sl')$$

reason about

ordered values + notion of ordered list

Verification of Concurrent Skiplists

- ▶ **Skiplist shape preservation** : $\square \text{SkipList}_4(h, sl)$

$$\begin{aligned} \text{SkipList}_4(h, sl : \text{SkipList}) &\stackrel{\hat{=}}{=} \text{OList}(h, sl, 0) & \wedge \\ &\left(h[sl].tail.next[0] = \text{null} \wedge h[sl].tail.next[1] = \text{null} \right) & \wedge \\ &\left(h[sl].tail.next[2] = \text{null} \wedge h[sl].tail.next[3] = \text{null} \right) & \wedge \\ &\left(\text{SubList}(h, sl.head, sl.tail, 1, sl.head, sl.tail, 0) \wedge \right. \\ &\quad \text{SubList}(h, sl.head, sl.tail, 2, sl.head, sl.tail, 1) \wedge \\ &\quad \left. \text{SubList}(h, sl.head, sl.tail, 3, sl.head, sl.tail, 2) \right) \end{aligned}$$

- ▶ **Program transitions** : $SL_4(h, sl) \wedge \varphi_{aux} \wedge \rho_{36}^{[t]}(V, V') \rightarrow SL_4(h', sl')$

$\text{SkipList}_4(h, sl)$

$$\left(\begin{array}{lcl} x.key & = & 14 \\ prev.key & < & 14 \\ x.next[0].key & > & 14 \\ prev.next[0] & = & x.next[0] \\ (x, 0) \notin sl.r & \wedge & 0 \leq 0 \leq 3 \end{array} \right) \wedge \left(\begin{array}{lcl} at_{36}[t] & & \wedge \\ prev'.next[0] = x & & \wedge \\ at'_{37}[t] & & \wedge \\ h' = h \wedge sl = sl' & & \wedge \\ x' = x & & \dots \end{array} \right) \rightarrow \text{SkipList}_4(h', sl')$$

reason about
levels

Verification of Concurrent Skiplists

- ▶ **Skiplist shape preservation** : $\square \text{SkipList}_4(h, sl)$

$$\begin{aligned} \text{SkipList}_4(h, sl : \text{SkipList}) &\triangleq \text{OList}(h, sl, 0) \wedge \\ &\left(h[sl].tail.next[0] = \text{null} \wedge h[sl].tail.next[1] = \text{null} \right) \wedge \\ &\left(h[sl].tail.next[2] = \text{null} \wedge h[sl].tail.next[3] = \text{null} \right) \wedge \\ &\left(\begin{array}{l} \text{SubList}(h, sl.head, sl.tail, 1, sl.head, sl.tail, 0) \wedge \\ \text{SubList}(h, sl.head, sl.tail, 2, sl.head, sl.tail, 1) \wedge \\ \text{SubList}(h, sl.head, sl.tail, 3, sl.head, sl.tail, 2) \end{array} \right) \end{aligned}$$

- ▶ **Program transitions** : $SL_4(h, sl) \wedge \varphi_{aux} \wedge \rho_{36}^{[t]}(V, V') \rightarrow SL_4(h', sl')$

$$\begin{aligned} \text{SkipList}_4(h, sl) &\\ \left(\begin{array}{l} x.key = 14 \wedge \\ prev.key < 14 \wedge \\ x.next[0].key > 14 \wedge \\ prev.next[0] = x.next[0] \wedge \\ (x, 0) \notin sl.r \wedge 0 \leq 0 \leq 3 \end{array} \right) \wedge \left(\begin{array}{l} at_{36}[t] \wedge \\ prev'.next[0] = x \wedge \\ at'_{37}[t] \wedge \\ h' = h \wedge sl = sl' \wedge \\ x' = x \dots \end{array} \right) &\rightarrow \text{SkipList}_4(h', sl') \end{aligned}$$

**reason about
masked regions**

Verification of Concurrent Skiplists

- ▶ Skiplist shape preservation : $\square \text{SkipList}_4(h, sl)$

$$\begin{aligned} \text{SkipList}_4(h, sl : \text{SkipList}) &\triangleq \text{OList}(h, sl, 0) \wedge \\ &\quad \left(h[sl].tail.next[0] = \text{null} \wedge h[sl].tail.next[1] = \text{null} \right) \wedge \\ &\quad \left(h[sl].tail.next[2] = \text{null} \wedge h[sl].tail.next[3] = \text{null} \right) \wedge \\ &\quad \left(\text{SubList}(h, sl.head, sl.tail, 1, sl.head, sl.tail, 0) \wedge \right. \\ &\quad \left. \text{SubList}(h, sl.head, sl.tail, 2, sl.head, sl.tail, 1) \wedge \right. \\ &\quad \left. \text{SubList}(h, sl.head, sl.tail, 3, sl.head, sl.tail, 2) \right) \end{aligned}$$

- ▶ Program transitions : $SL_4(h, sl) \wedge \varphi_{aux} \wedge \rho_{36}^{[t]}(V, V') \rightarrow SL_4(h', sl')$

$$\begin{aligned} \text{SkipList}_4(h, sl) &\\ \left(\begin{array}{lcl} x.key &=& 14 \\ prev.key &<& 14 \\ x.next[0].key &>& 14 \\ prev.next[0] &=& x.next[0] \\ (x, 0) \notin sl.r \wedge 0 \leq 0 \leq 3 \end{array} \wedge \right) \wedge & \left(\begin{array}{lcl} at_{36}[t] && \wedge \\ prev'.next[0] = x && \wedge \\ at'_{37}[t] && \wedge \\ h' = h \wedge sl = sl' && \wedge \\ x' = x && \dots \end{array} \right) \rightarrow \text{SkipList}_4(h', sl') \end{aligned}$$

**reason about
memory, cells**

Verification of Concurrent Skiplists

- ▶ **Skiplist shape preservation** : $\square \text{SkipList}_4(h, sl)$

$$\begin{aligned} \text{SkipList}_4(h, sl : \text{SkipList}) &\triangleq \text{OList}(h, sl, 0) \wedge \\ &\left(h[sl].tail.next[0] = \text{null} \wedge h[sl].tail.next[1] = \text{null} \right) \wedge \\ &\left(h[sl].tail.next[2] = \text{null} \wedge h[sl].tail.next[3] = \text{null} \right) \wedge \\ &\left(\text{SubList}(h, sl.head, sl.tail, 1, sl.head, sl.tail, 0) \wedge \right. \\ &\quad \text{SubList}(h, sl.head, sl.tail, 2, sl.head, sl.tail, 1) \wedge \\ &\quad \left. \text{SubList}(h, sl.head, sl.tail, 3, sl.head, sl.tail, 2) \right) \end{aligned}$$

- ▶ **Program transitions** : $SL_4(h, sl) \wedge \varphi_{aux} \wedge \rho_{36}^{[t]}(V, V') \rightarrow SL_4(h', sl')$

$$\begin{aligned} \text{SkipList}_4(h, sl) &\\ \left(\begin{array}{lcl} x.key &=& 14 \\ prev.key &<& 14 \\ x.next[0].key &>& 14 \\ prev.next[0] &=& x.next[0] \\ (x, 0) \notin sl.r \wedge 0 \leq 0 \leq 3 \end{array} \wedge \right) \wedge & \left(\begin{array}{lcl} at_{36}[t] && \wedge \\ prev'.next[0] = x && \wedge \\ at'_{37}[t] && \wedge \\ h' = h \wedge sl = sl' && \wedge \\ x' = x && \dots \end{array} \right) \rightarrow \text{SkipList}_4(h', sl') \end{aligned}$$

**reason about
sublist**

Our Contribution

- ▶ TSL_K , a theory for concurrent skip lists of height K
- ▶ We show TSL_K **decidable**
- ▶ We propose a combination based **decision procedure**

TSL_K: A Theory for Concurrent Skiplists of height K

- ▶ Based on Theory of Linked Lists (**TLL**)
- ▶ Each element is **ordered** by a **key**
- ▶ **Reasoning** on levels is extended to **K levels**
- ▶ Regions extended to **masked regions**
- ▶ List extended to **ordered lists** and **subpaths** of ordered lists

TSL_K : A Theory for Concurrent Skip Lists

- ▶ TSL_K is a union of other theories

TSL_K: A Theory for Concurrent SkipLists

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$$\Sigma_{\text{addr}}$$

TSL_K: A Theory for Concurrent SkipLists

- ▶ TSL_K is a union of other theories

$$\Sigma_{\text{addr}} \cup \Sigma_{\text{elem}}$$

TSL_K: A Theory for Concurrent SkipLists

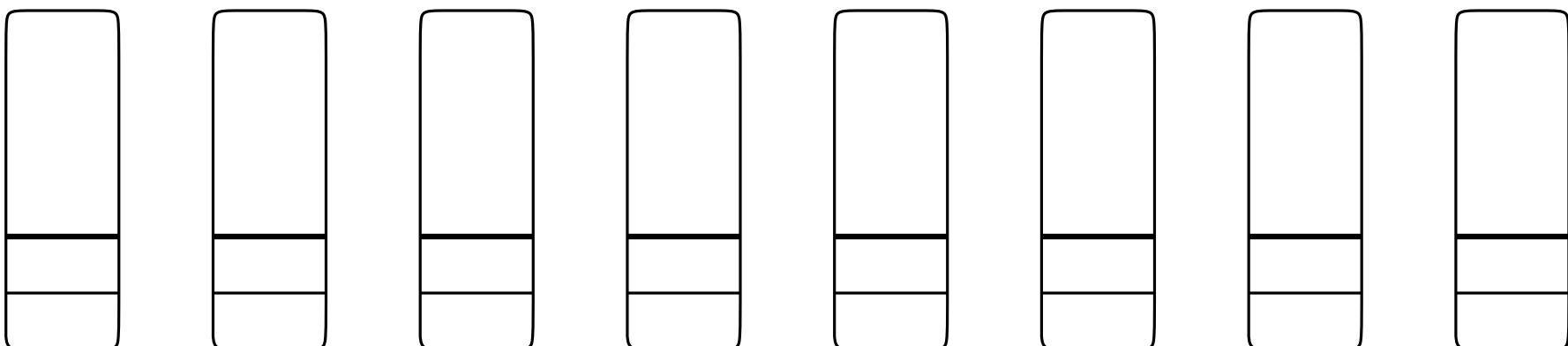
- ▶ TSL_K is a union of other theories

$$\Sigma_{\text{addr}} \cup \Sigma_{\text{elem}} \cup \Sigma_{\text{ord}}$$

TSL_K: A Theory for Concurrent SkipLists

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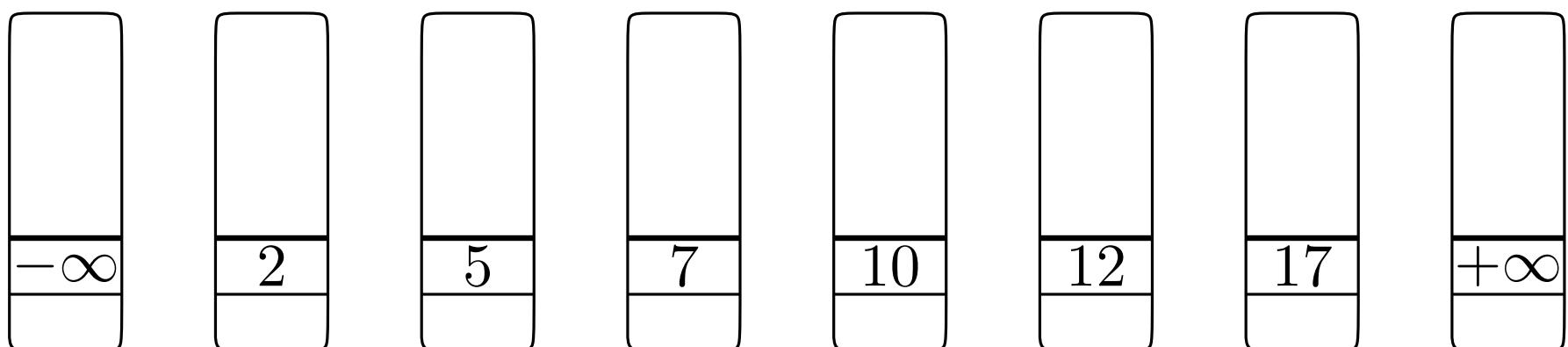
$$\Sigma_{\text{addr}} \cup \Sigma_{\text{elem}} \cup \Sigma_{\text{ord}} \cup \Sigma_{\text{cell}}$$



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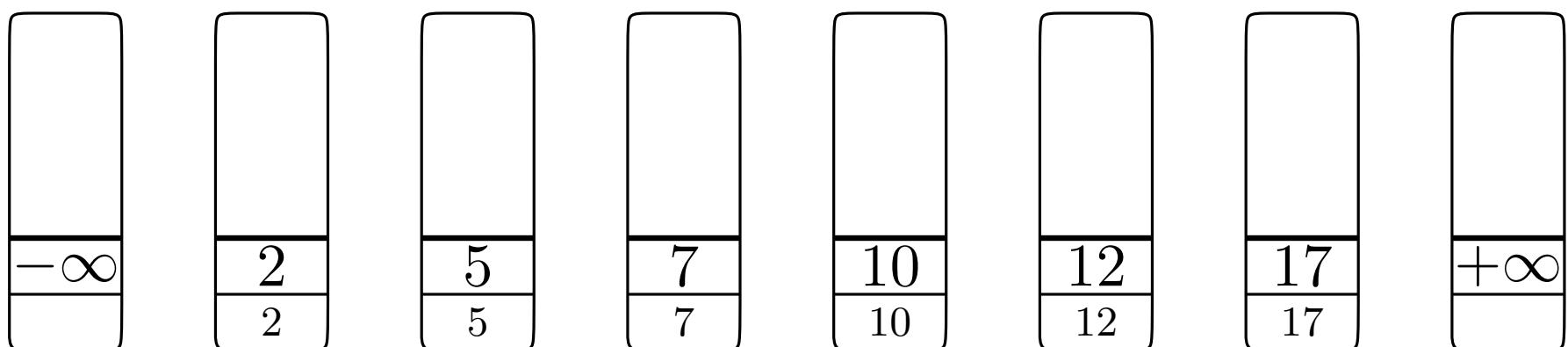
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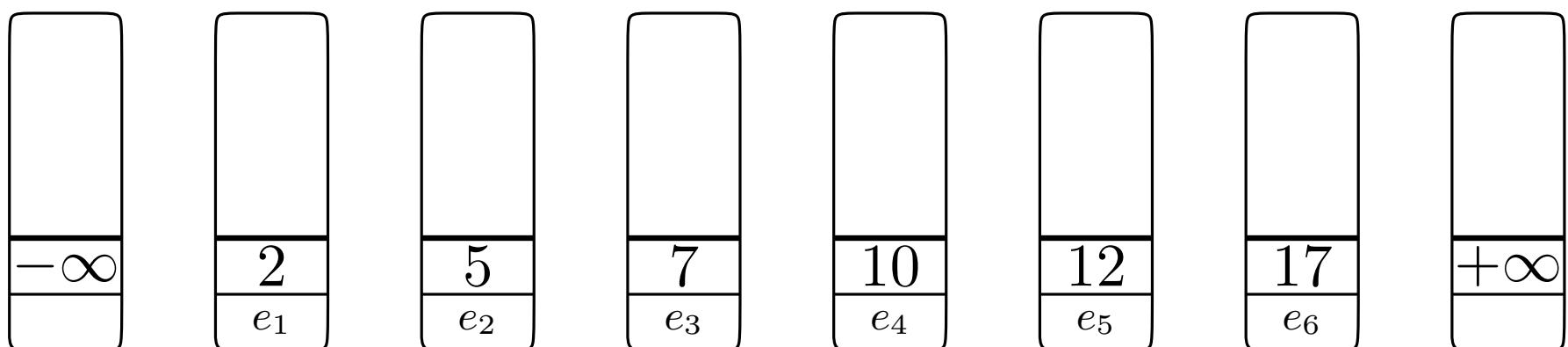
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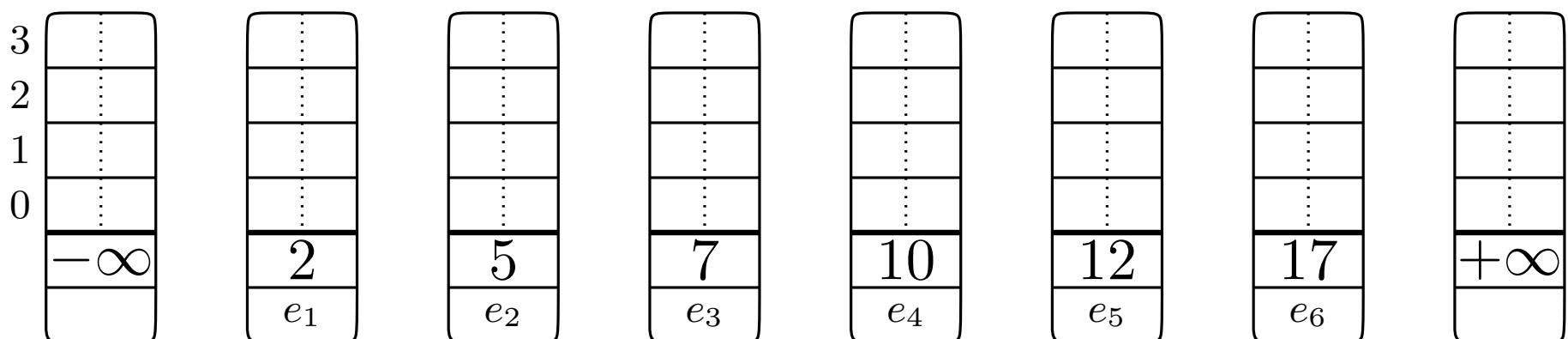
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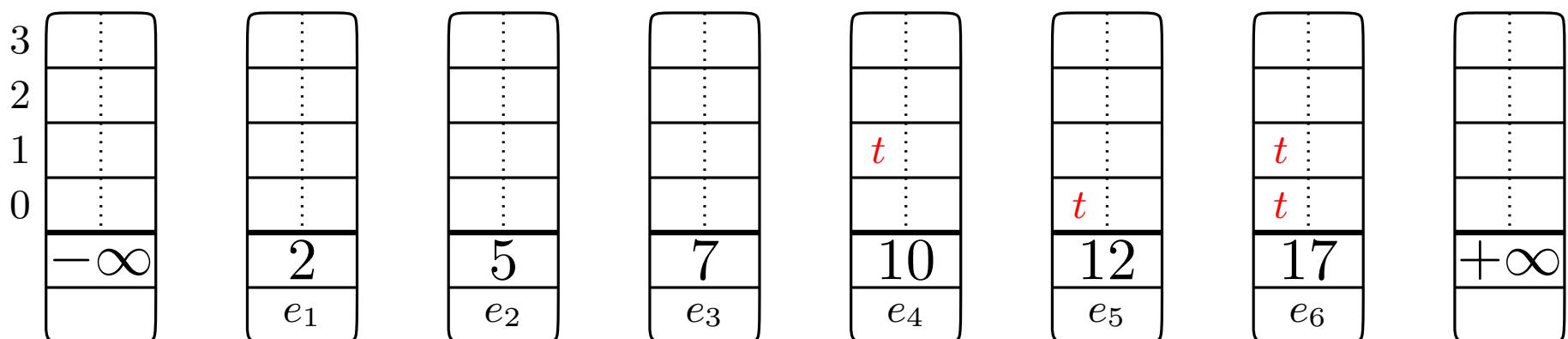
$$\Sigma_{\text{addr}} \cup \Sigma_{\text{elem}} \cup \Sigma_{\text{ord}} \cup \Sigma_{\text{cell}} \cup \Sigma_{\text{level}_K}$$



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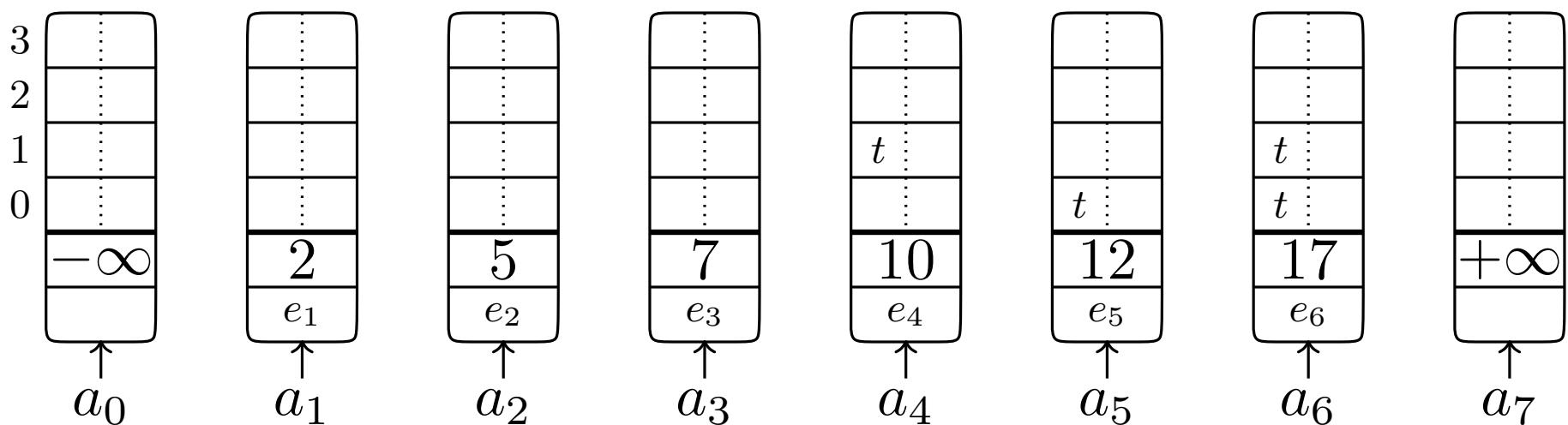
$$\Sigma_{\text{addr}} \cup \Sigma_{\text{elem}} \cup \Sigma_{\text{ord}} \cup \Sigma_{\text{cell}} \cup \Sigma_{\text{level}_K} \cup \Sigma_{\text{thid}}$$



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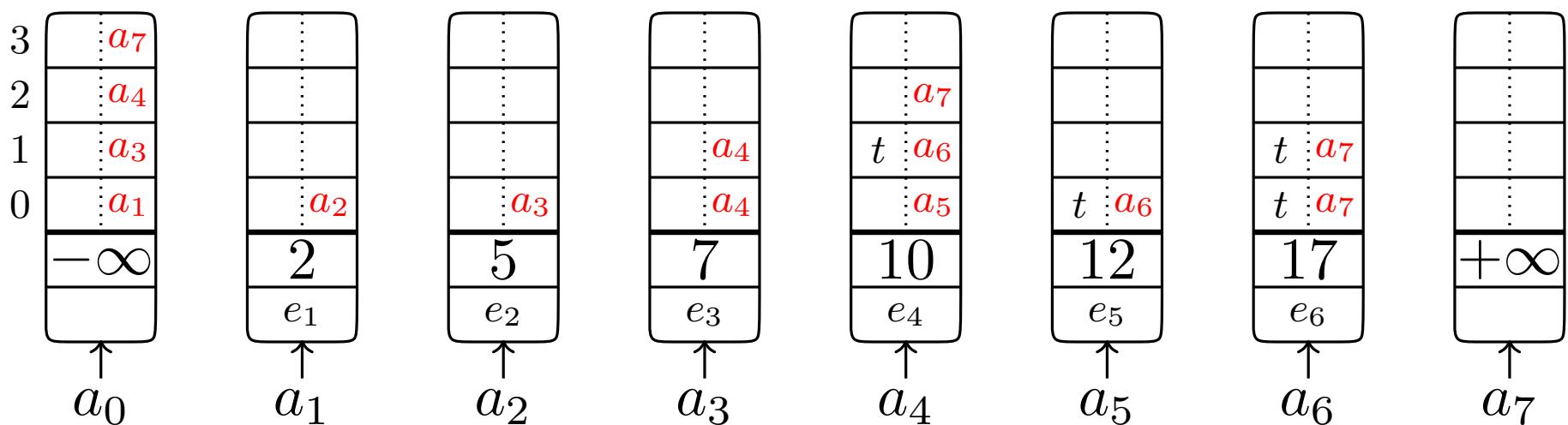
$$\Sigma_{\text{addr}} \cup \Sigma_{\text{elem}} \cup \Sigma_{\text{ord}} \cup \Sigma_{\text{cell}} \cup \Sigma_{\text{level}_K} \cup \Sigma_{\text{thid}} \cup \Sigma_{\text{mem}}$$



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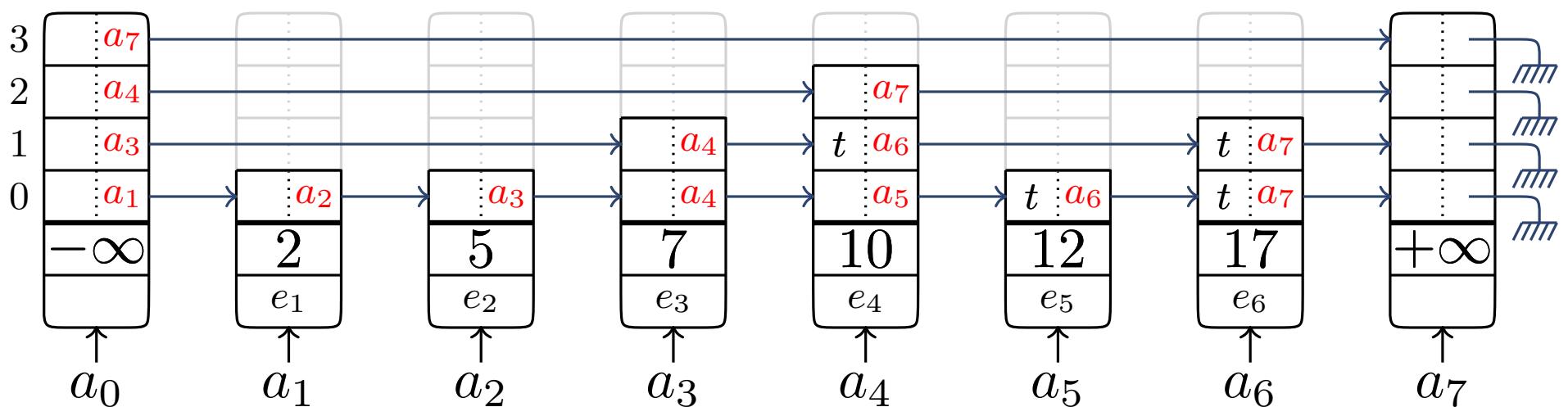
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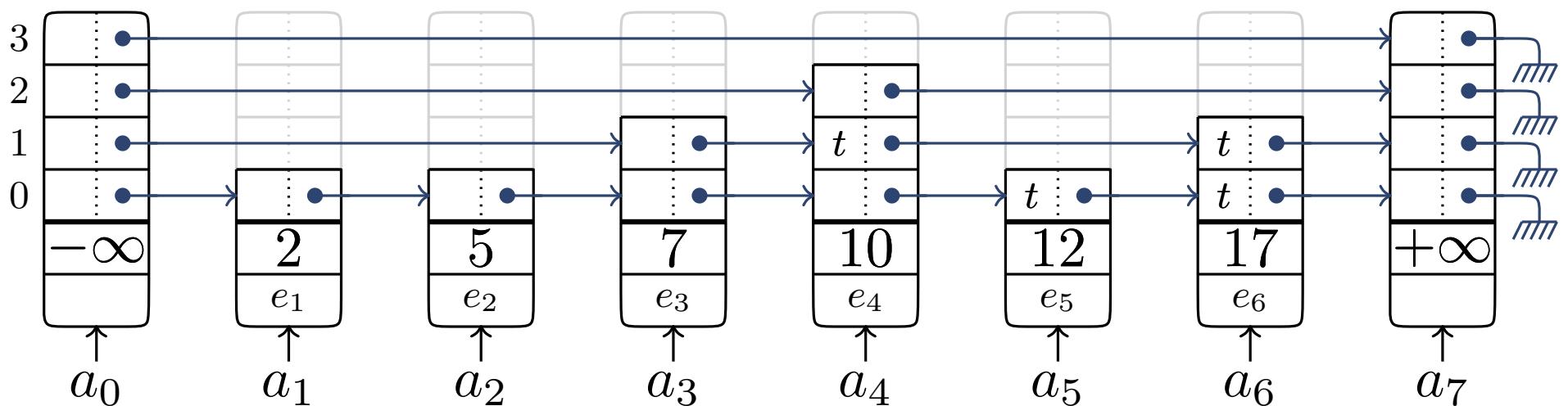
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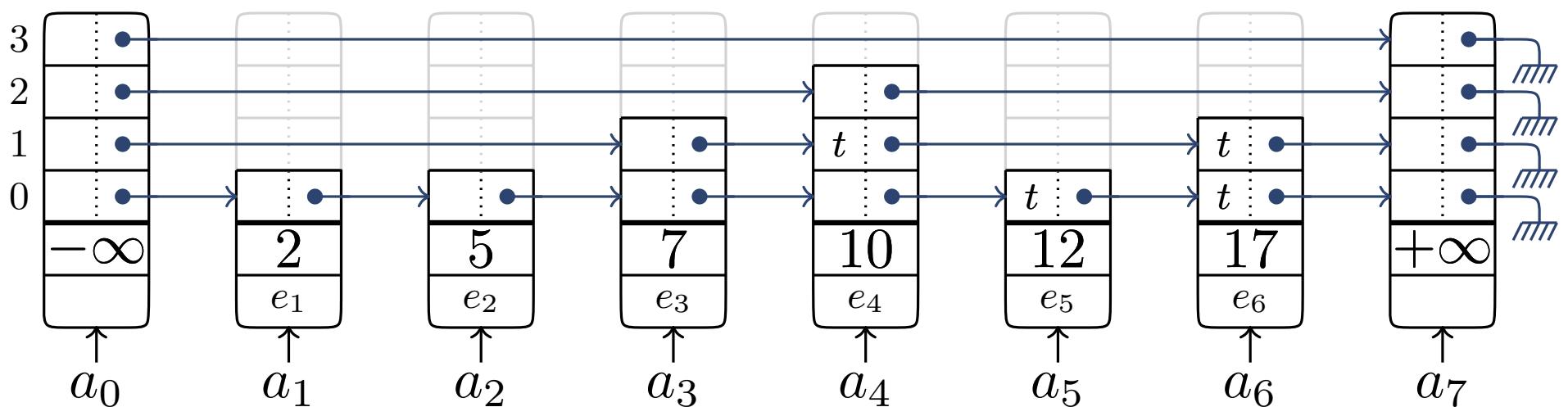
$$\Sigma_{\text{addr}} \cup \Sigma_{\text{elem}} \cup \Sigma_{\text{ord}} \cup \Sigma_{\text{cell}} \cup \Sigma_{\text{level}_K} \cup \Sigma_{\text{thid}} \cup \Sigma_{\text{mem}}$$



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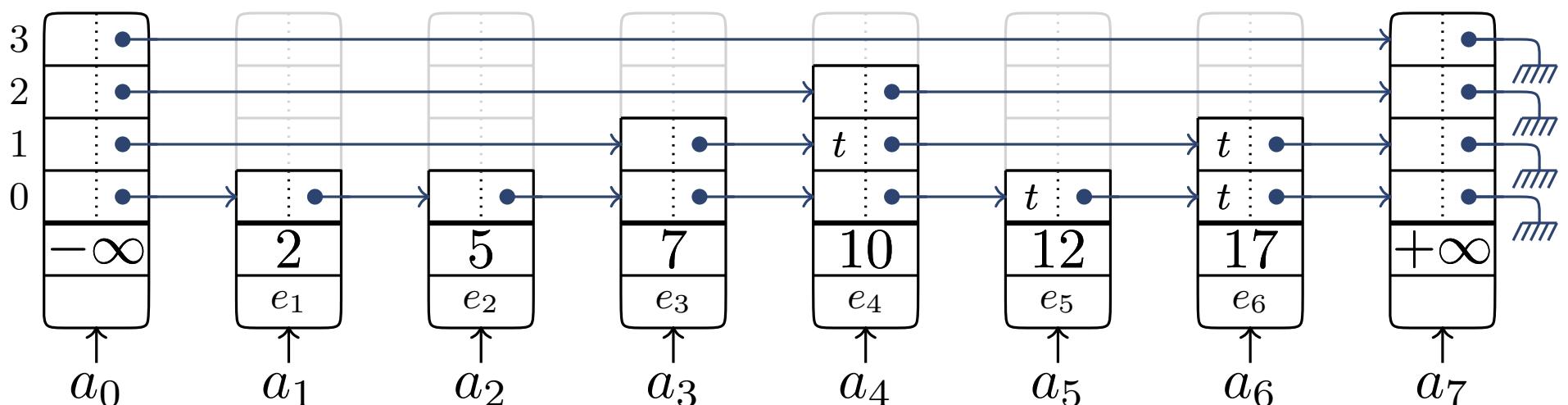
$$\Sigma_{\text{addr}} \cup \Sigma_{\text{elem}} \cup \Sigma_{\text{ord}} \cup \Sigma_{\text{cell}} \cup \Sigma_{\text{level}_K} \cup \Sigma_{\text{thid}} \cup \Sigma_{\text{mem}} \cup \Sigma_{\text{set}}$$



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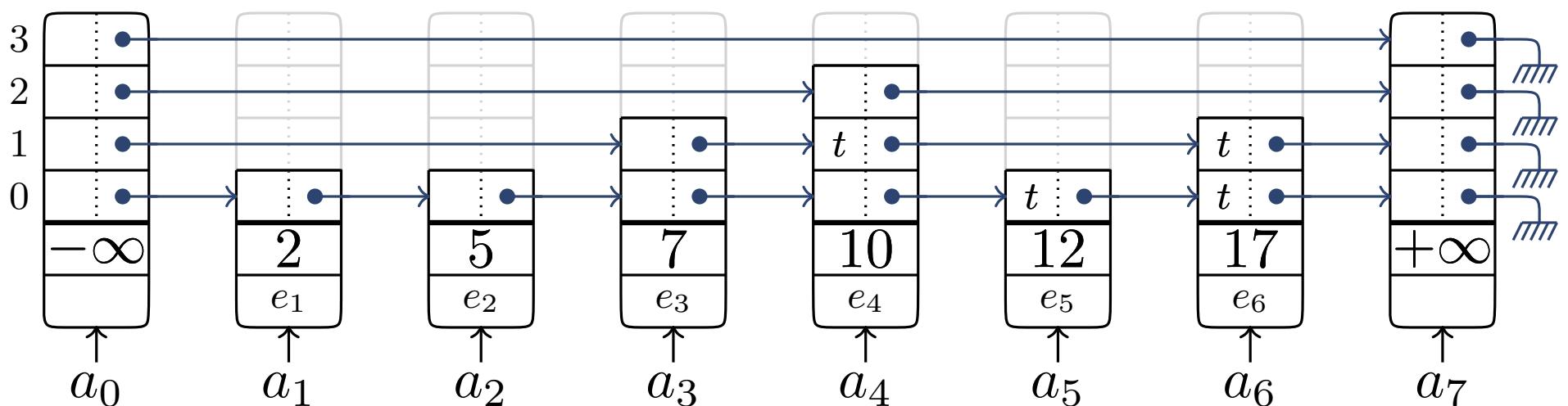
$$\Sigma_{\text{addr}} \cup \Sigma_{\text{elem}} \cup \Sigma_{\text{ord}} \cup \Sigma_{\text{cell}} \cup \Sigma_{\text{level}_K} \cup \Sigma_{\text{thid}} \cup \Sigma_{\text{mem}} \cup \Sigma_{\text{set}} \cup \Sigma_{\text{setth}}$$



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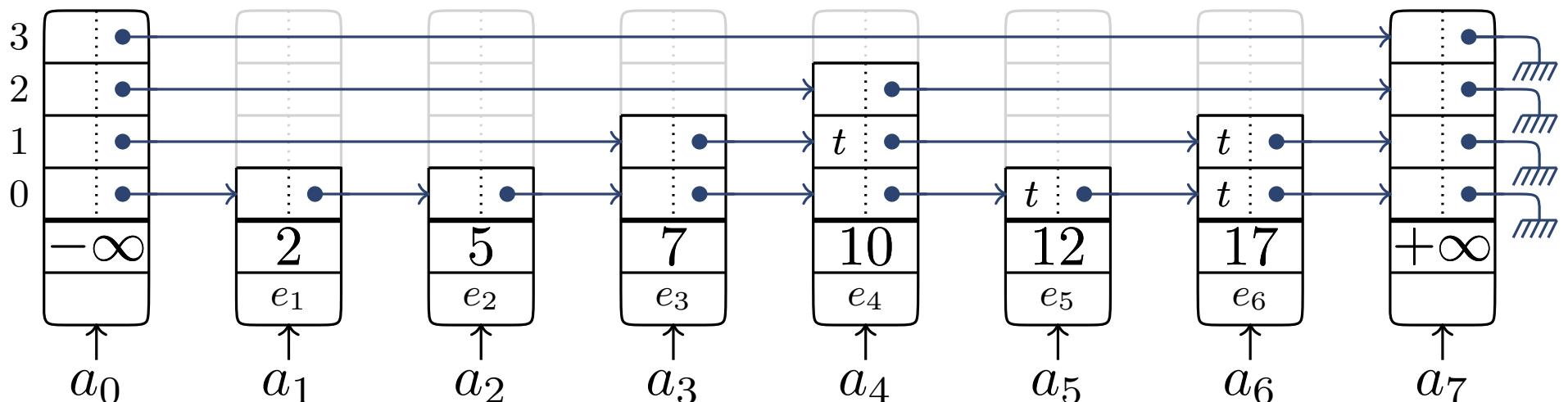
TSL_K: A Theory for Concurrent Skip Lists

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$$\Sigma_{\text{addr}} \cup \Sigma_{\text{elem}} \cup \Sigma_{\text{ord}} \cup \Sigma_{\text{cell}} \cup \Sigma_{\text{level}_K} \cup \Sigma_{\text{thid}} \cup \Sigma_{\text{mem}} \cup \\ \Sigma_{\text{set}} \cup \Sigma_{\text{setth}} \cup \Sigma_{\text{mrgn}} \cup \Sigma_{\text{reachability}}$$

path = a non-repeating sequence of addresses

$$[a_1, a_2, a_3]$$

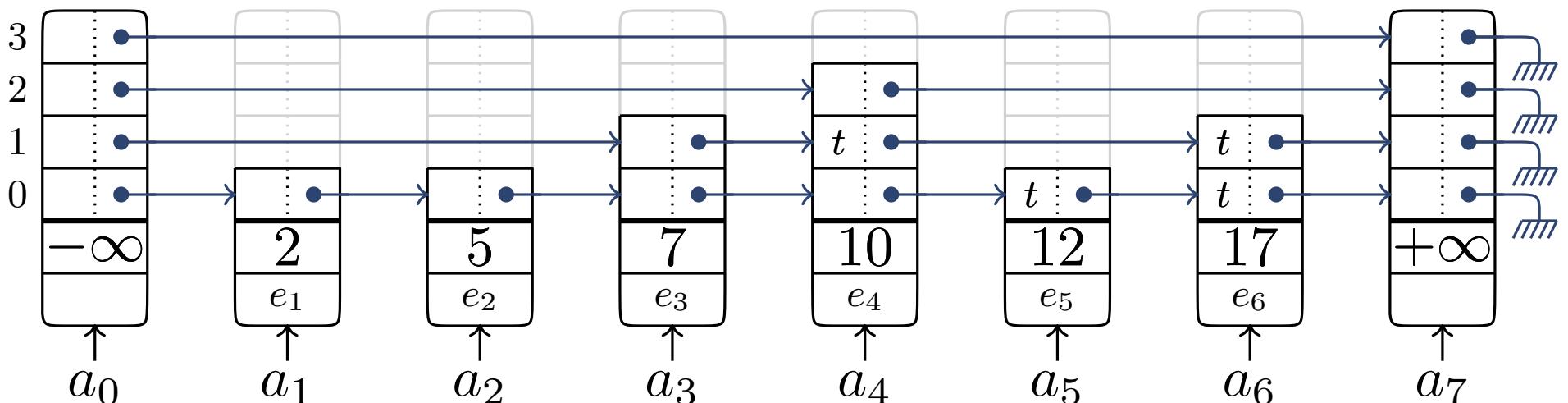


TSL_K: A Theory for Concurrent SkipLists

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append([a₁, a₂], [a₃], [a₁, a₂, a₃])

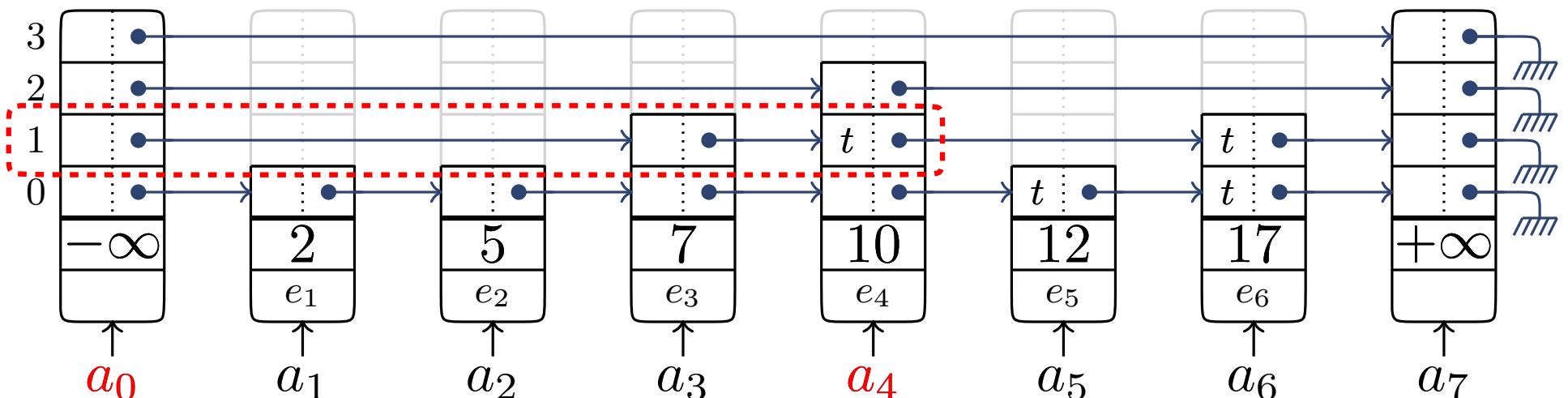


TSL_K: A Theory for Concurrent SkipLists

- ▶ TSL_K is a union of other theories

$$\Sigma_{\text{addr}} \cup \Sigma_{\text{elem}} \cup \Sigma_{\text{ord}} \cup \Sigma_{\text{cell}} \cup \Sigma_{\text{level}_K} \cup \Sigma_{\text{thid}} \cup \Sigma_{\text{mem}} \cup \\ \Sigma_{\text{set}} \cup \Sigma_{\text{setth}} \cup \Sigma_{\text{mrgn}} \cup \Sigma_{\text{reachability}}$$

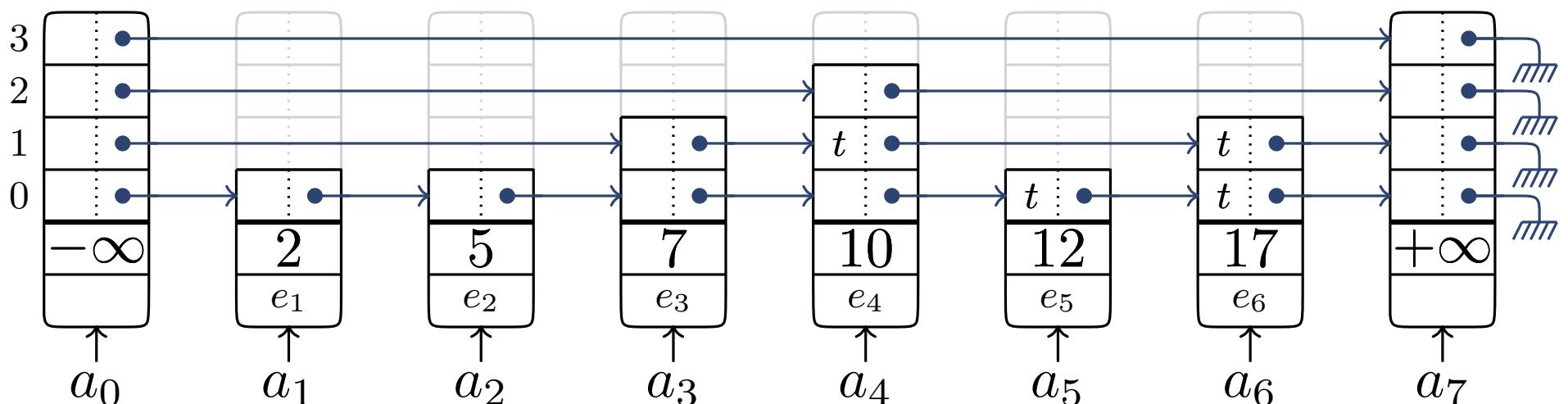
$\text{reach}_K(a_0, a_4, 1, [a_0, a_3])$



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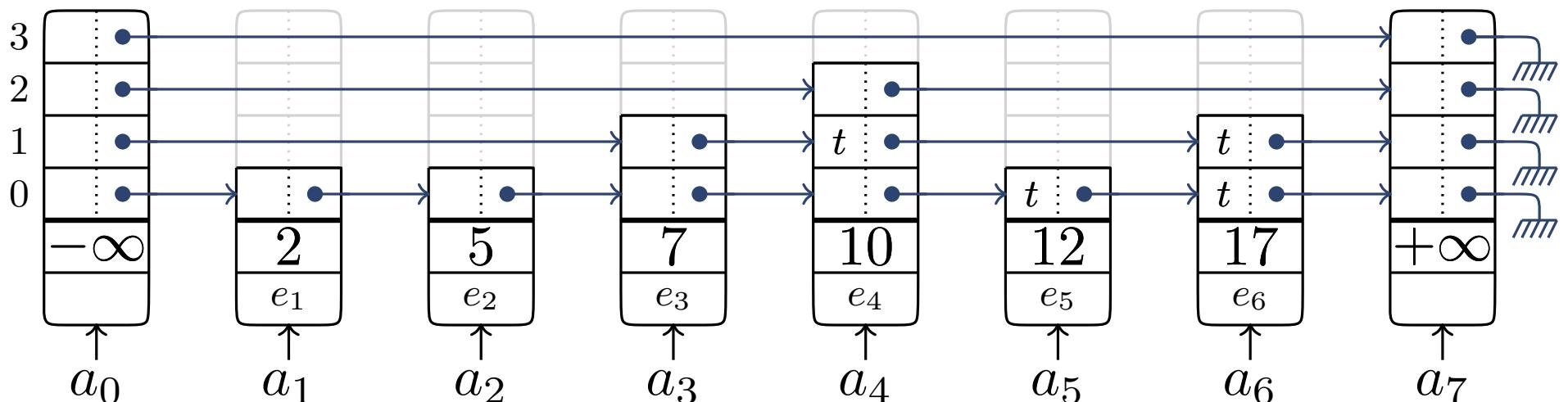


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$$\text{path2set}([a_2, a_3]) = \{a_2, a_3\}$$

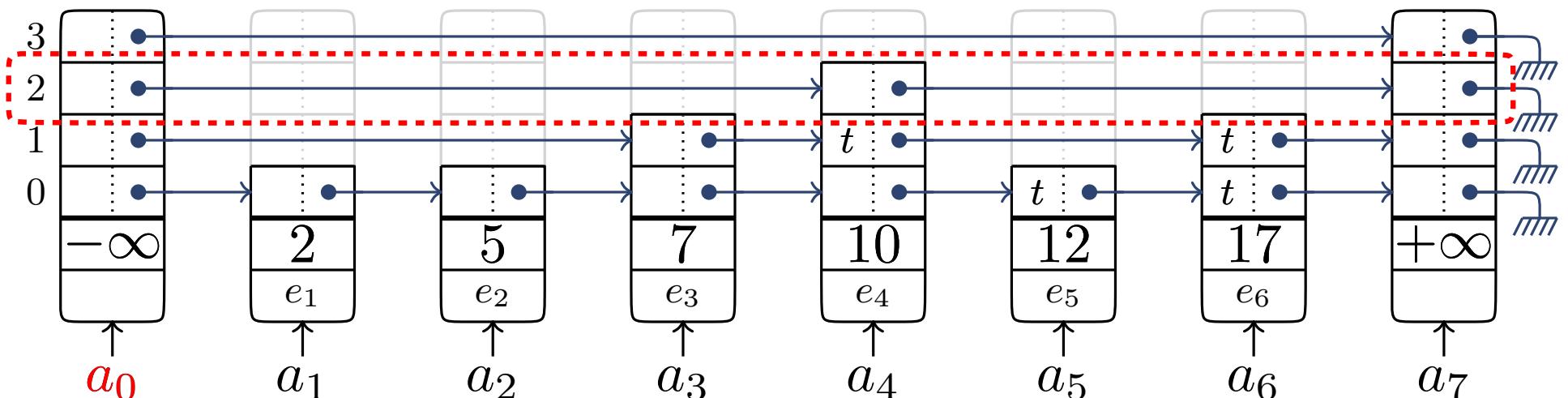


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$$addr2set_K(a_0, 2) = \{a_0, a_4, a_7\}$$

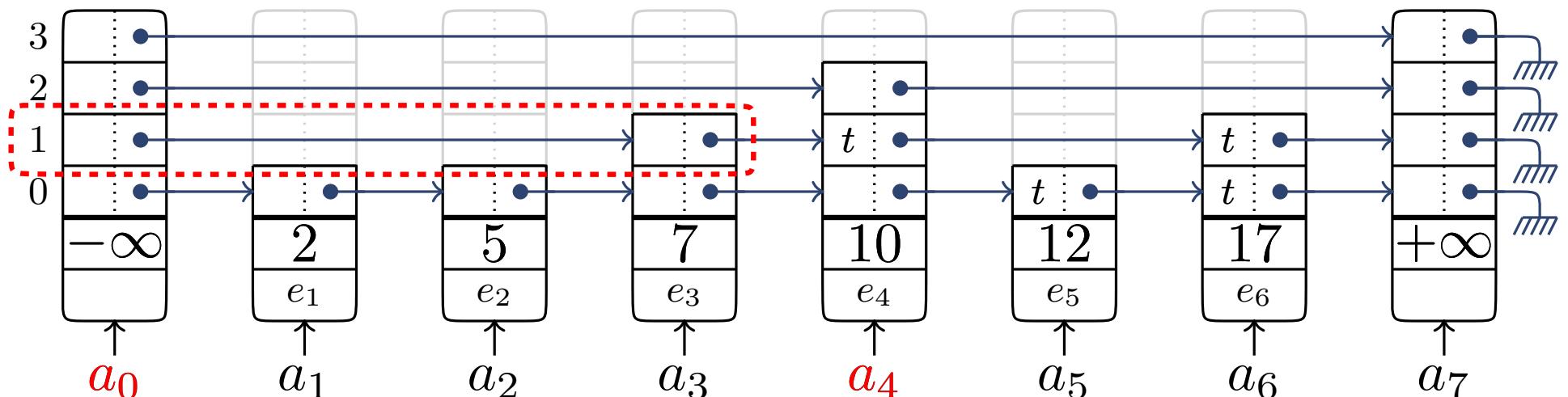


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$$getp_K(a_0, a_4, 1) = [a_0, a_3]$$

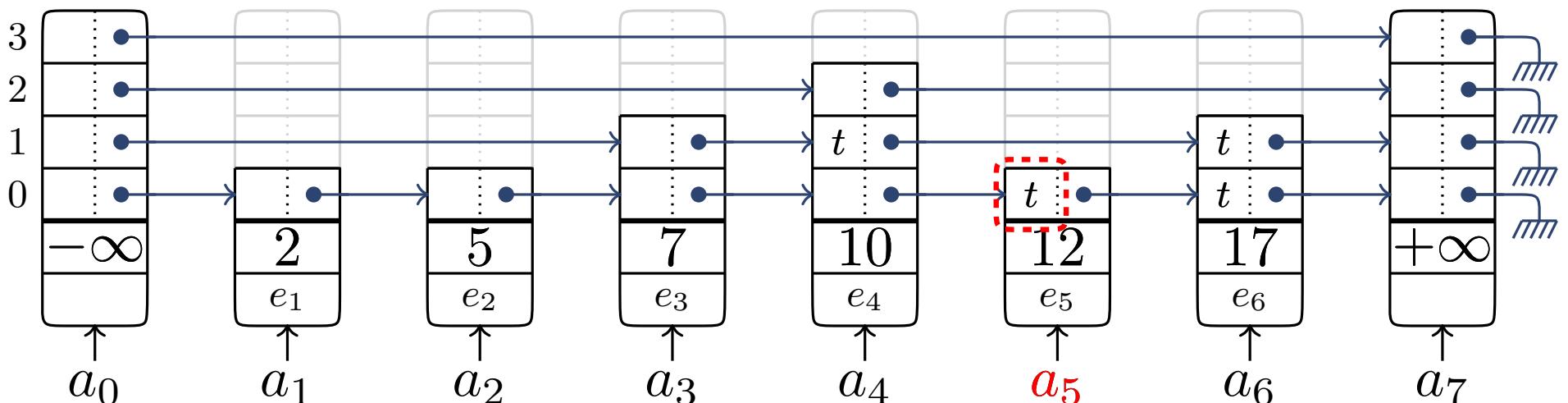


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$$\text{firstlocked}_K([a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7], 0) = a_5$$

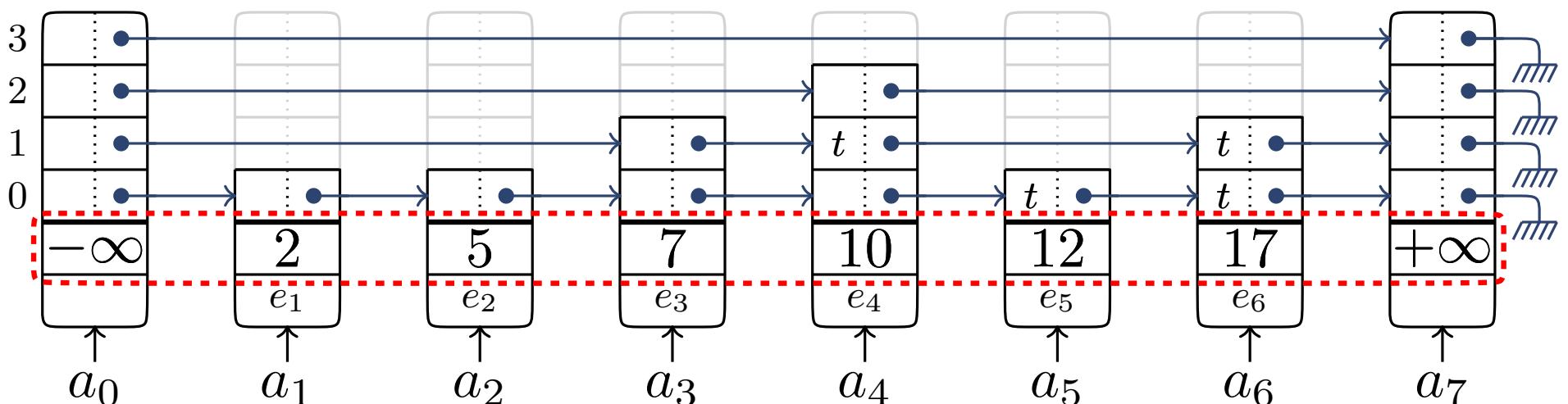


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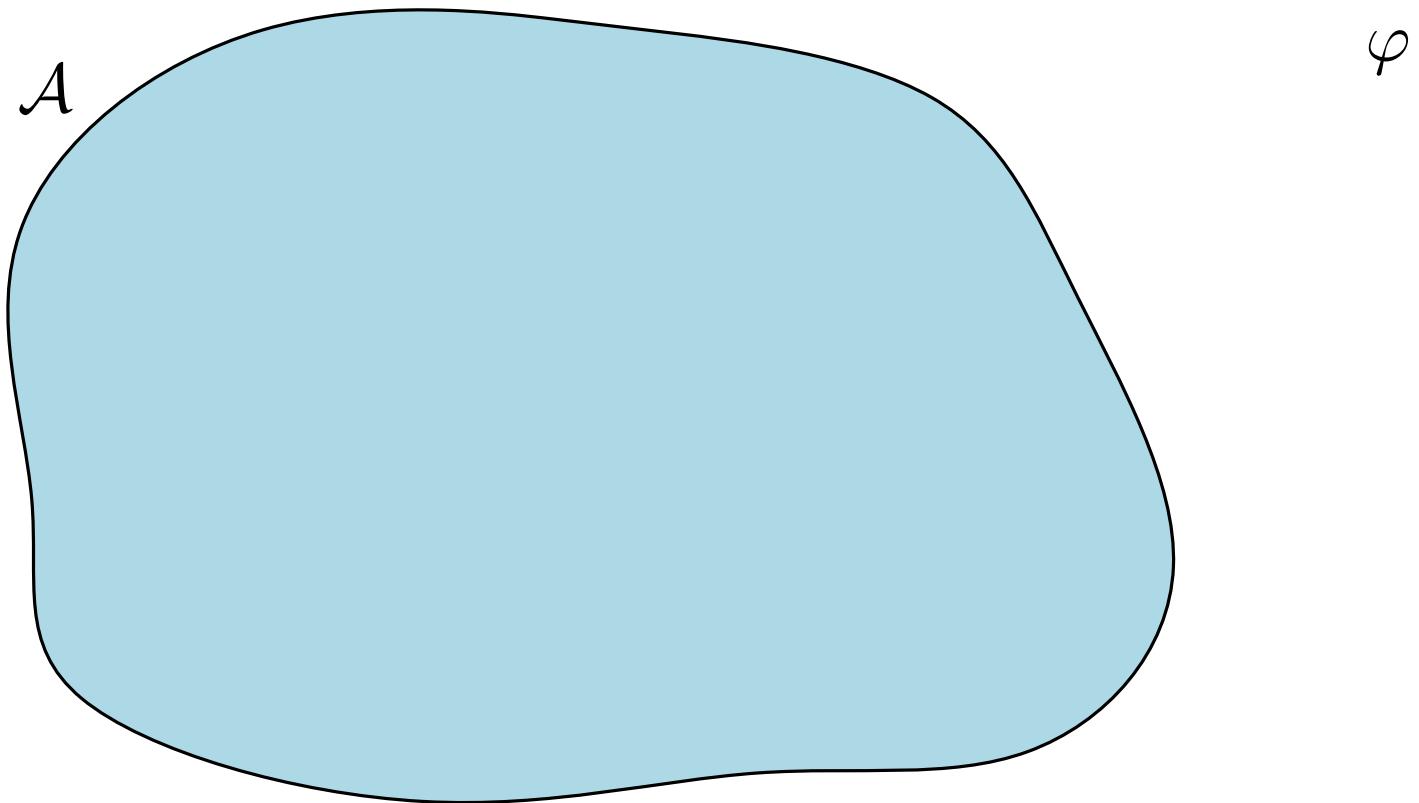
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$\text{ordList}([a_0, a_1, a_2, a_3, a_4, a_5, a_6, a_7])$

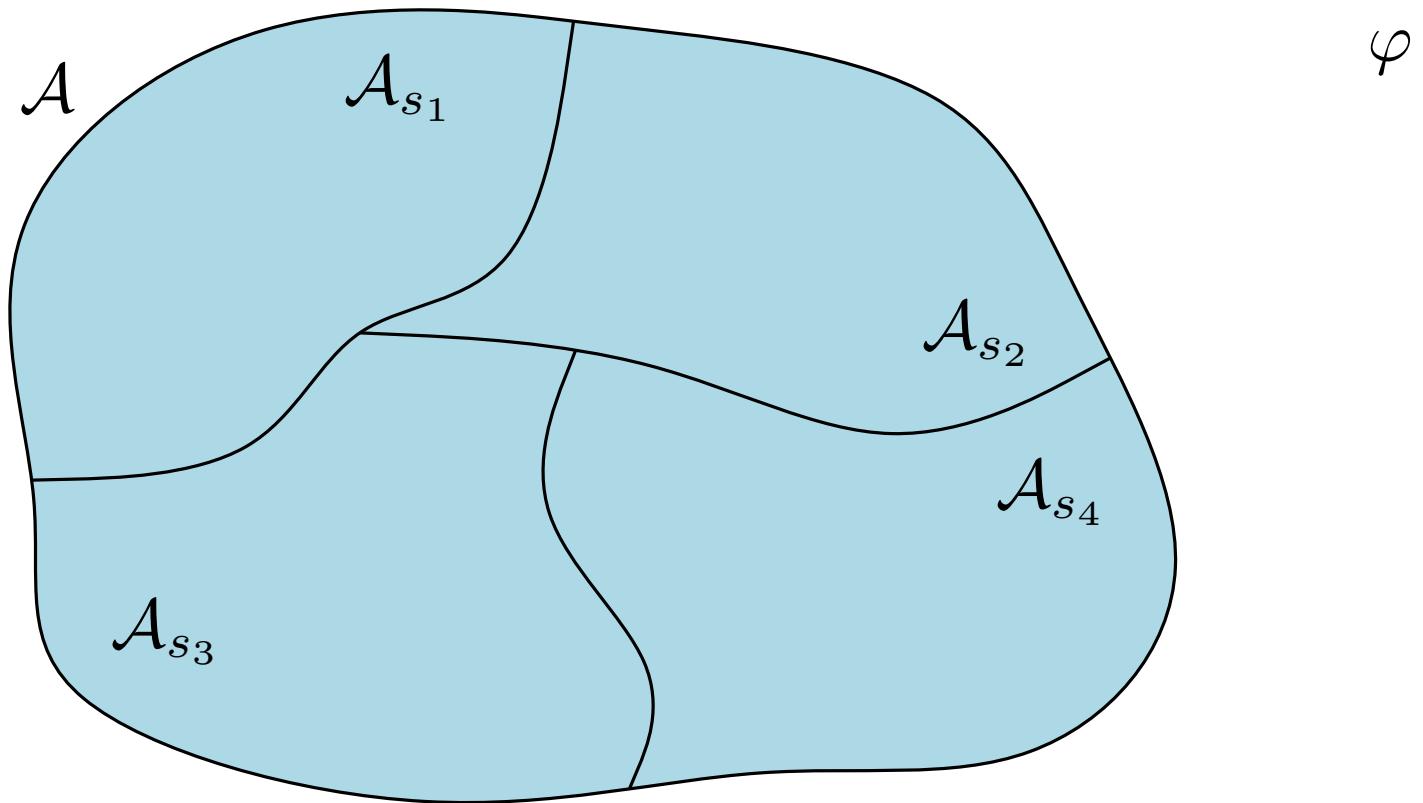


Decidability by Small Model Property (SMP)

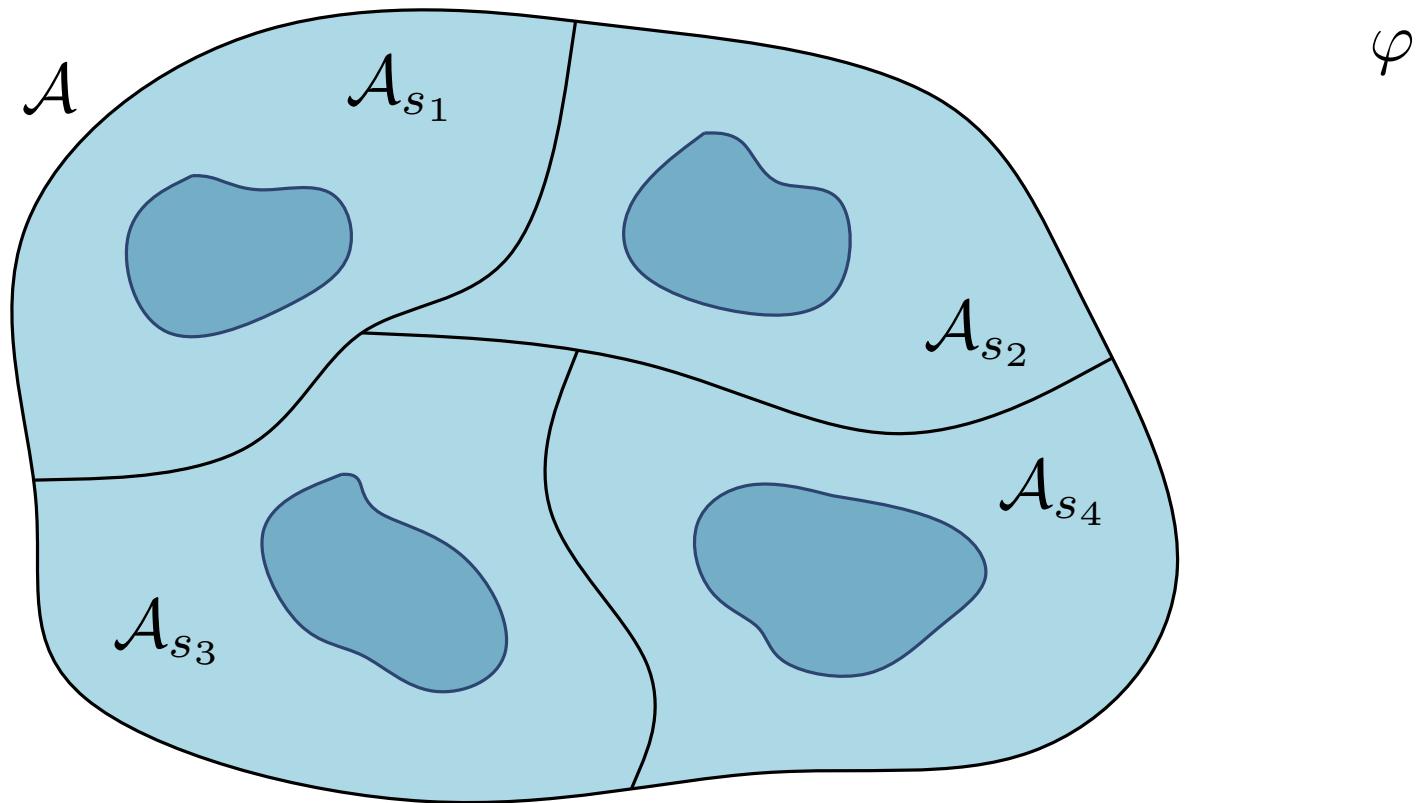
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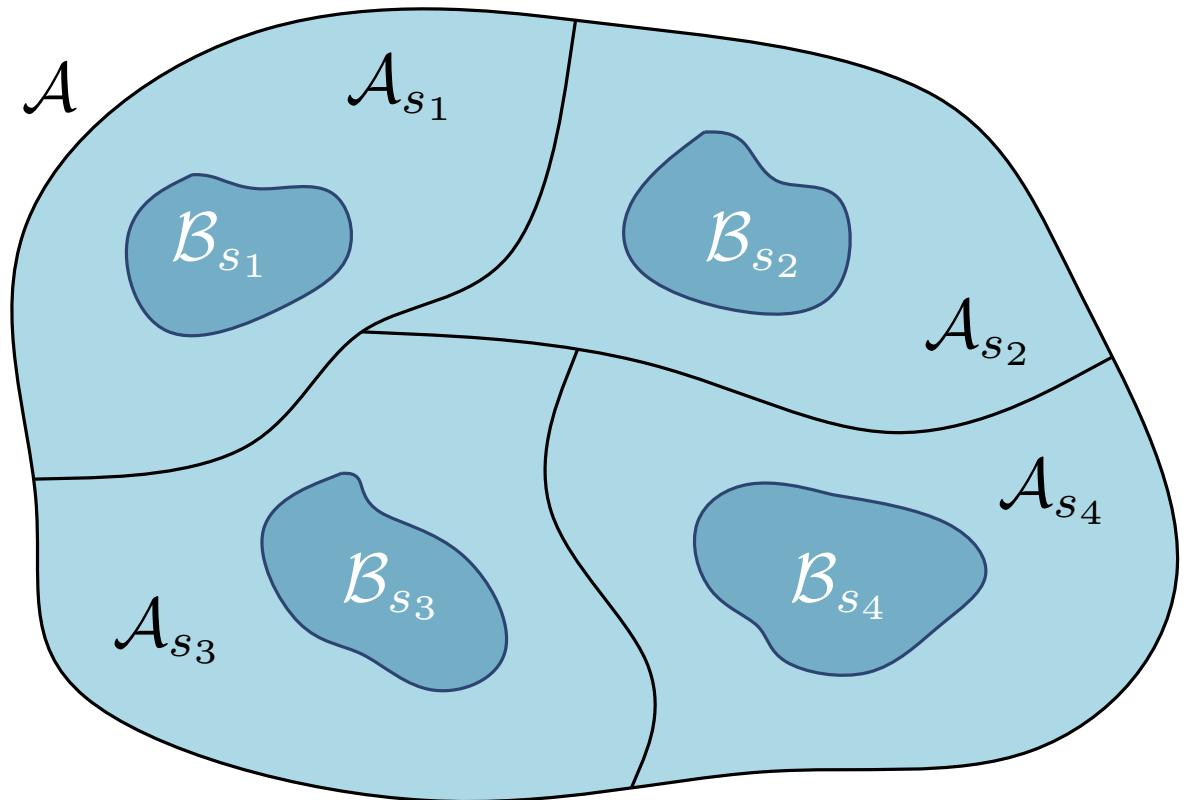
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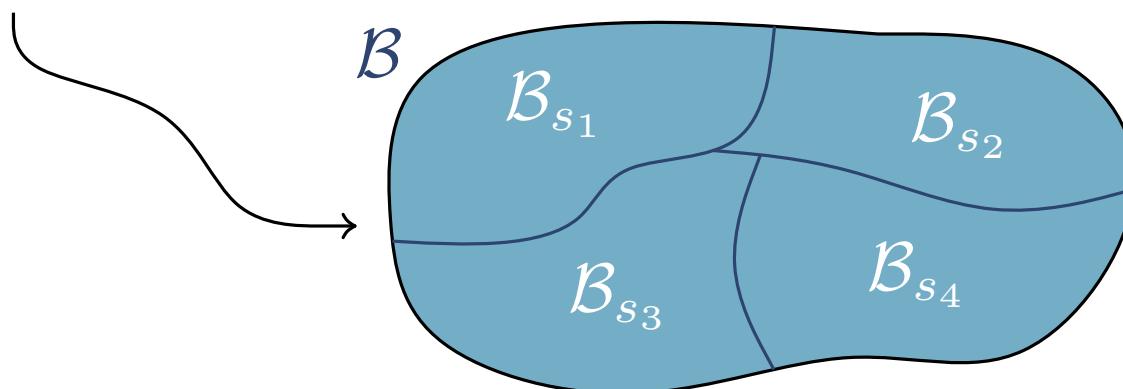


Decidability by Small Model Property (SMP)



φ

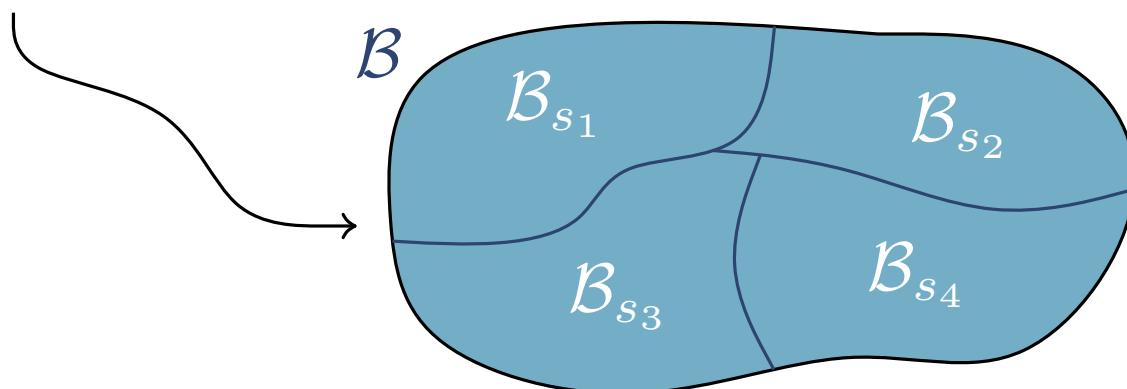
Finite elements



Decidability by Small Model Property (SMP)

- ▶ Let Γ be a conjunction of TSL_K -literals

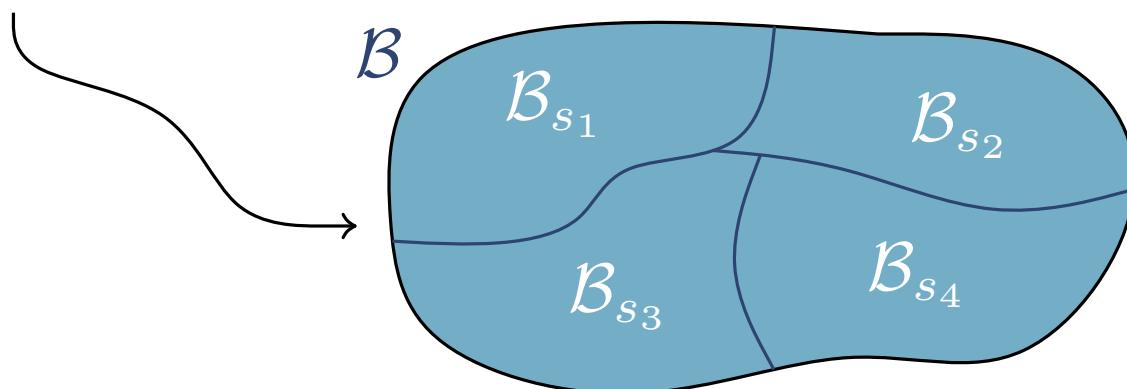
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Decidability by Small Model Property (SMP)

- ▶ Let Γ be a conjunction of TSL_K -literals
- ▶ If Γ is satisfied in an arbitrary TSL_K interpretation \mathcal{A}

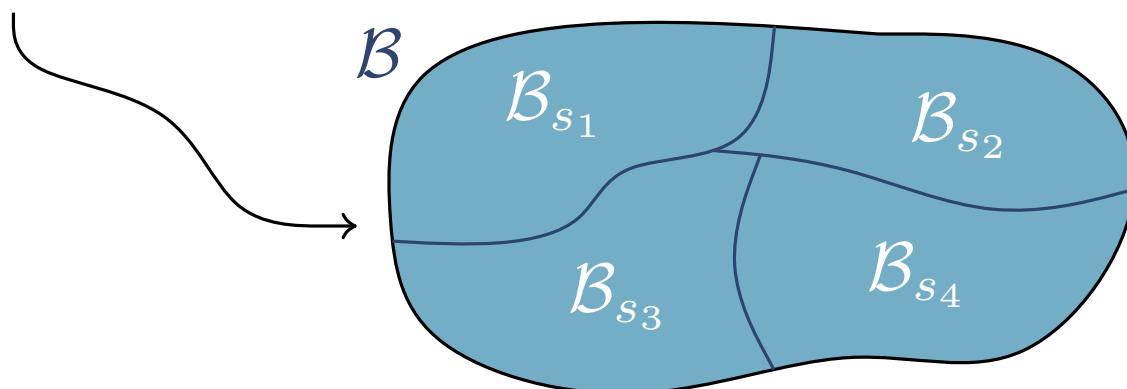
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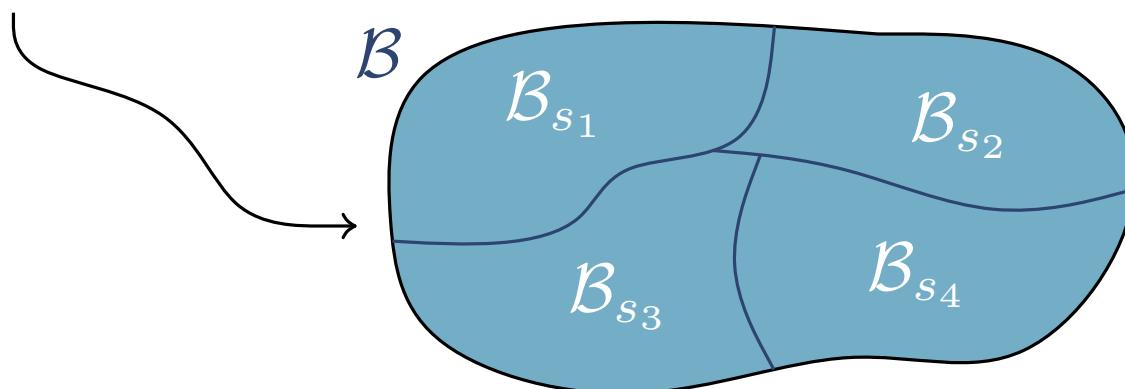


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TSL_K is **decidable** by enumerating all possible elements

Finite elements



A Decision Procedure for TSL_k (main idea)

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Stable Infinite & Politeness

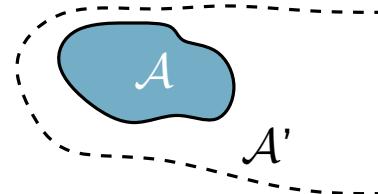
Stable Infinite & Politeness

- ▶ **Stable infinite**
- ▶ **Polite** with respect to sorts s_1, \dots, s_n

Stable Infinite & Politeness

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for all QF-formula φ , exists an infinite interpretation \mathcal{A}'

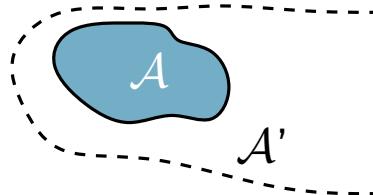


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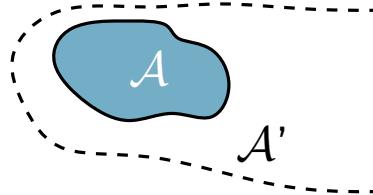
- ▶ **Smooth**

- ▶ **Finite witnessable**

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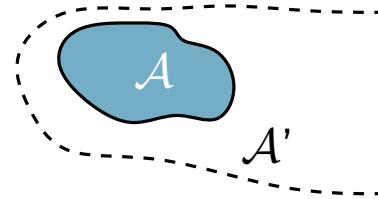
if \mathcal{A} is a model of φ , with domains $\mathcal{A}_{s_1} \dots \mathcal{A}_{s_n}$

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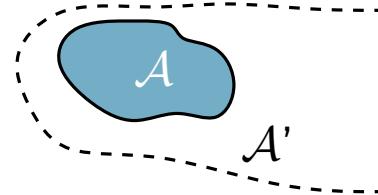
$$\frac{|A_{s_1}| \cdots |A_{s_n}|}{\wedge \quad \cdots \quad \wedge} \quad k_1 \quad \cdots \quad k_n$$

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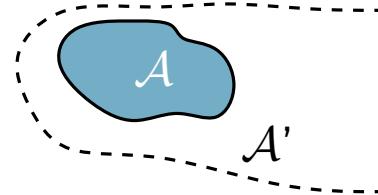
$$\begin{array}{c} |\mathcal{A}_{s_1}| \cdots |\mathcal{A}_{s_n}| \\ \wedge \quad \cdots \quad \wedge \\ k_1 \quad \cdots \quad k_n \\ || \quad \cdots \quad || \\ |\mathcal{B}_{s_1}| \cdots |\mathcal{B}_{s_n}| \end{array}$$

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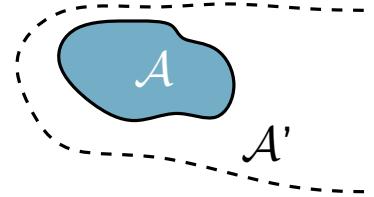
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f

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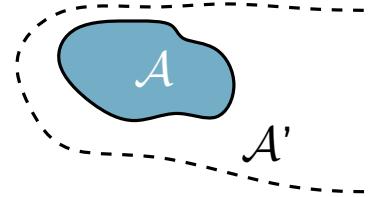
► Finite witnessable

$$\varphi \quad f$$

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then, for every

there is a model \mathcal{B} of φ , such that

► Finite witnessable

$$\varphi \xrightarrow{f} (\exists \bar{v})\psi \quad \text{s.t. } [\bar{v} = V_\varphi \setminus V_\psi]$$

if ψ is **satisfiable**, then exists \mathcal{B} with **one variable per value**

Checklist

- ▶ We use a **many-sorted variant of Nelson-Oppen**

To apply it, we require:

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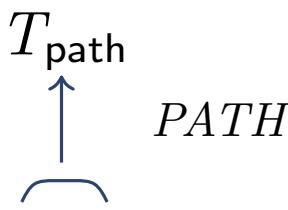
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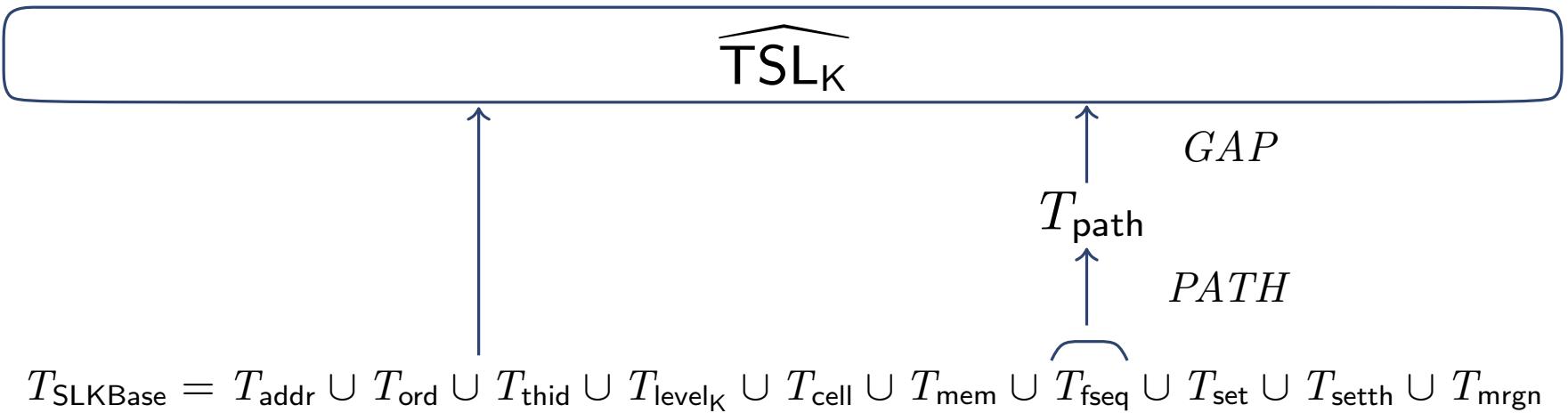
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The diagram shows the term T_{path} positioned above T_{fseq} . A blue curved brace groups T_{fseq} with T_{path} , and a vertical blue arrow points upwards from T_{fseq} towards T_{path} . The word "PATH" is written to the right of the brace.

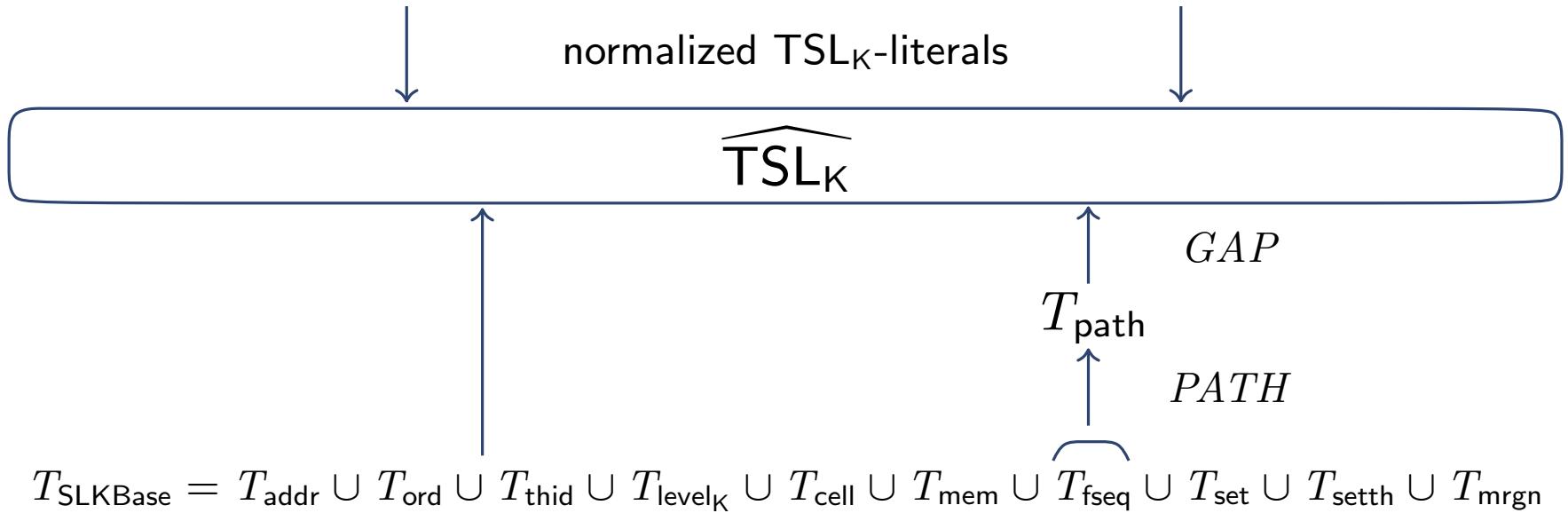
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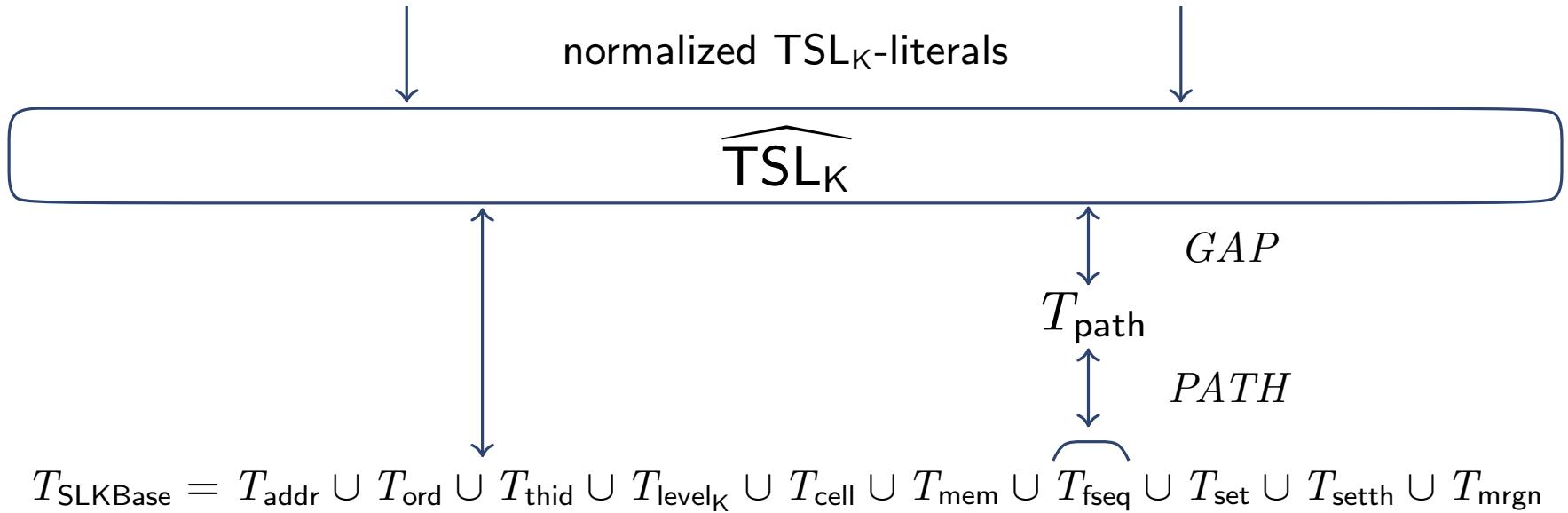
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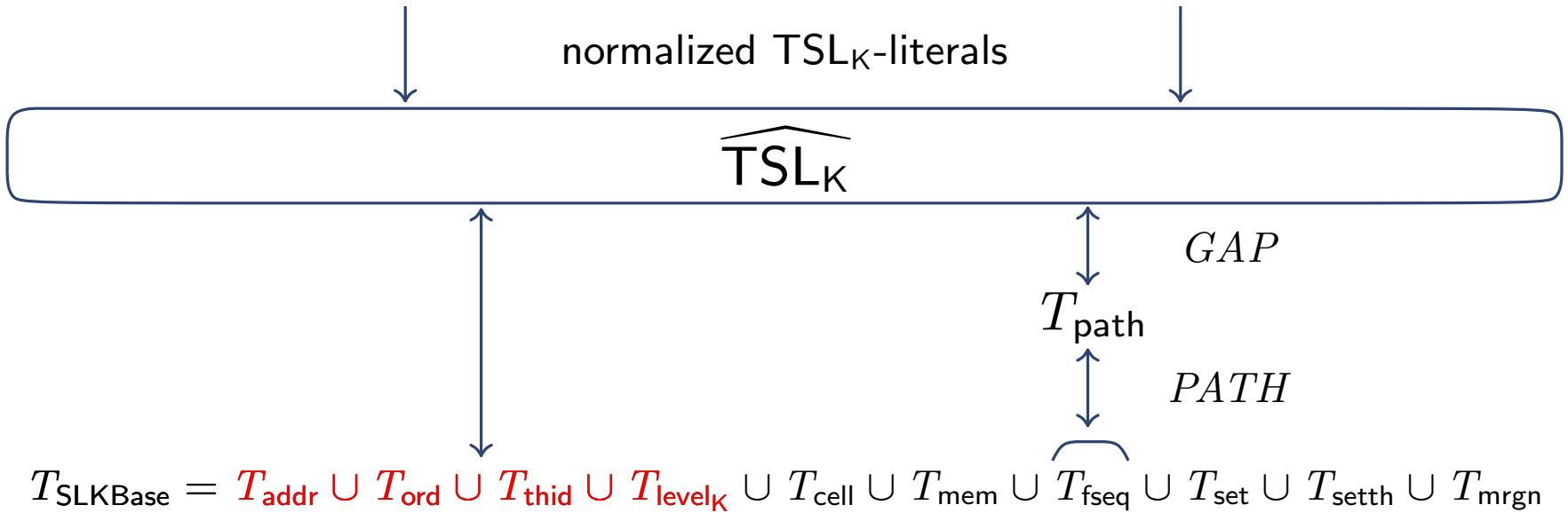
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- **Unfolding** of definitions in $PATH$ and GAP

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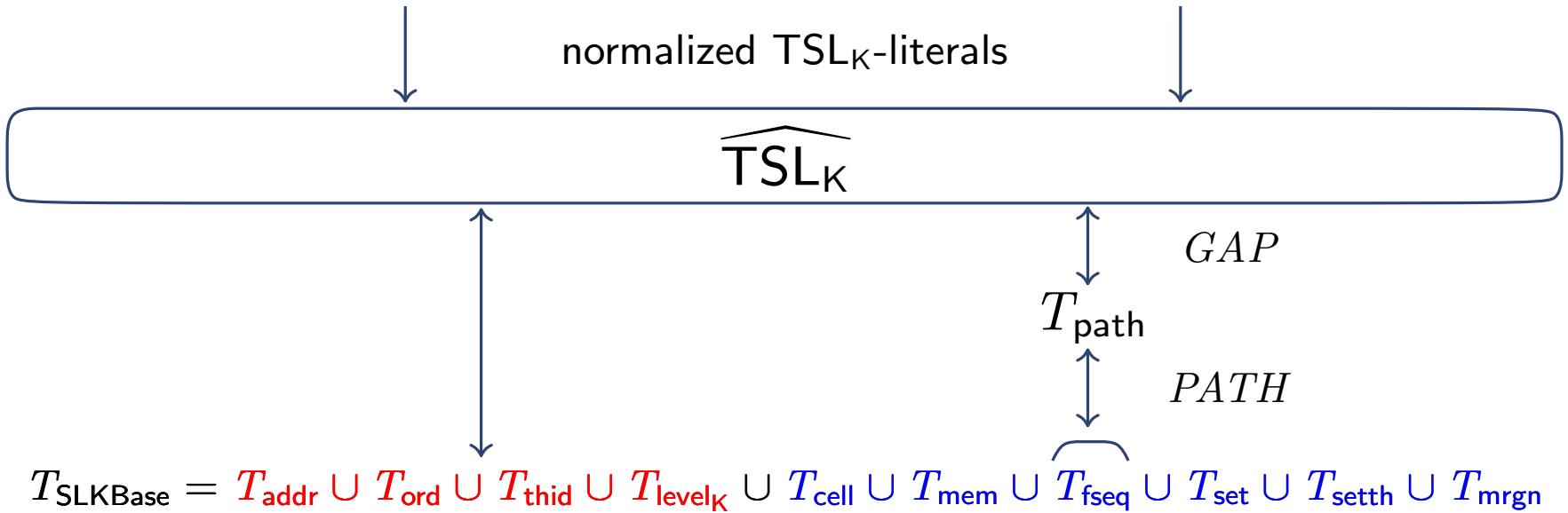
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- ▶ **Unfolding** of definitions in $PATH$ and GAP
- ▶ Share sorts only, hence **trivial**

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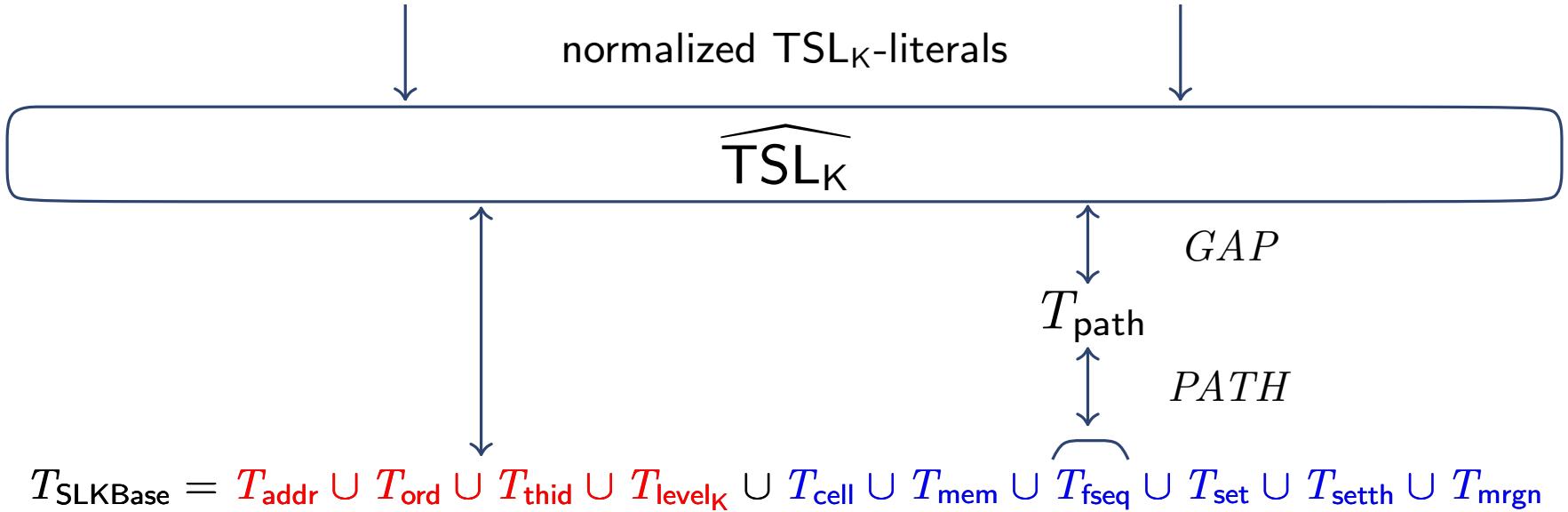
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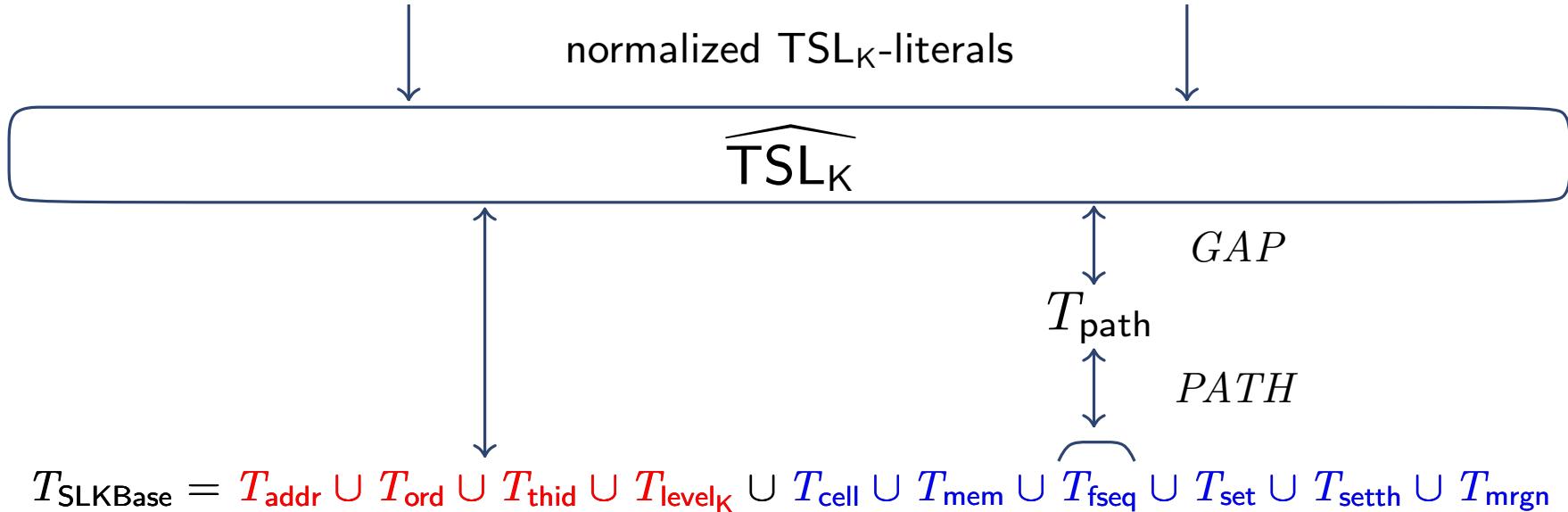
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- ▶ Smooth w.r.t. addr , level_K , elem , ord and thid and hence **polite**

Conclusions

- ▶ We defined **TSL_K**, a theory for concurrent skip lists
- ▶ We proved TSL_K **decidable**, by *Small Model Property*
- ▶ We provide a **combination-based decision procedure** for TSL_K
- ▶ TSL_K can reason about memory, cells, pointers, masked regions, reachability, ordered lists and sublists
- ▶ A step towards the assisted verification of **temporal properties** over **concurrent data-types**: VD + DP
- ▶ **Current and future** work:
 - parametrized verification diagrams, DP for concurrent structures,
 - verification of current implementations, unbounded levels
- ▶ Many possible **collaborations**:
 - DPS as combination, SMTs, implementation