

Decision Procedures for the Temporal Verification of Concurrent Lists

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ICFEM'10, Shanghai, 17 November 2010

Motivation

Why do we need decision procedures for concurrent data structures?

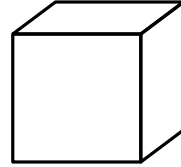
Verification of Concurrent Data-structures

Main Idea

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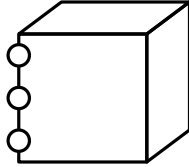
Concurrent DataStructure



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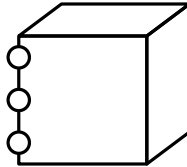
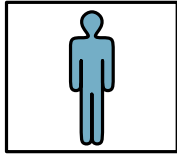
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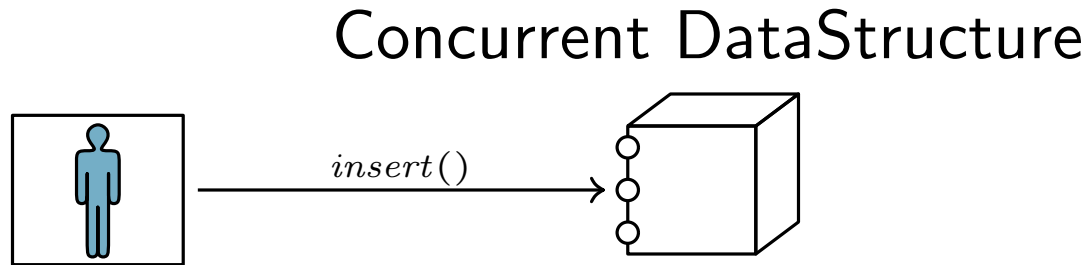
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Most General Client

Verification of Concurrent Data-structures

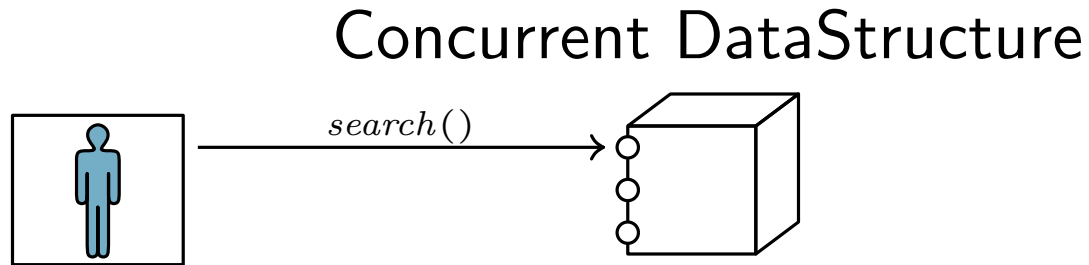
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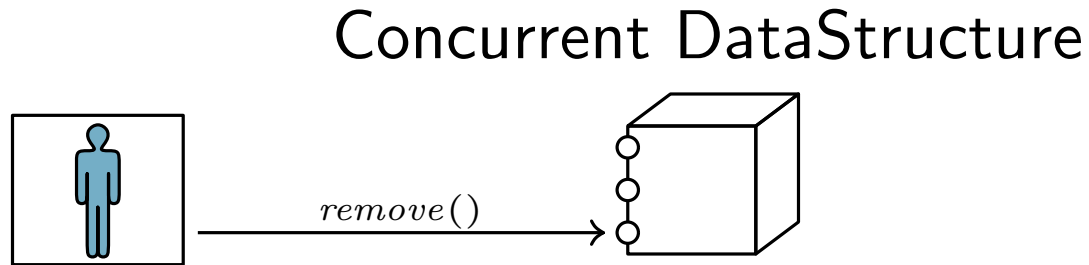
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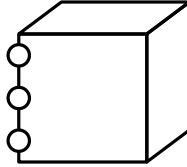
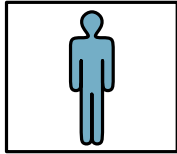


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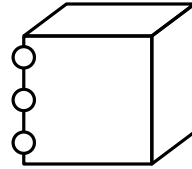


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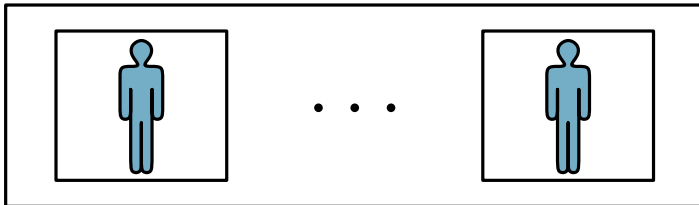
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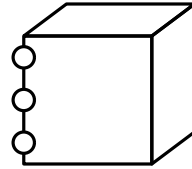
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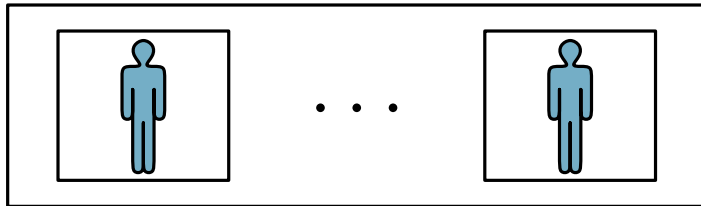
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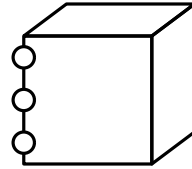


$$P[N] : P(1) || \dots || P(N)$$

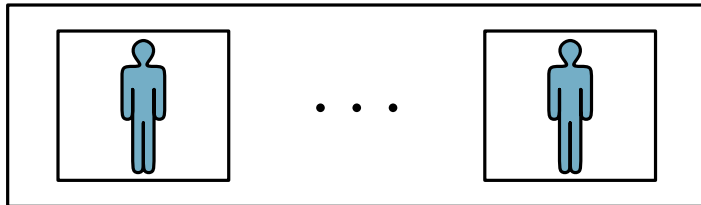
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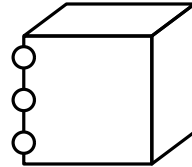
+

ghost variables

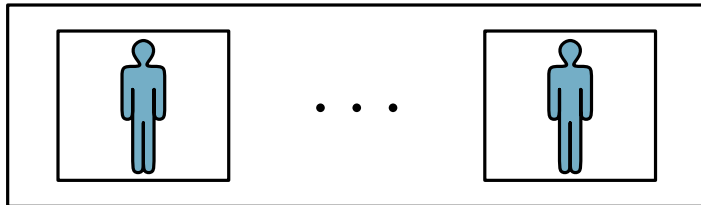
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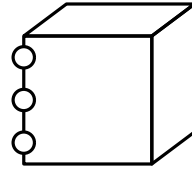
Property

$\varphi^{(k)}$

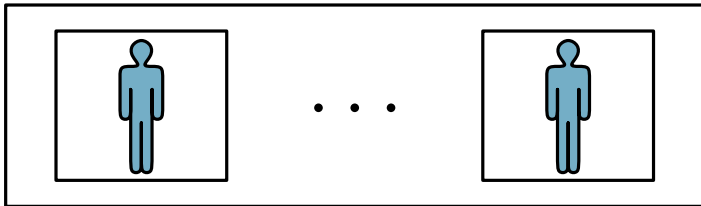
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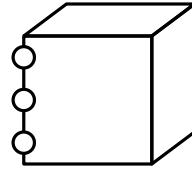
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LTL ($\square, \diamond, \mathcal{U}, \dots$)

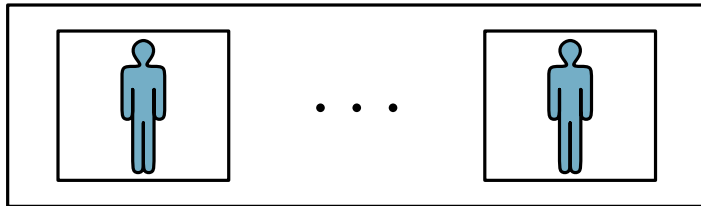
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Diagram

\mathcal{D}

Property

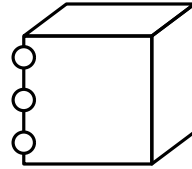
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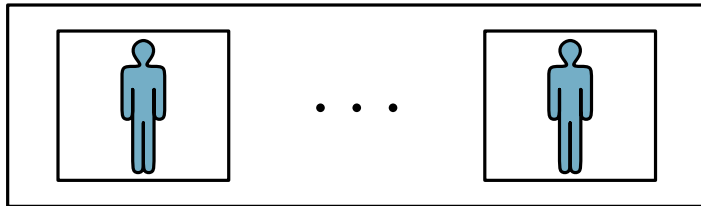
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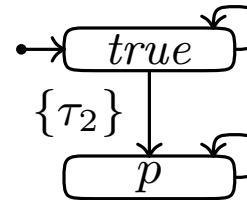


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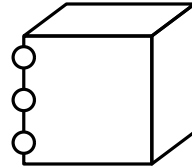
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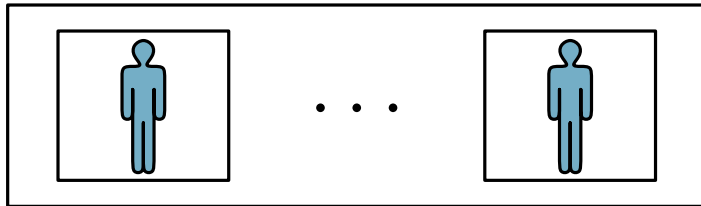
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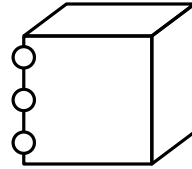
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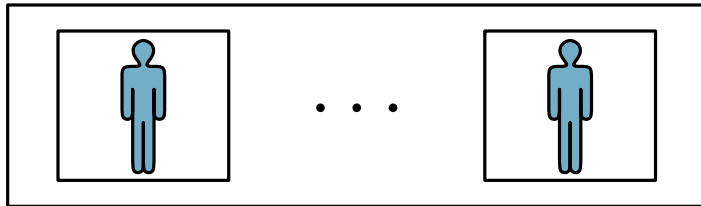
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Diagram

$\models \mathcal{D}$

Verification Conditions:

- ▶ Initiation
- ▶ Consecution
- ▶ Acceptance
- ▶ Fairness

Property

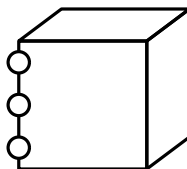
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Satisfaction
(Model Checking)

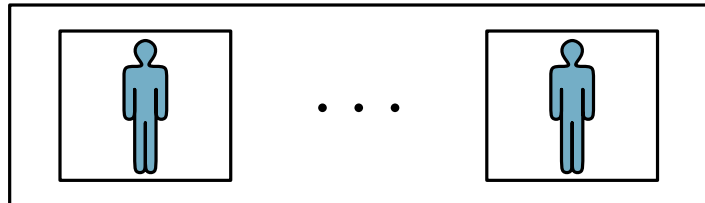
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Property

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Satisfaction
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Decision Procedures
(first order propositional logic)

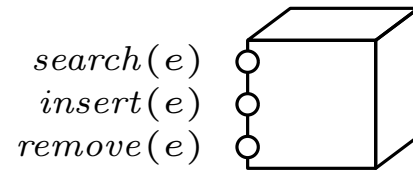
Concurrent Lock-coupling Lists

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- ▶ A concurrent implementation of sets

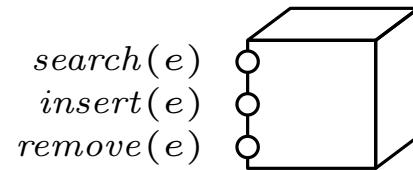
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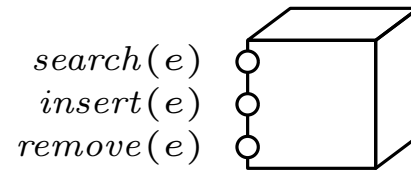


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    Node next;  
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class List {  
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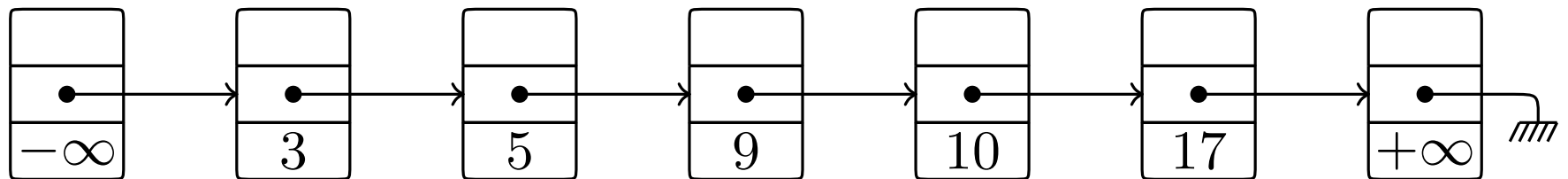

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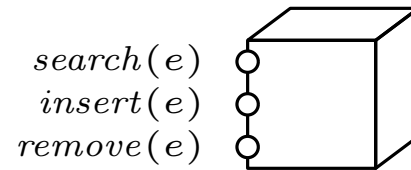
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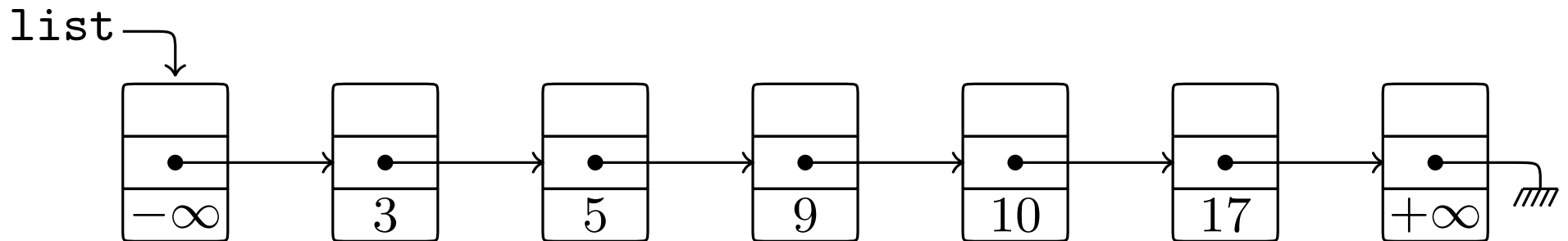
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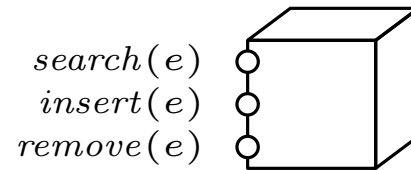
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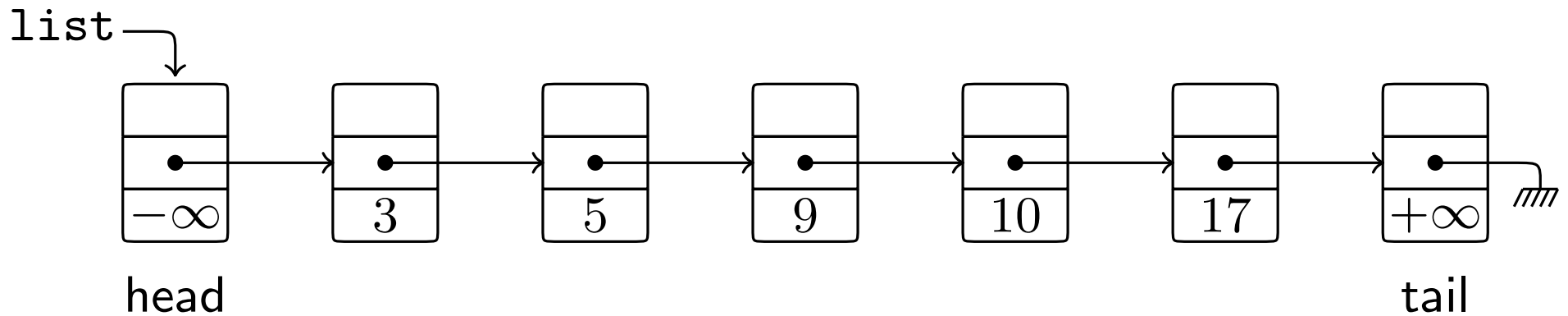
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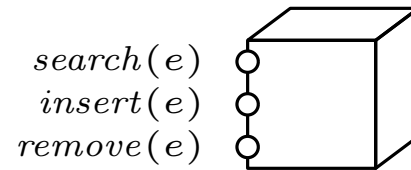
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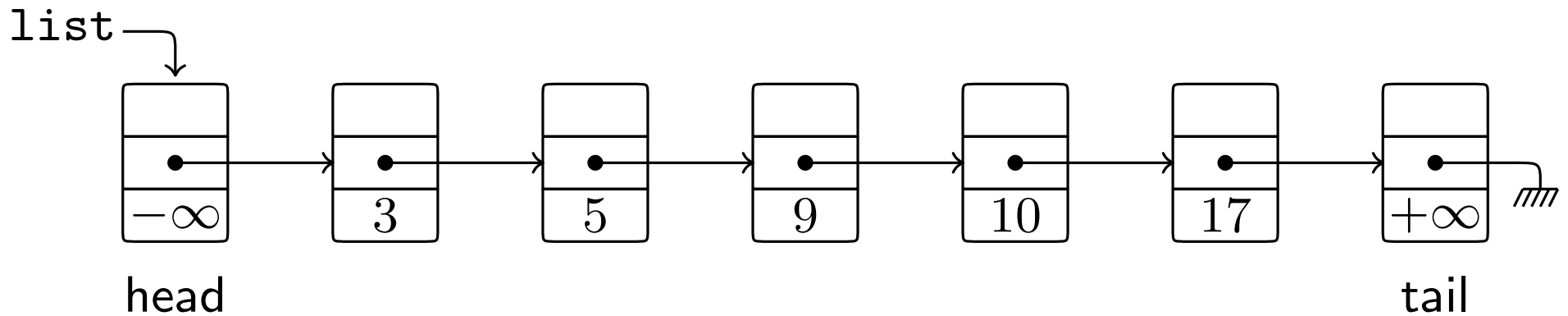
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search(8)

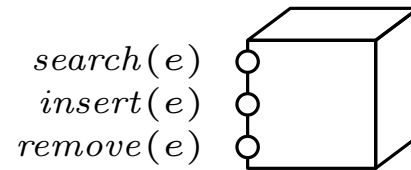
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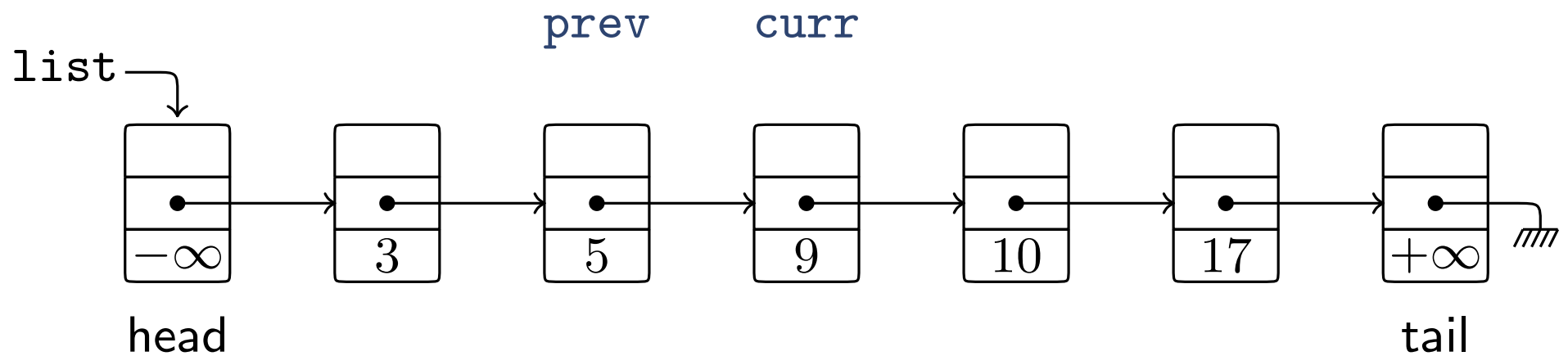
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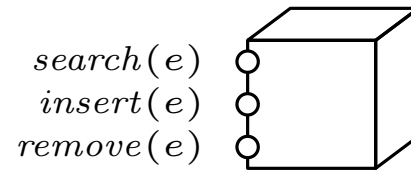
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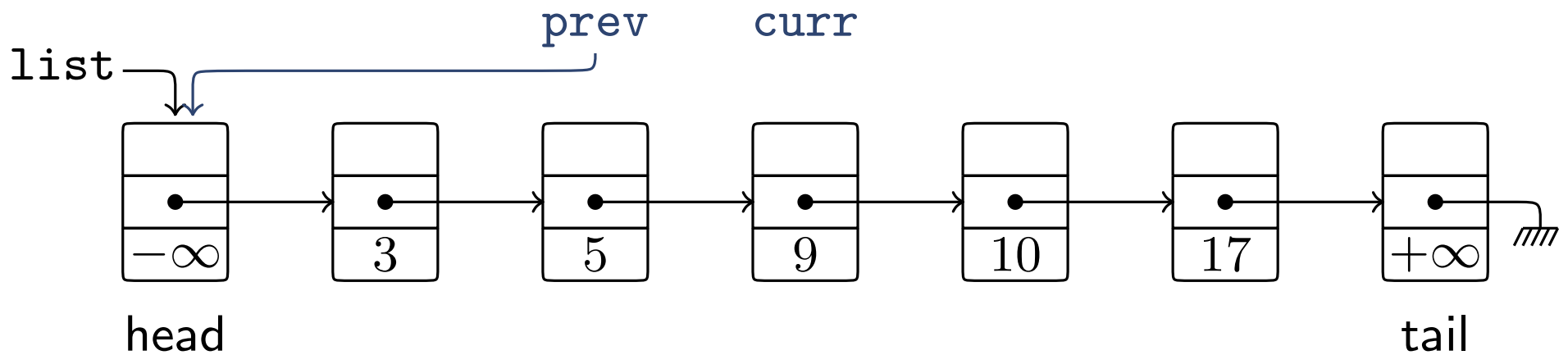
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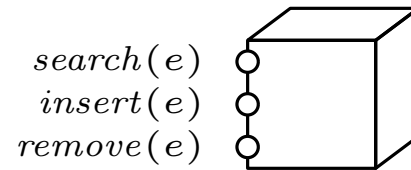
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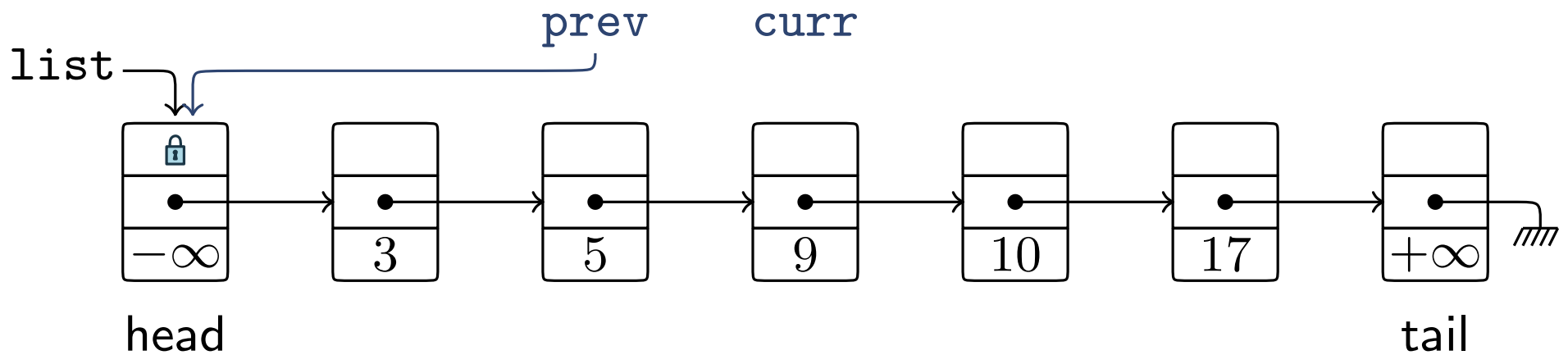
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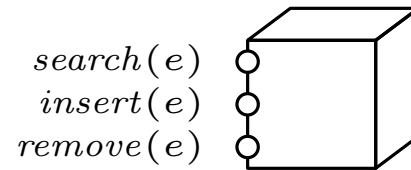
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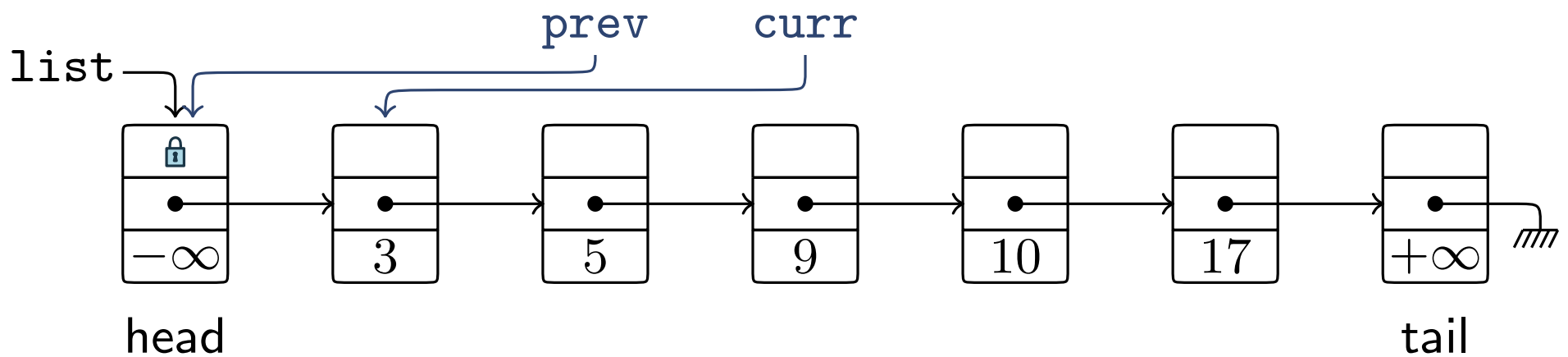
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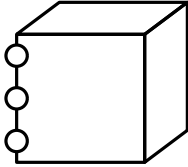
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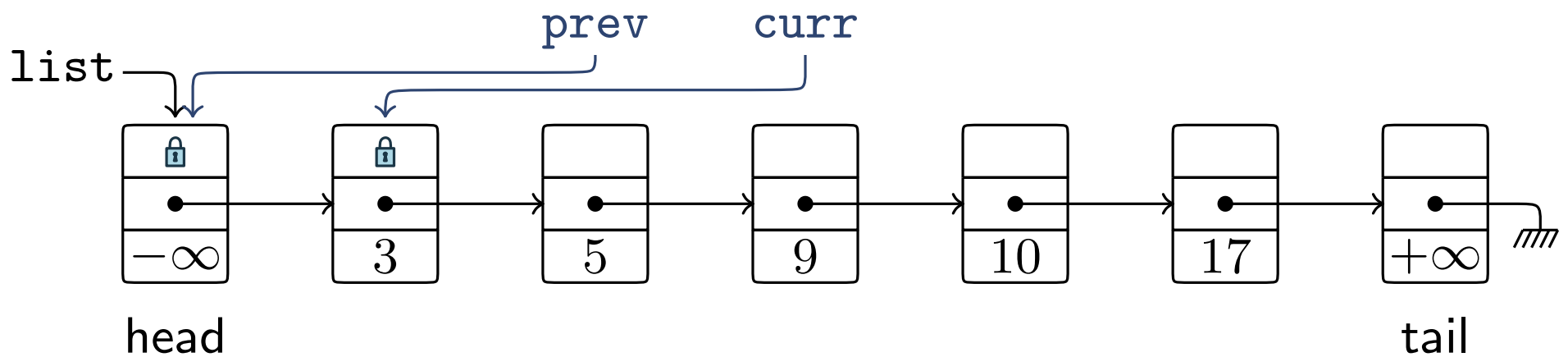
search(e)
insert(e)
remove(e)



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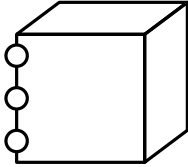
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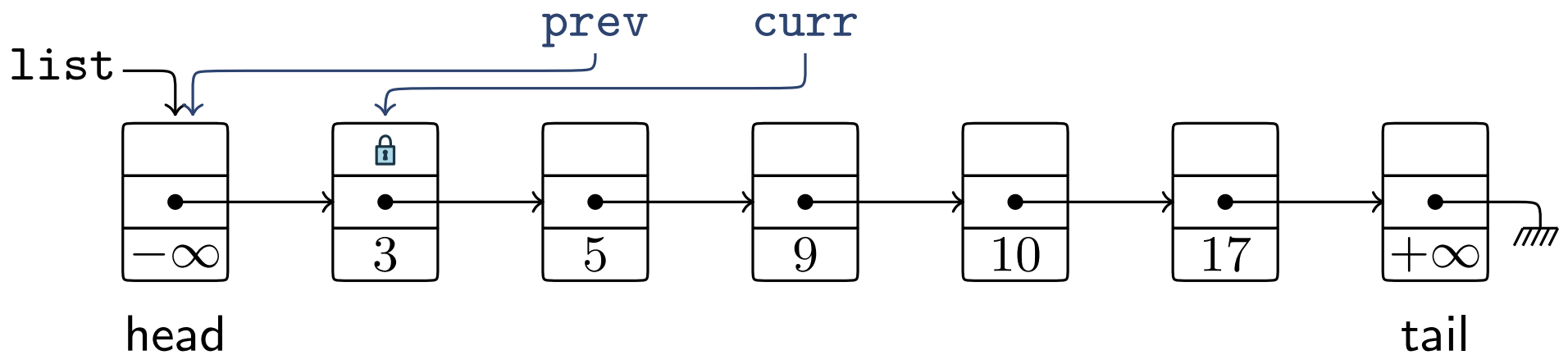
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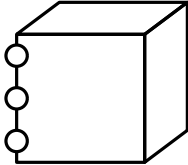
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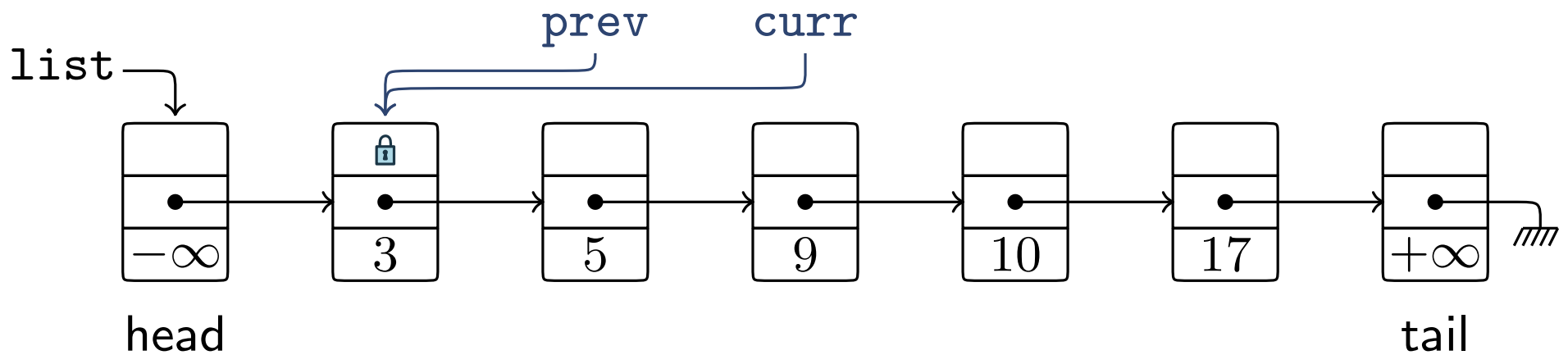
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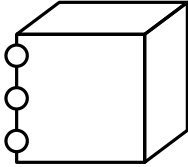
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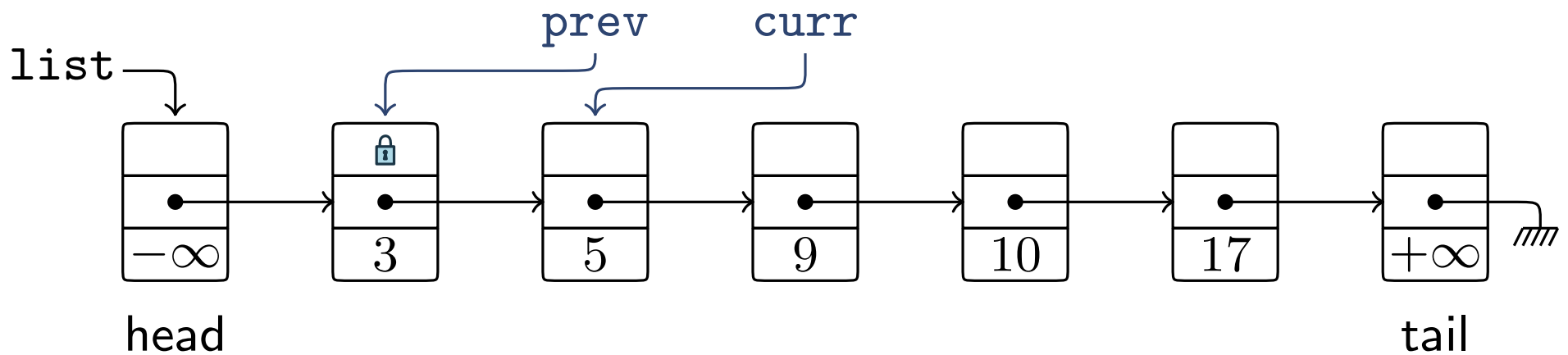
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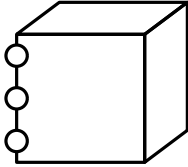
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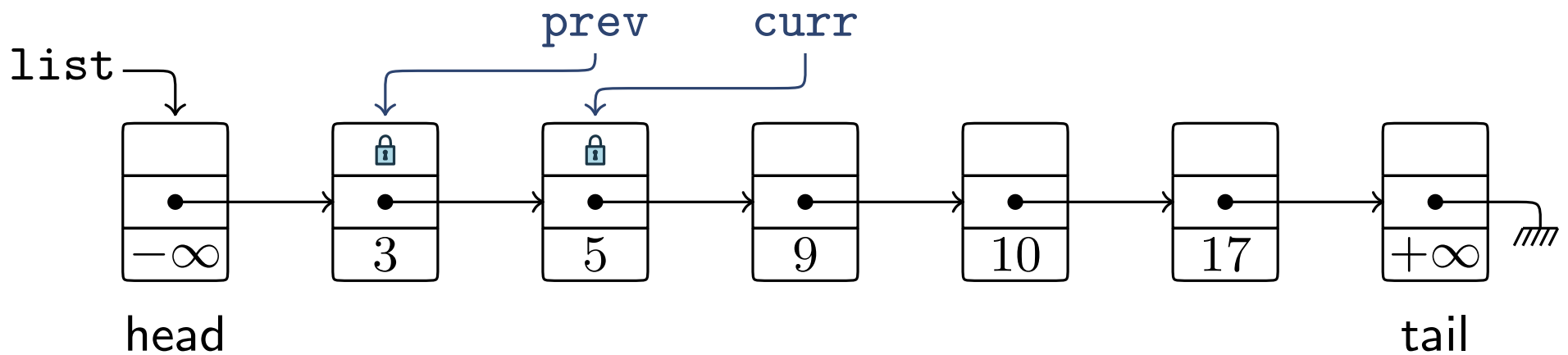
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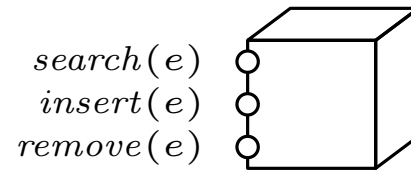
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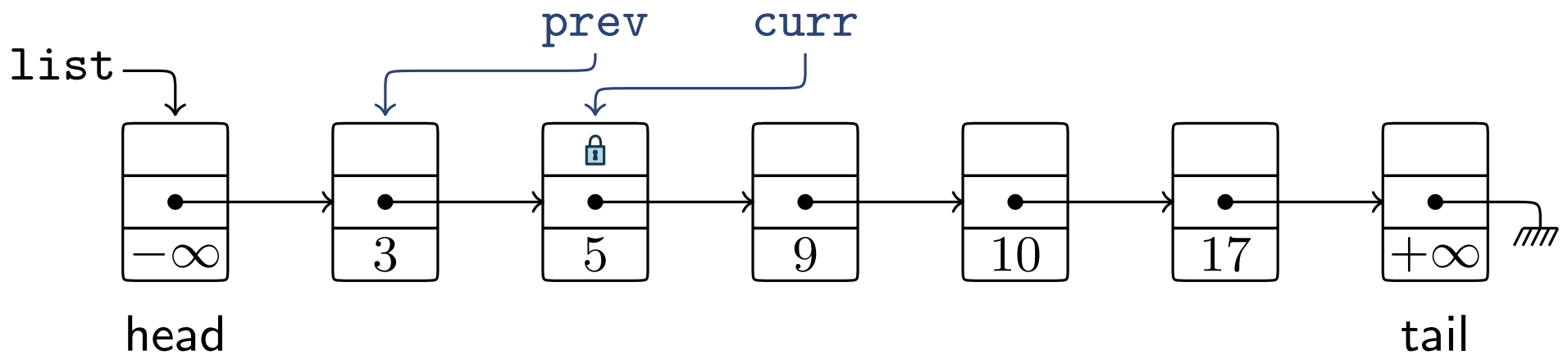
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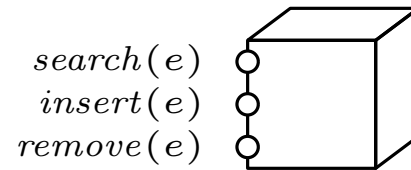
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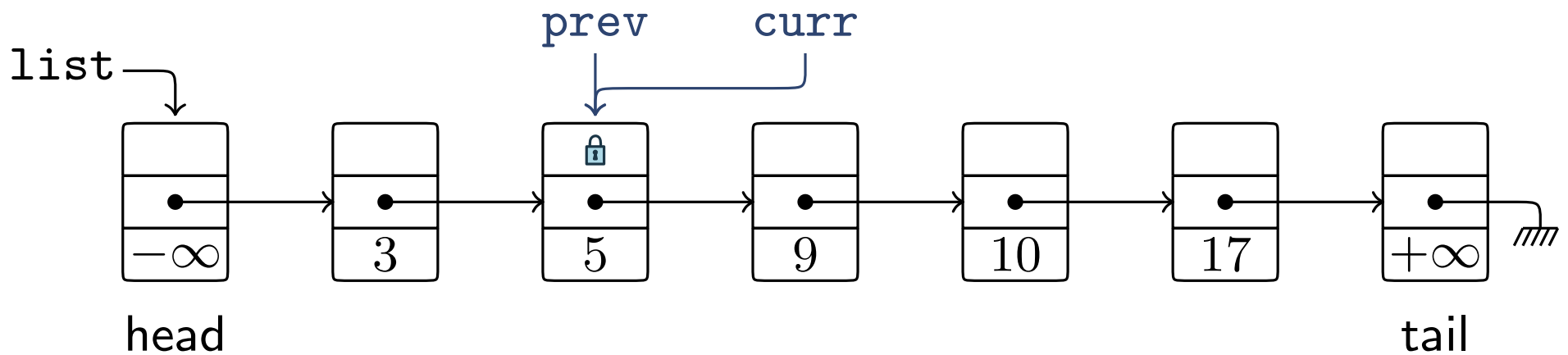
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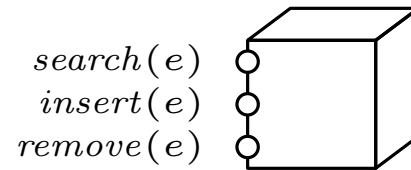
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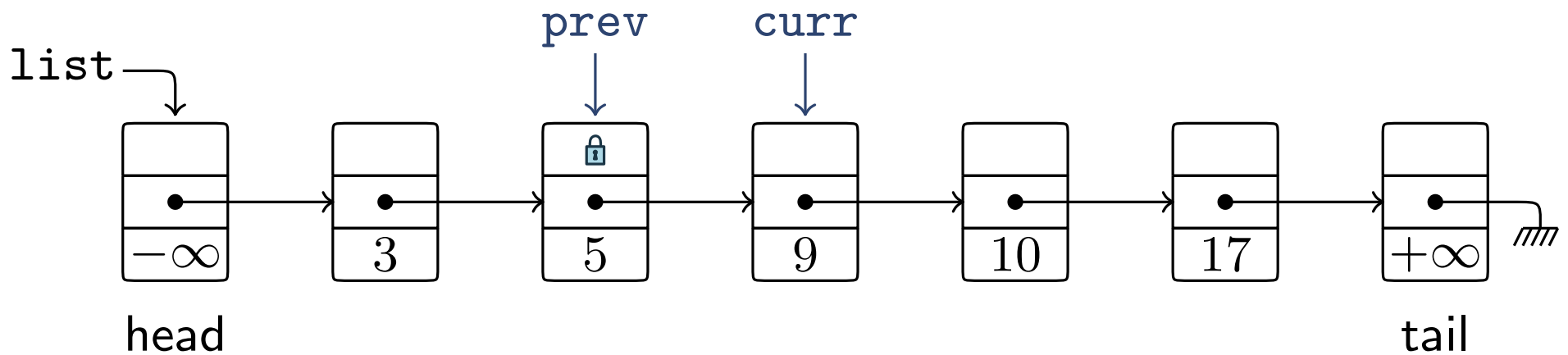
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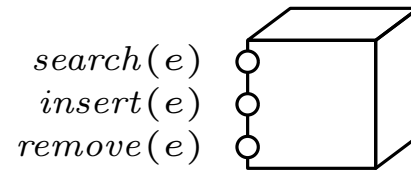
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Concurrent Lock-coupling Lists

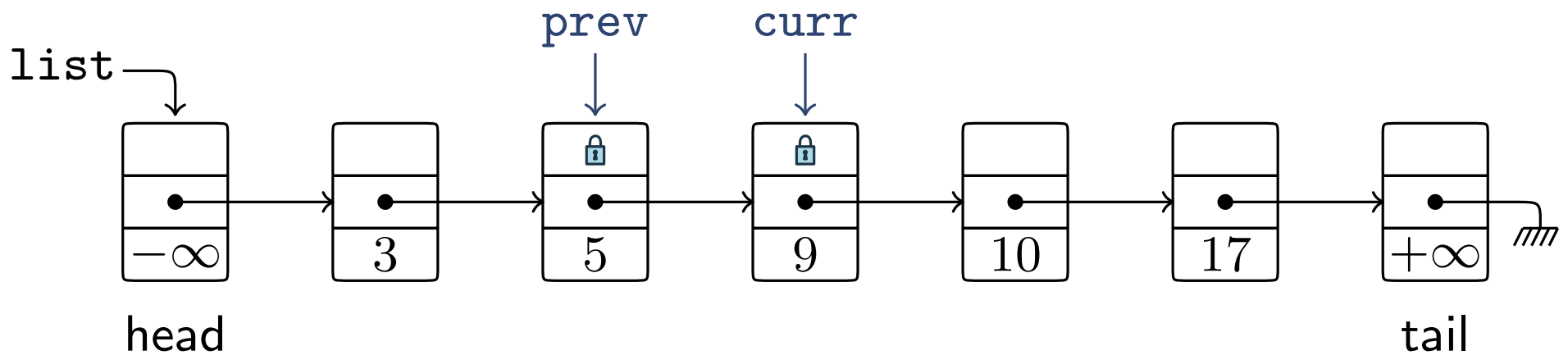
- ▶ A concurrent implementation of sets



search(8)

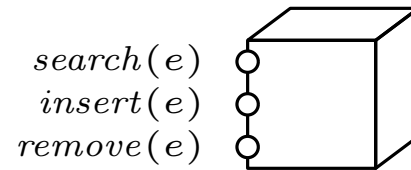
```
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    Lock lock;  
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```

```
class List {  
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}
```



Concurrent Lock-coupling Lists

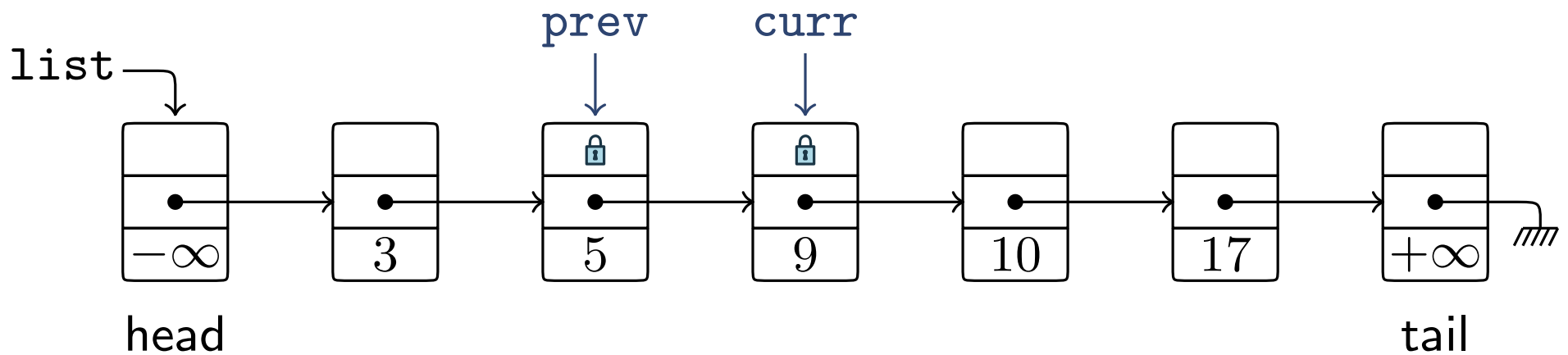
- ▶ A concurrent implementation of sets



insert(8)

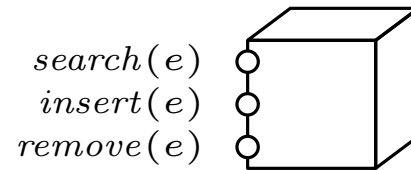
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Concurrent Lock-coupling Lists

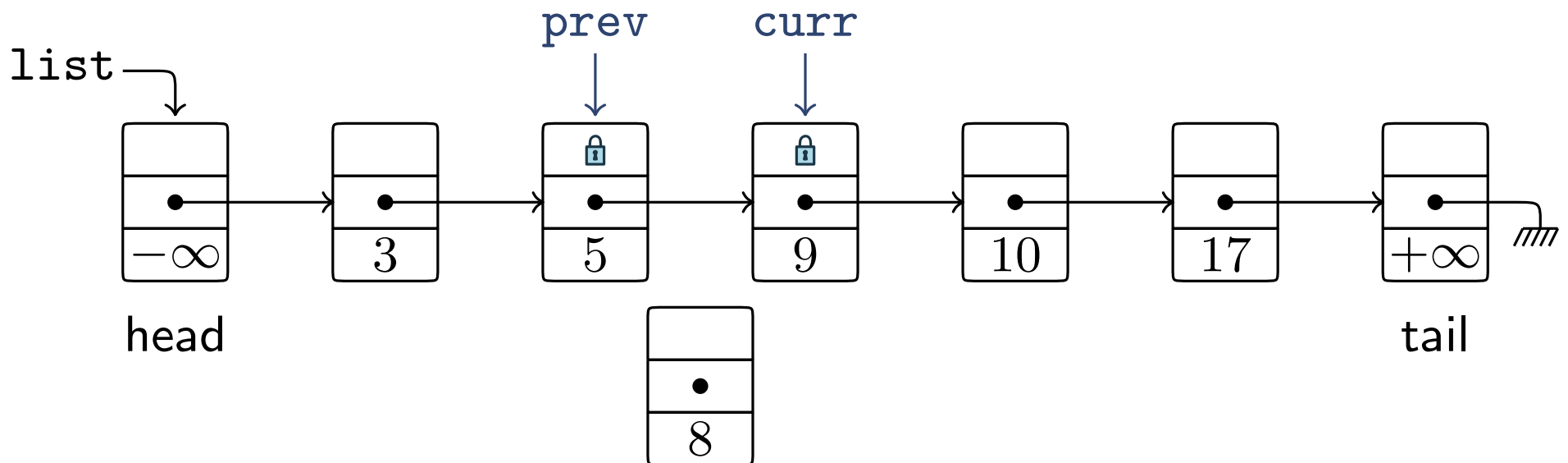
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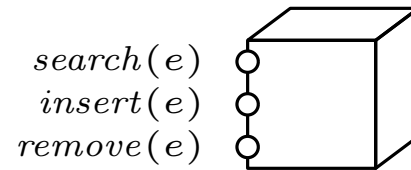
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Concurrent Lock-coupling Lists

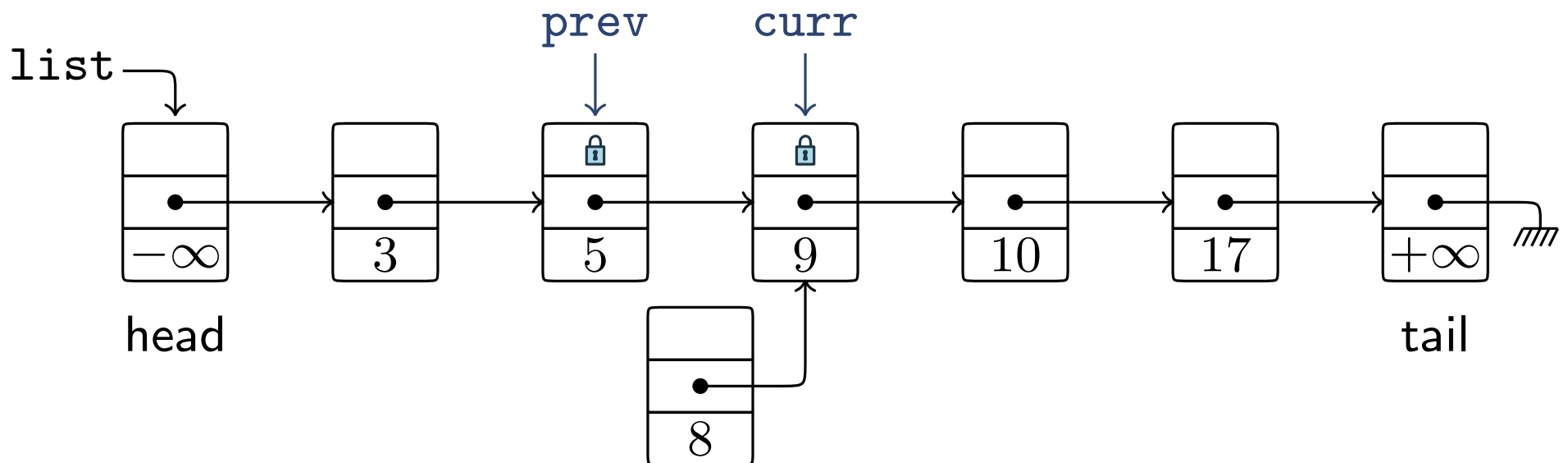
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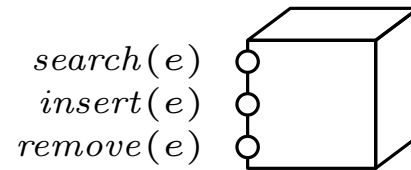
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Concurrent Lock-coupling Lists

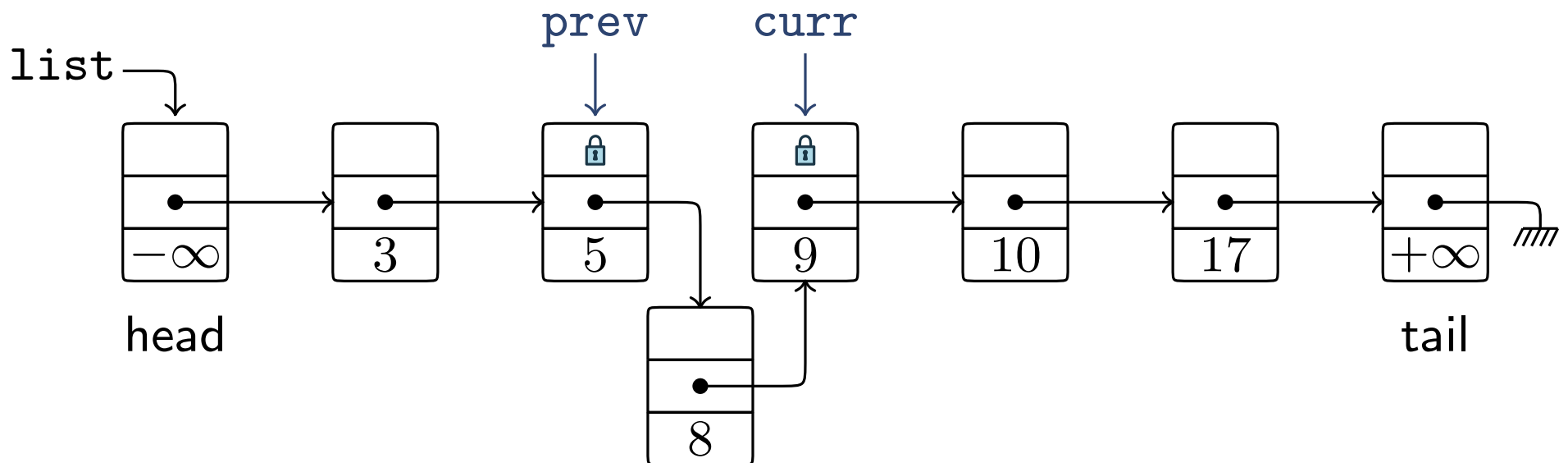
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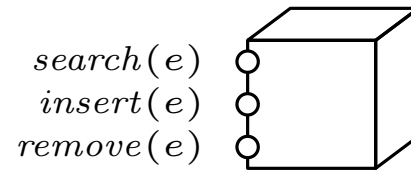
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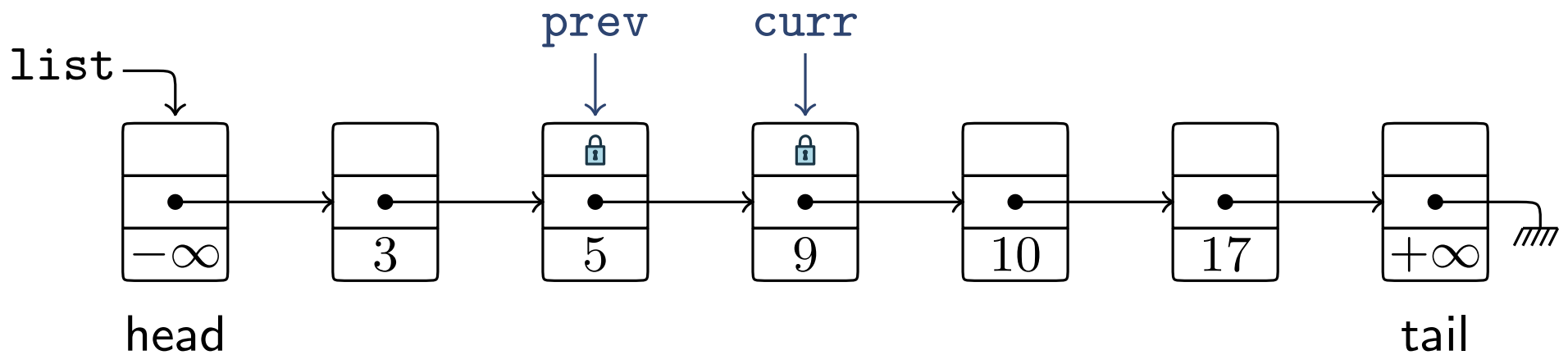


Concurrent Lock-coupling Lists

- ▶ A concurrent implementation of sets

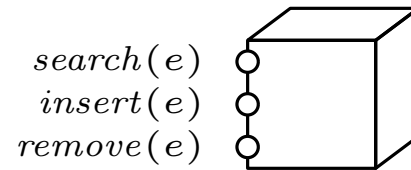


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Concurrent Lock-coupling Lists

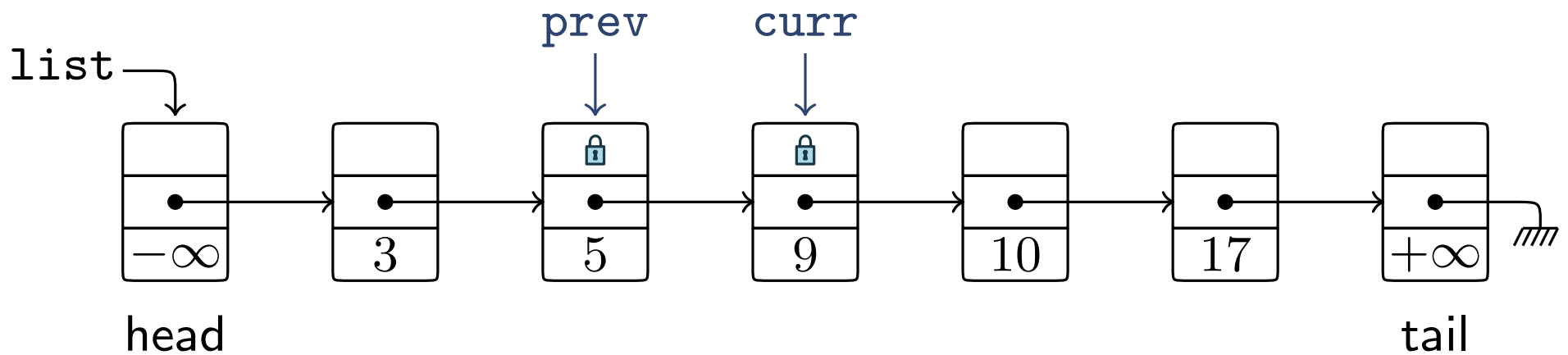
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remove(9)

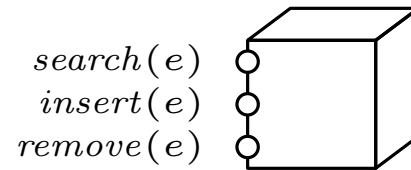
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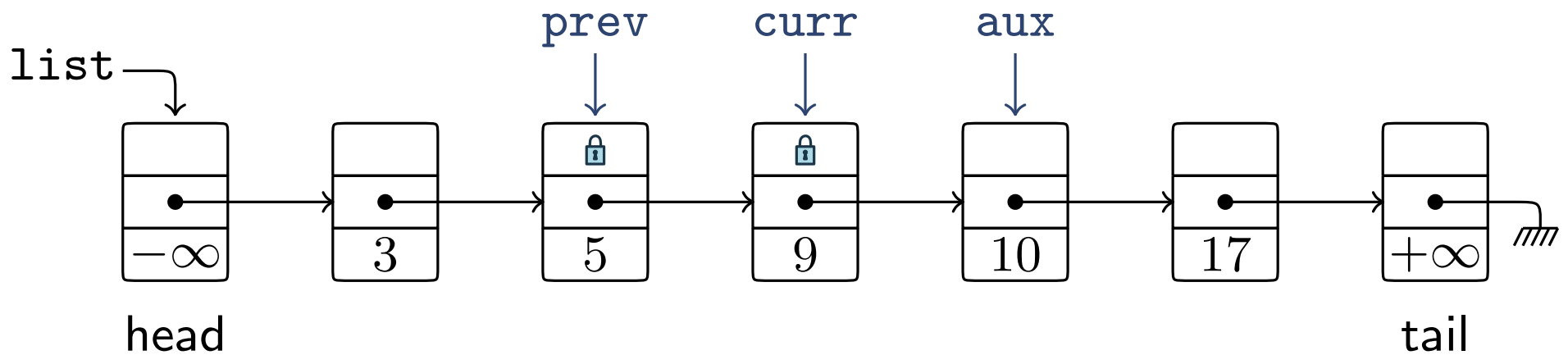
Concurrent Lock-coupling Lists

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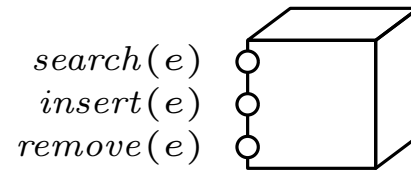
remove(9)

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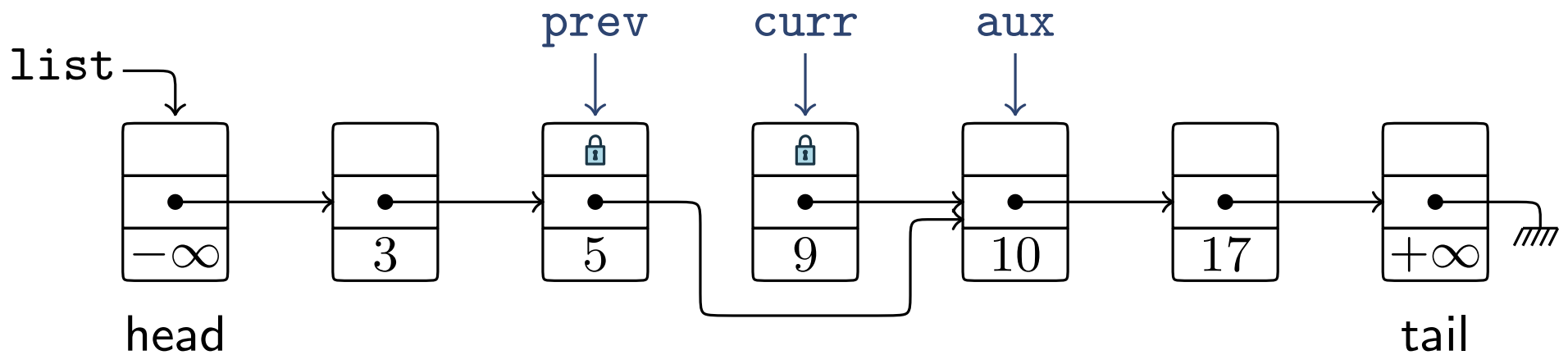
Concurrent Lock-coupling Lists

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remove(9)

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    Node list;  
}
```



Verification Diagram for "Last Terminates"

Verification Diagram for "Last Terminates"

$$\mathcal{S}[N] \models \psi^{(k)}$$

Verification Diagram for "Last Terminates"

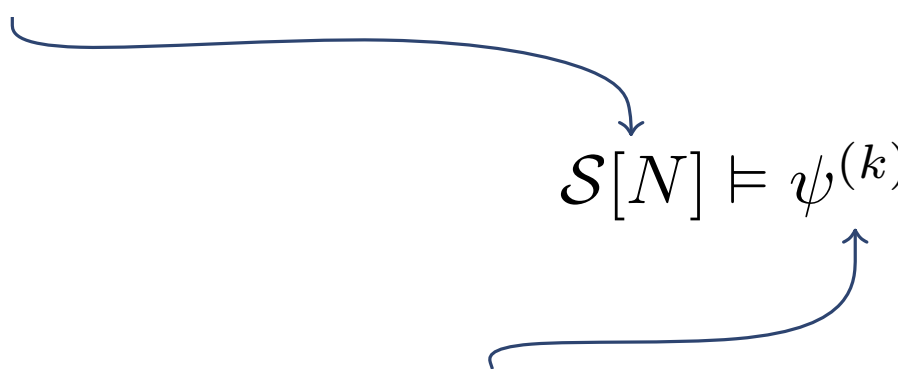
Concurrent execution of N "most general clients"



$$\mathcal{S}[N] \models \psi^{(k)}$$

Verification Diagram for "Last Terminates"

Concurrent execution of N "most general clients"


$$\mathcal{S}[N] \models \psi^{(k)}$$

Thread k holding the rightmost lock, terminates

Verification Diagram for "Last Terminates"

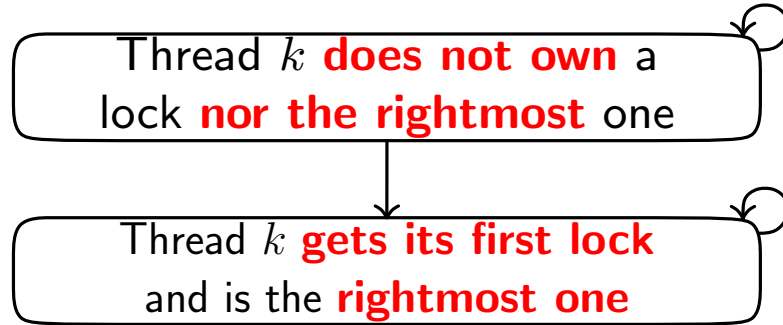
$$\mathcal{S}[N] \models \psi^{(k)}$$

Verification Diagram for "Last Terminates"

Thread k **does not own** a
lock **nor the rightmost** one

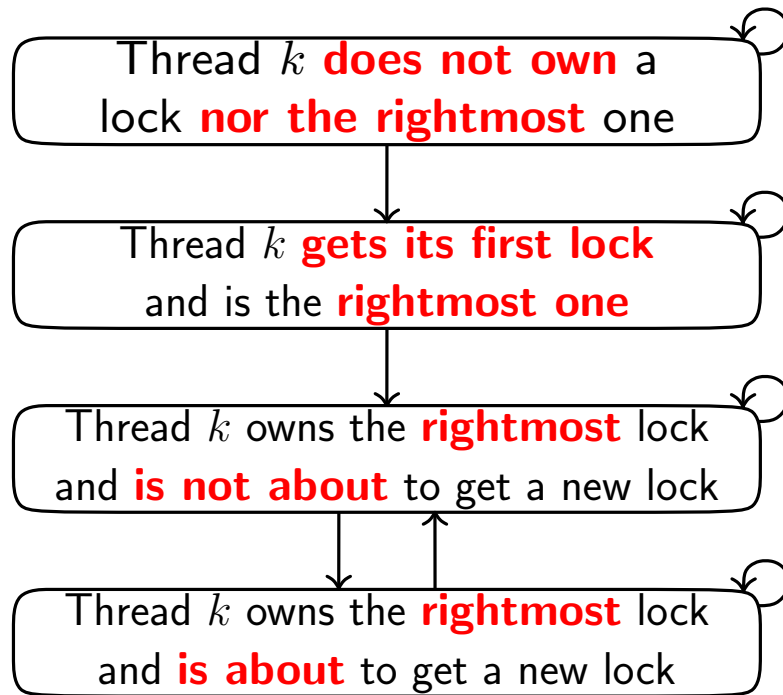
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Verification Diagram for "Last Terminates"



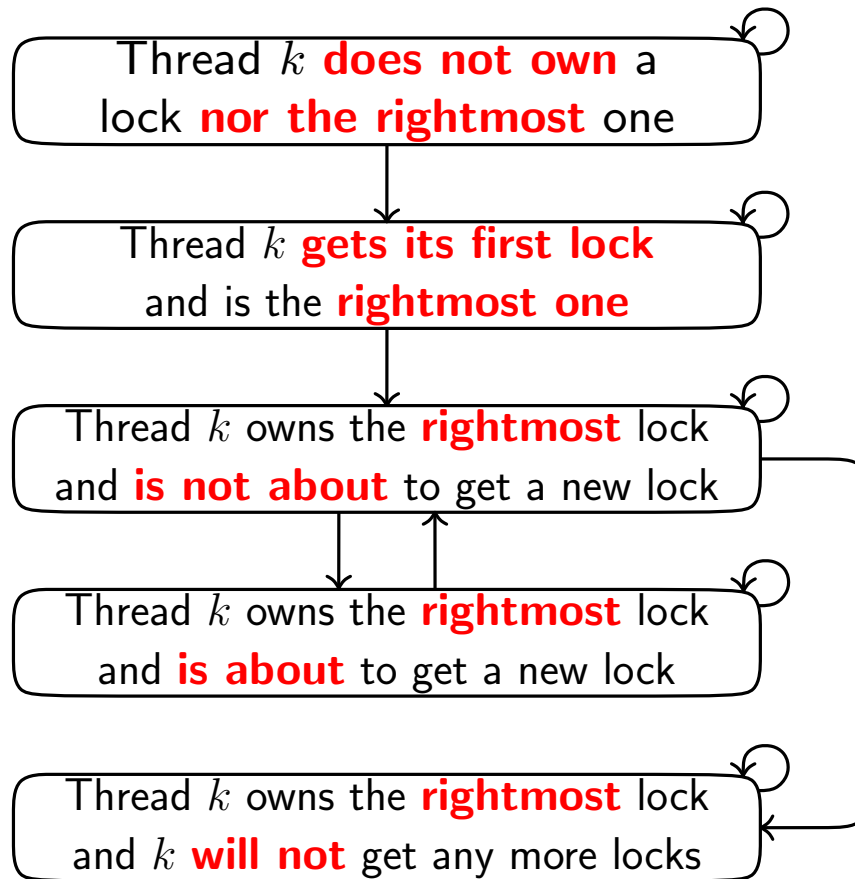
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Verification Diagram for "Last Terminates"



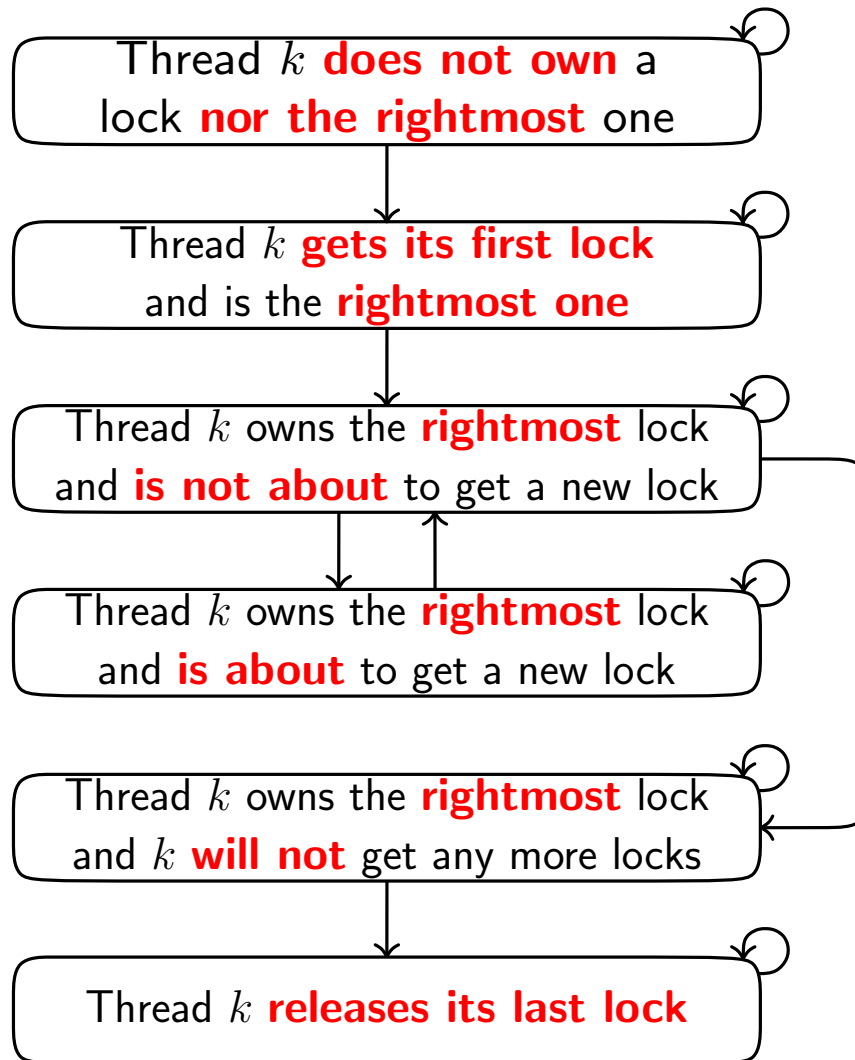
$$\mathcal{S}[N] \models \psi^{(k)}$$

Verification Diagram for "Last Terminates"



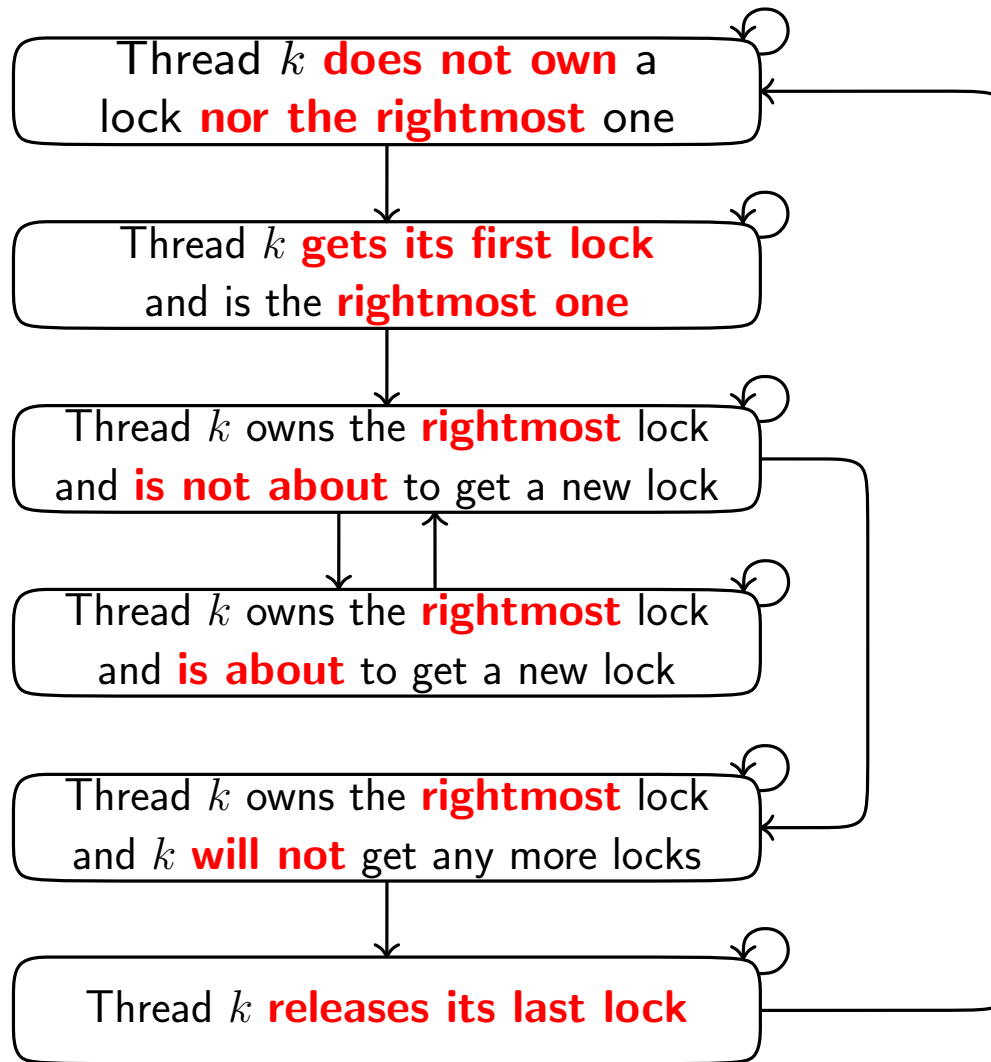
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Verification Diagram for "Last Terminates"



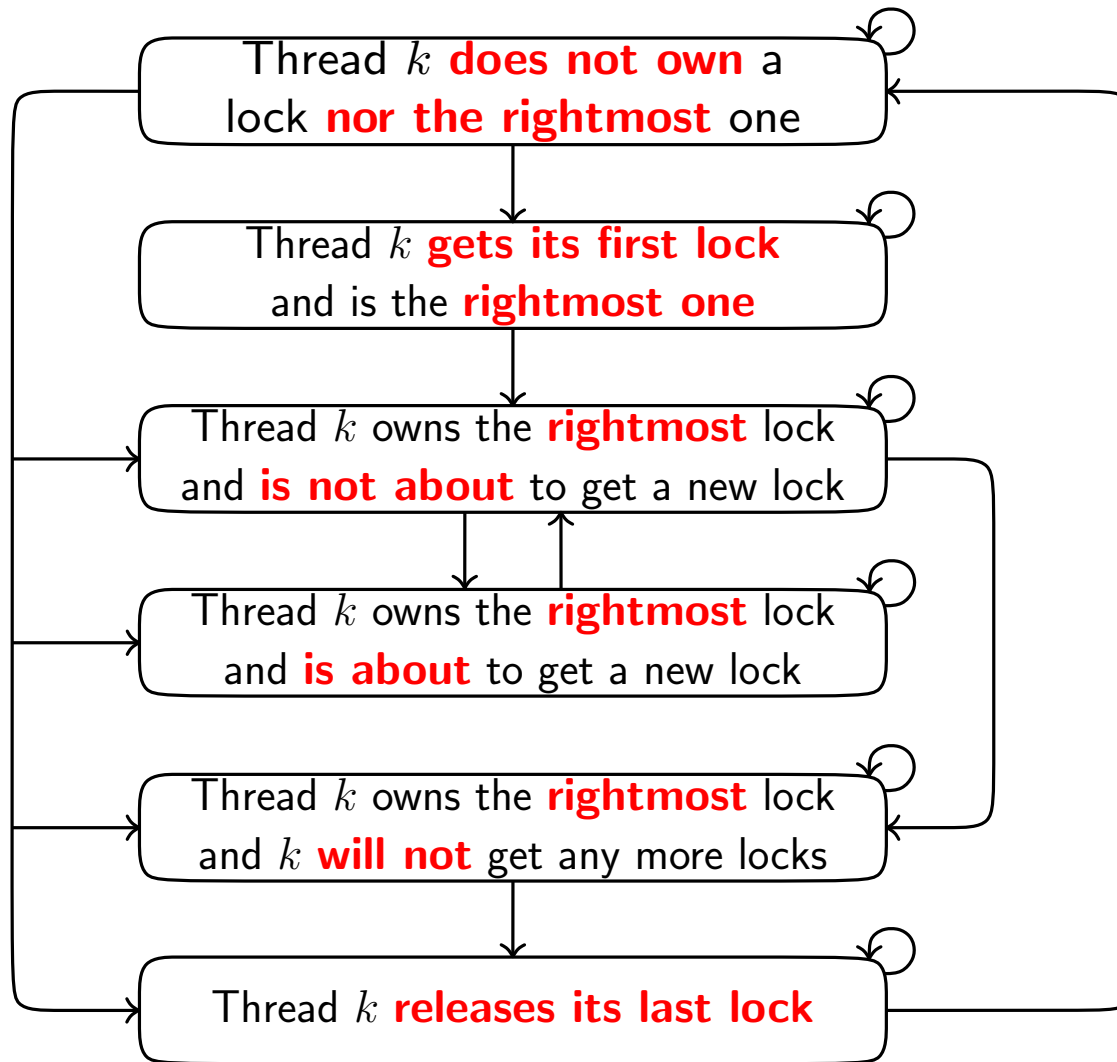
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Verification Diagram for "Last Terminates"



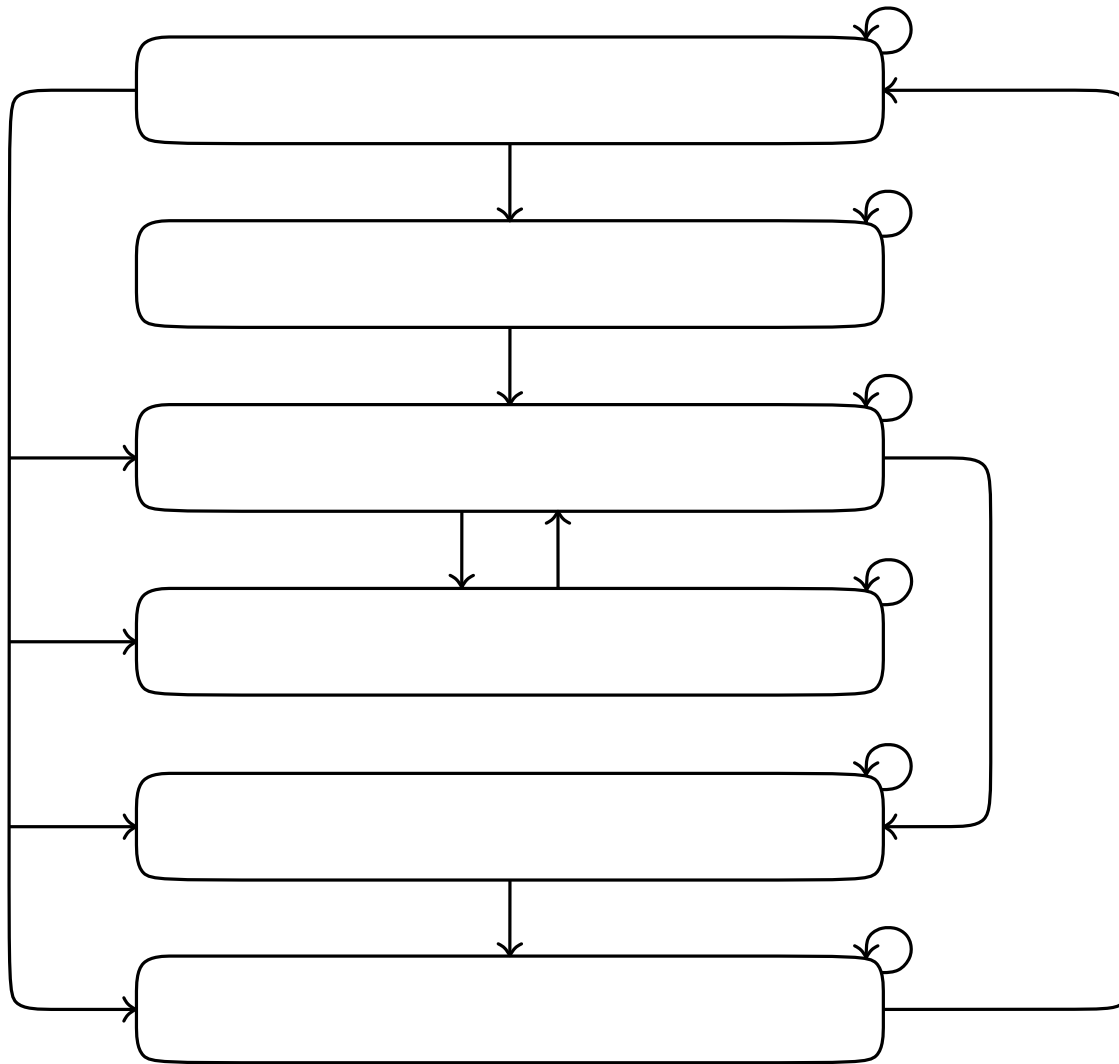
$$\mathcal{S}[N] \models \psi^{(k)}$$

Verification Diagram for "Last Terminates"



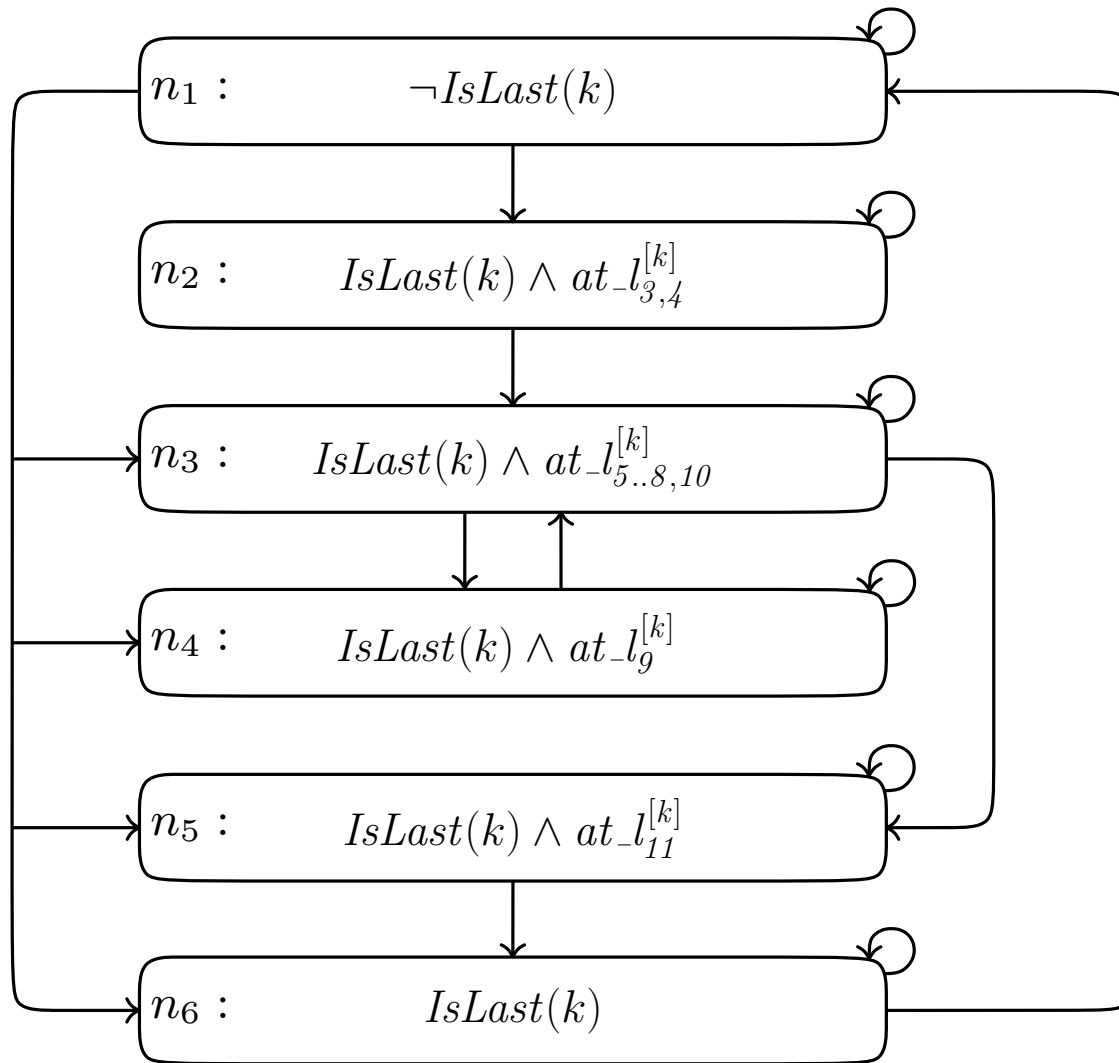
$$\mathcal{S}[N] \models \psi^{(k)}$$

Verification Diagram for "Last Terminates"



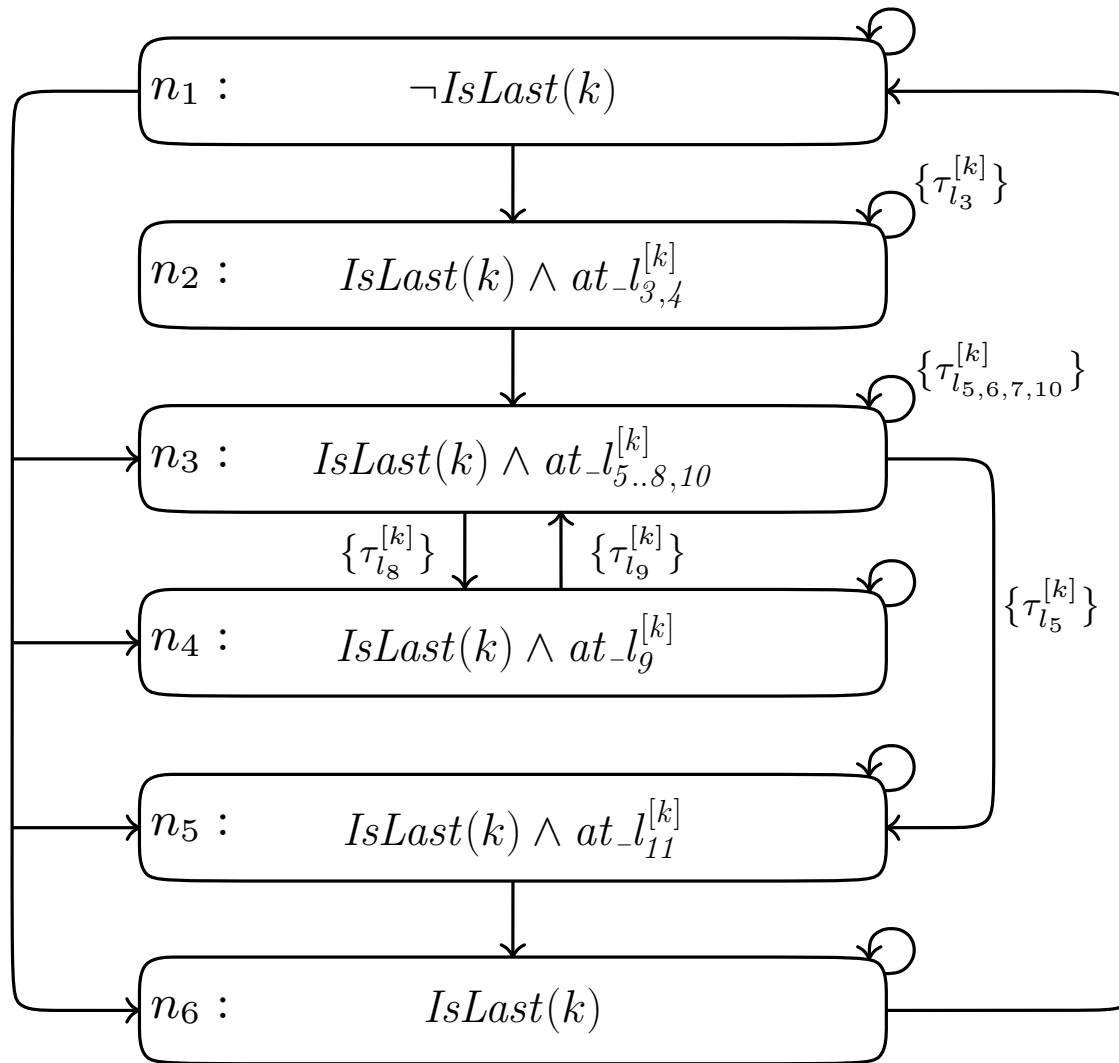
$$\mathcal{S}[N] \models \psi^{(k)}$$

Verification Diagram for "Last Terminates"



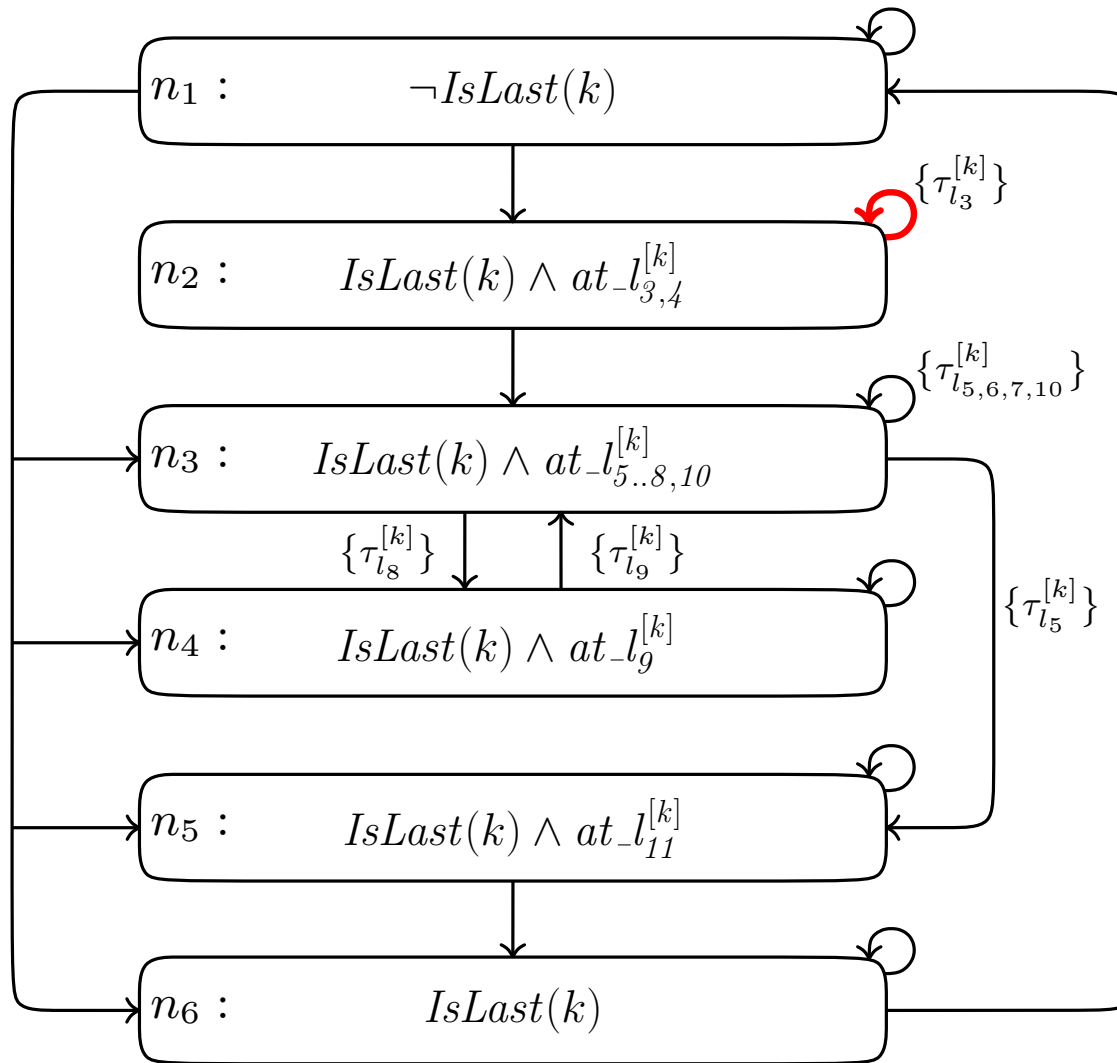
$$\mathcal{S}[N] \models \psi^{(k)}$$

Verification Diagram for "Last Terminates"



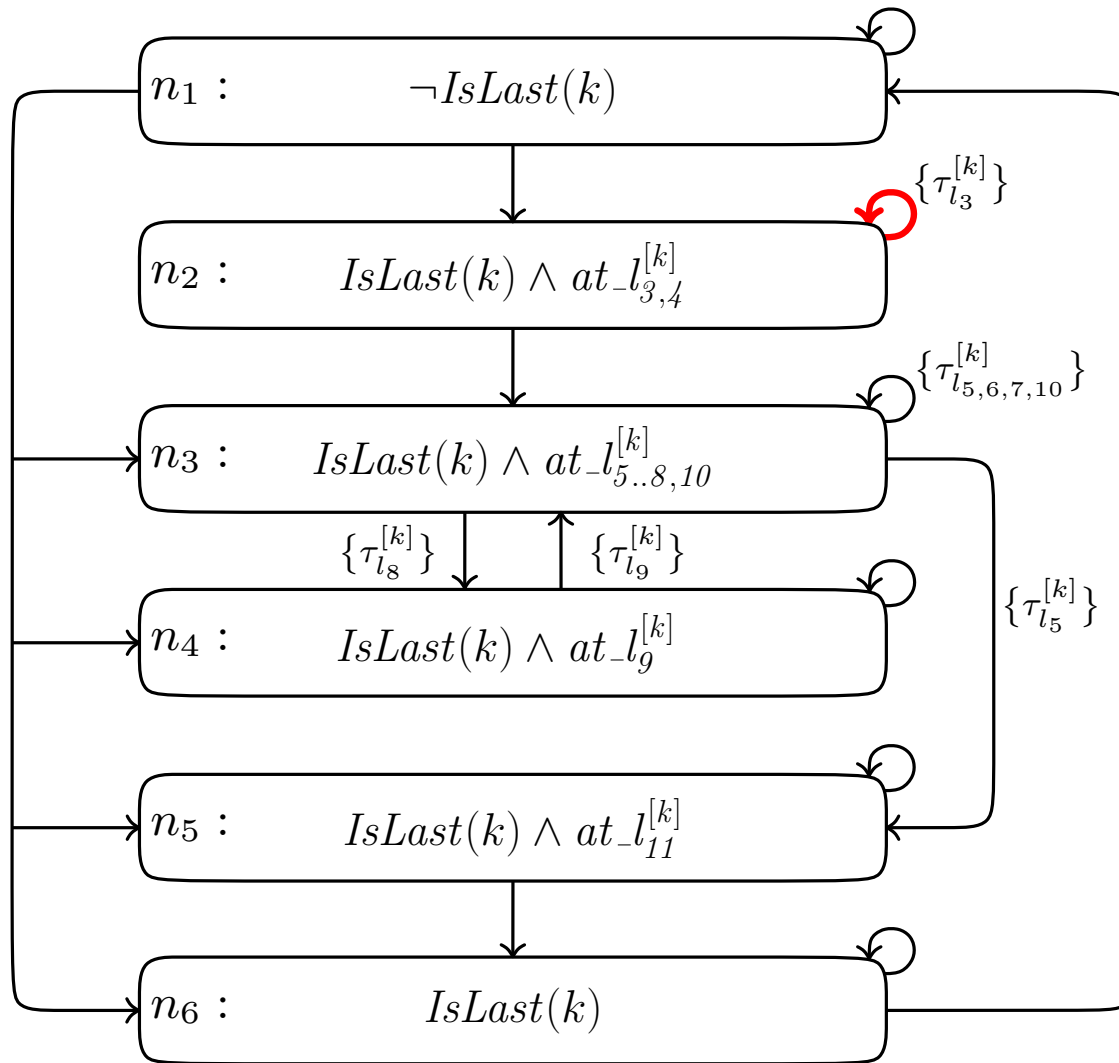
$$\mathcal{S}[N] \models \psi(k)$$

Verification Diagram for "Last Terminates"

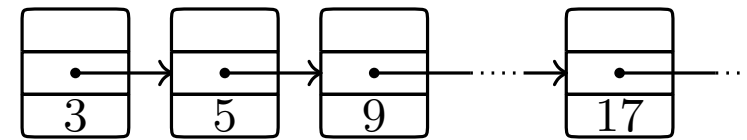


$$\mathcal{S}[N] \models \psi(k)$$

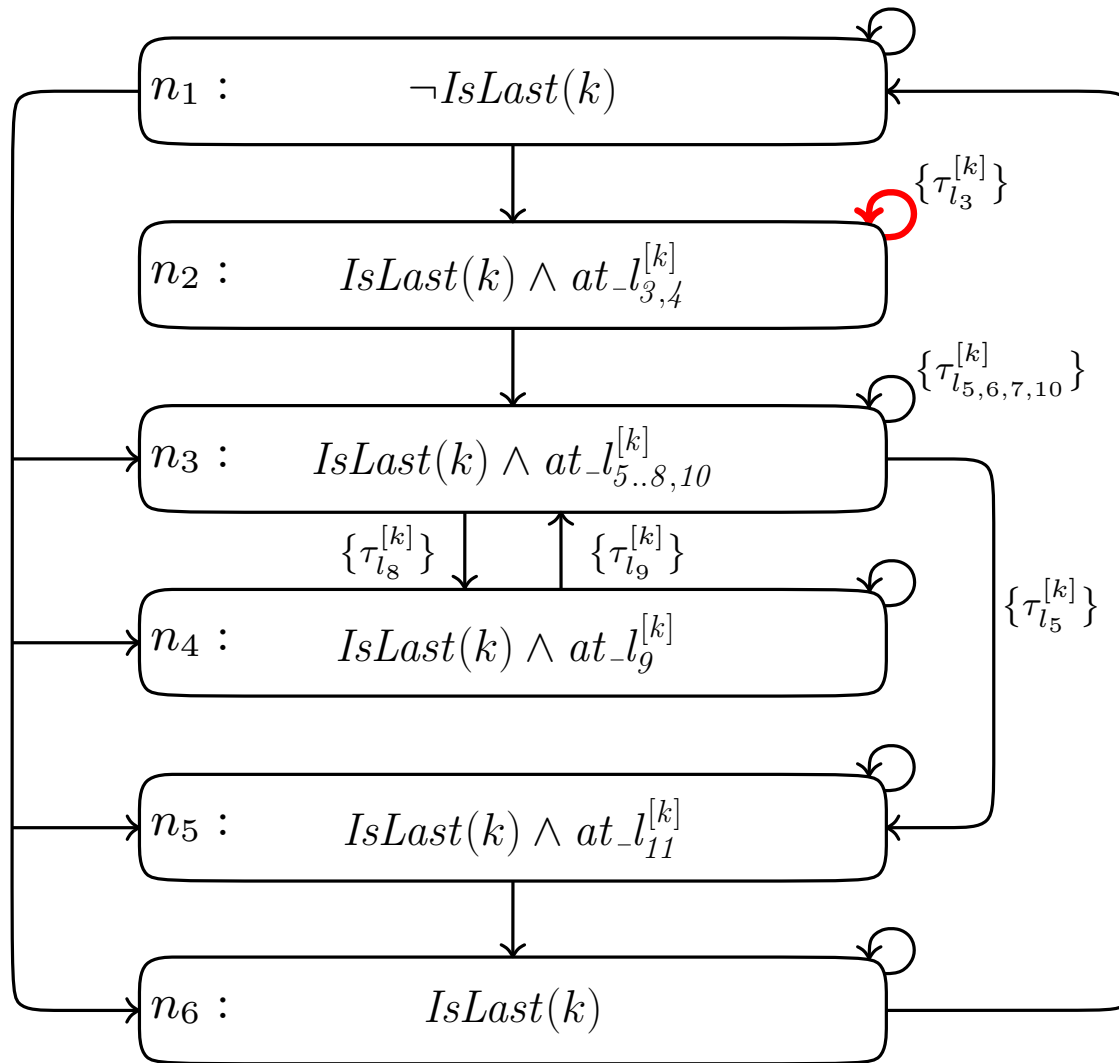
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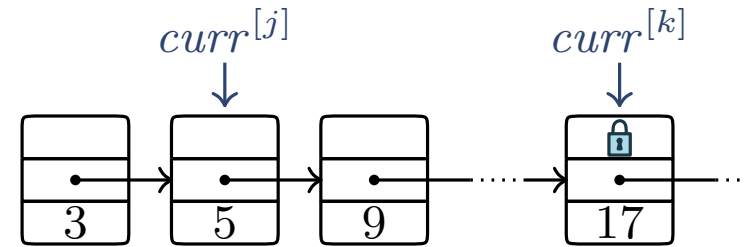
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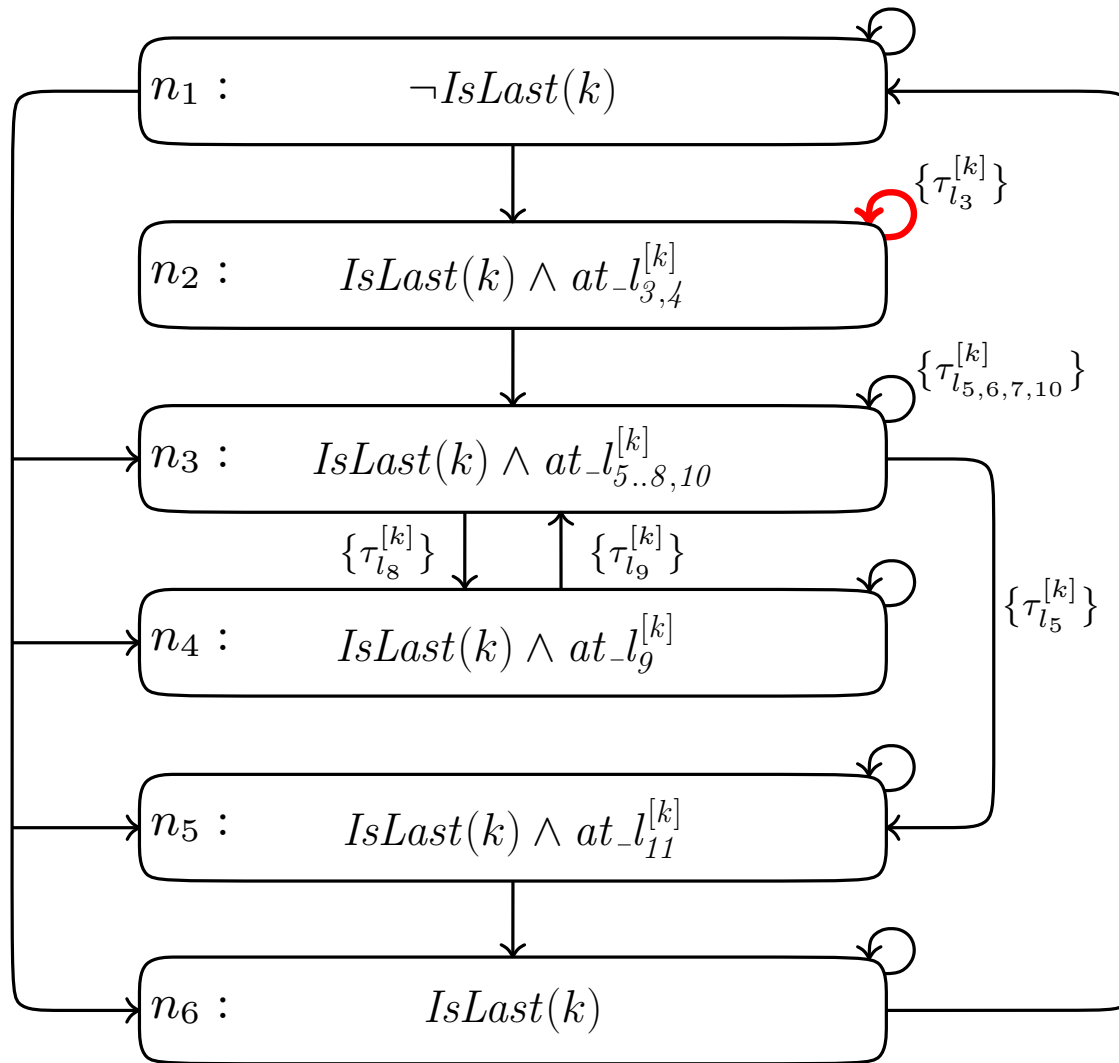
Verification Diagram for "Last Terminates"



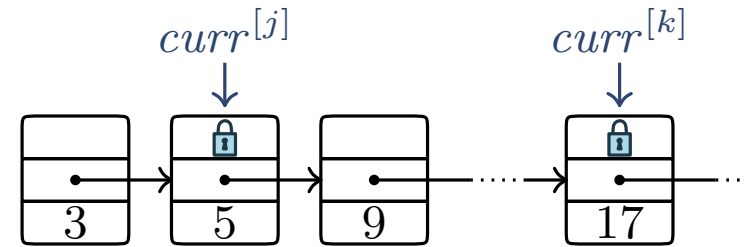
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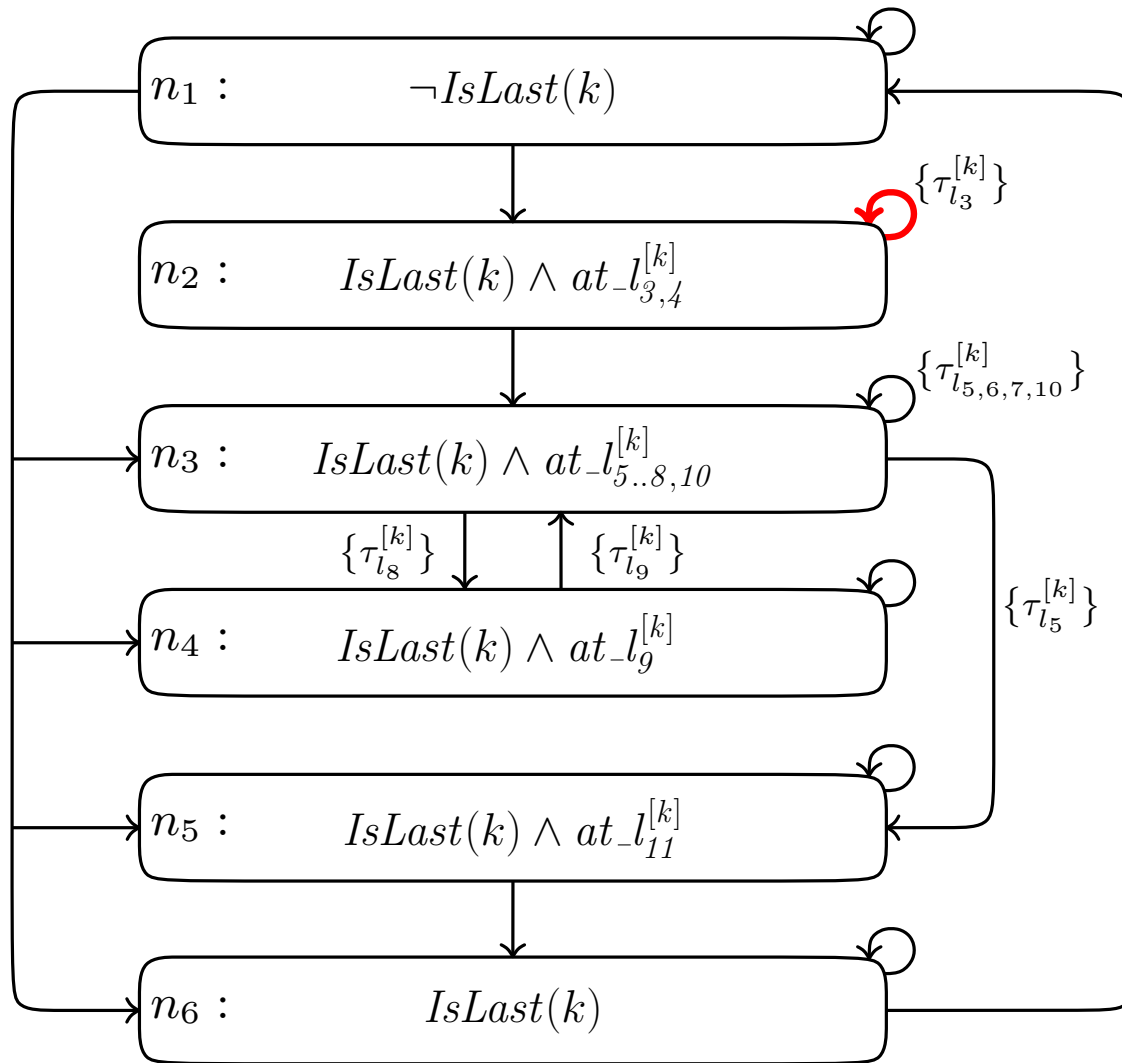
Verification Diagram for "Last Terminates"



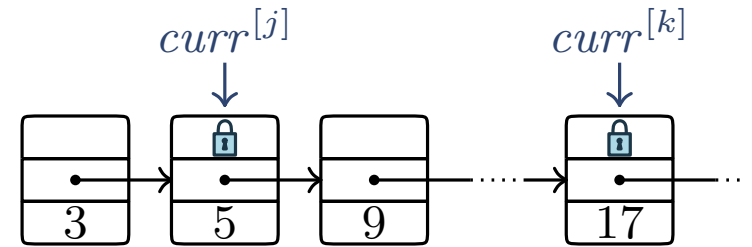
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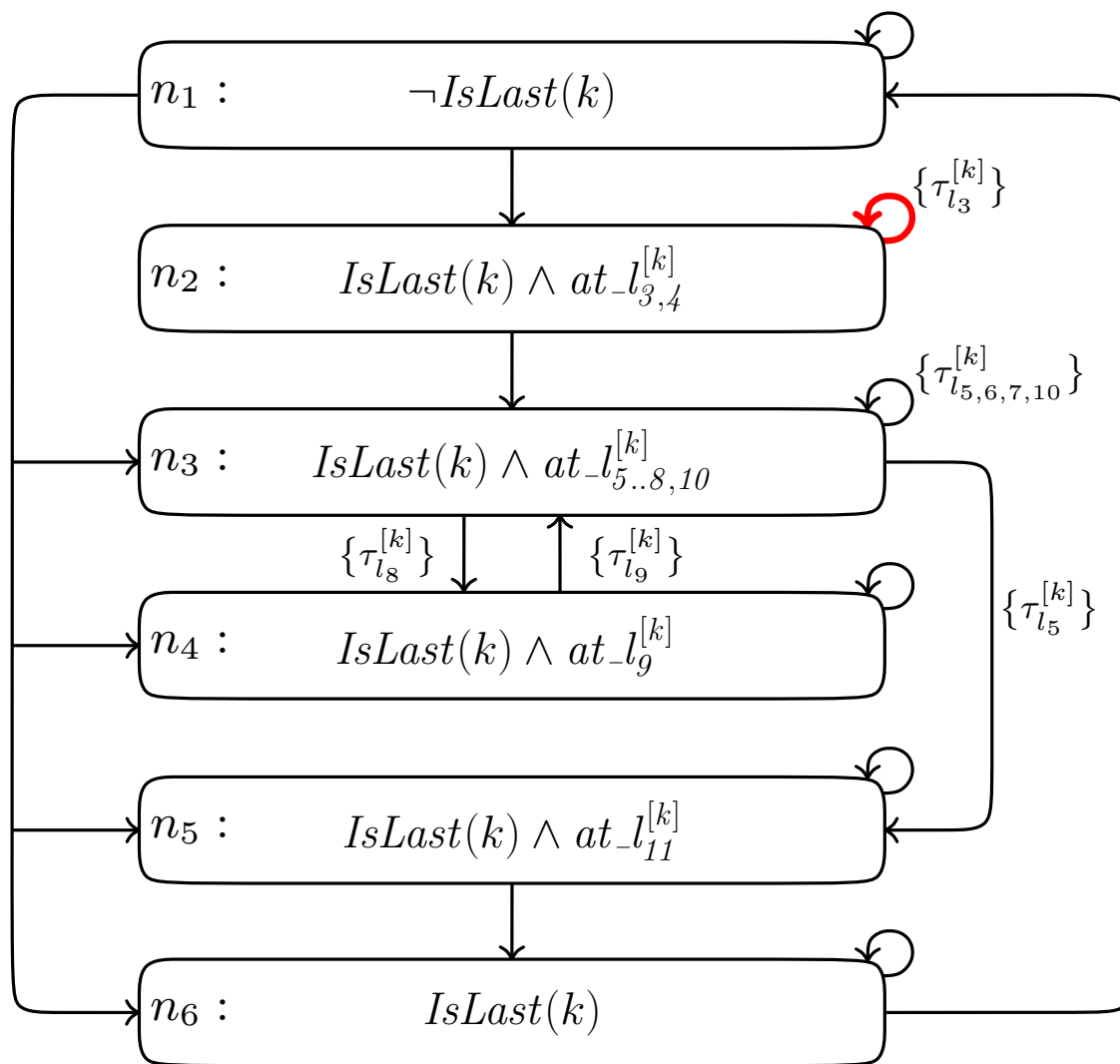


$$\mathcal{S}[N] \models \psi(k)$$

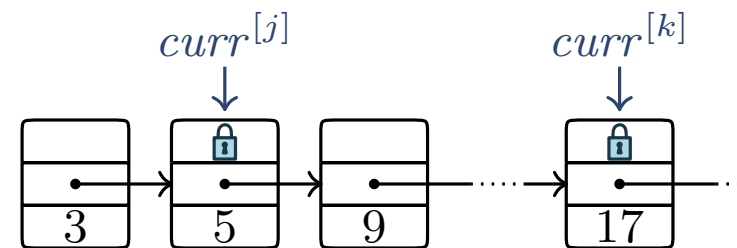


$$\left(\begin{array}{l} IsLast(k) \wedge j \neq k \wedge \\ at_{l_{3,4}}^{[k]} \wedge at_{l_9}^{[j]} \wedge \\ curr^{[j]}.lockid = \emptyset \end{array} \right) \wedge curr^{[j]}.lock(j) \rightarrow \left(\begin{array}{l} IsLast(k') \wedge at'_{l_{3,4}}^{[k']} \wedge \\ at'_{l_{10}}^{[j']} \wedge pres(V - curr^{[j]}) \wedge \\ curr'^{[j']}.lockid = j' \end{array} \right)$$

Verification Diagram for "Last Terminates"



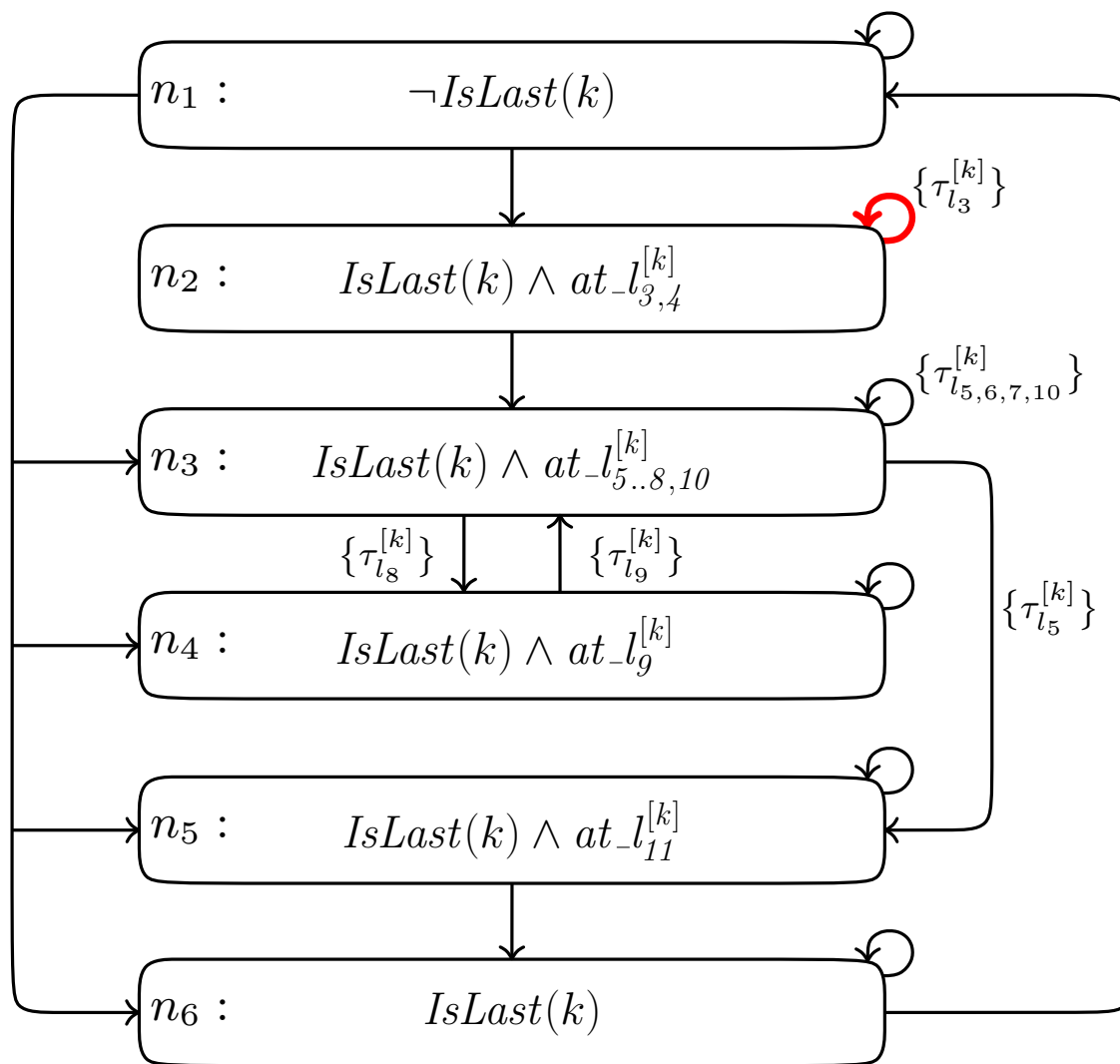
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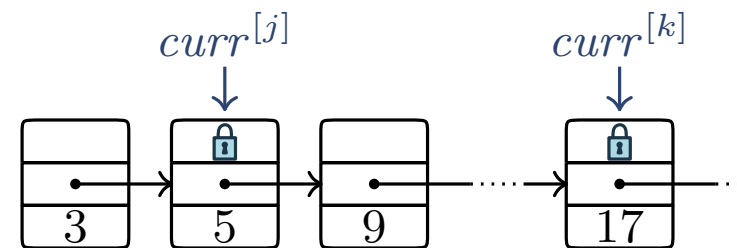
Reason about:
locks

$$\left(\begin{array}{l} IsLast(k) \wedge j \neq k \wedge \\ at_{l_{3,4}}^{[k]} \wedge at_{l_9}^{[j]} \wedge \\ curr^{[j]}.lockid = \emptyset \end{array} \right) \wedge curr^{[j]}.lock(j) \rightarrow \left(\begin{array}{l} IsLast(k') \wedge at'_{l_{3,4}}^{[k']} \wedge \\ at'_{l_{10}}^{[j']} \wedge pres(V - curr^{[j]}) \wedge \\ curr'^{[j']}.lockid = j' \end{array} \right)$$

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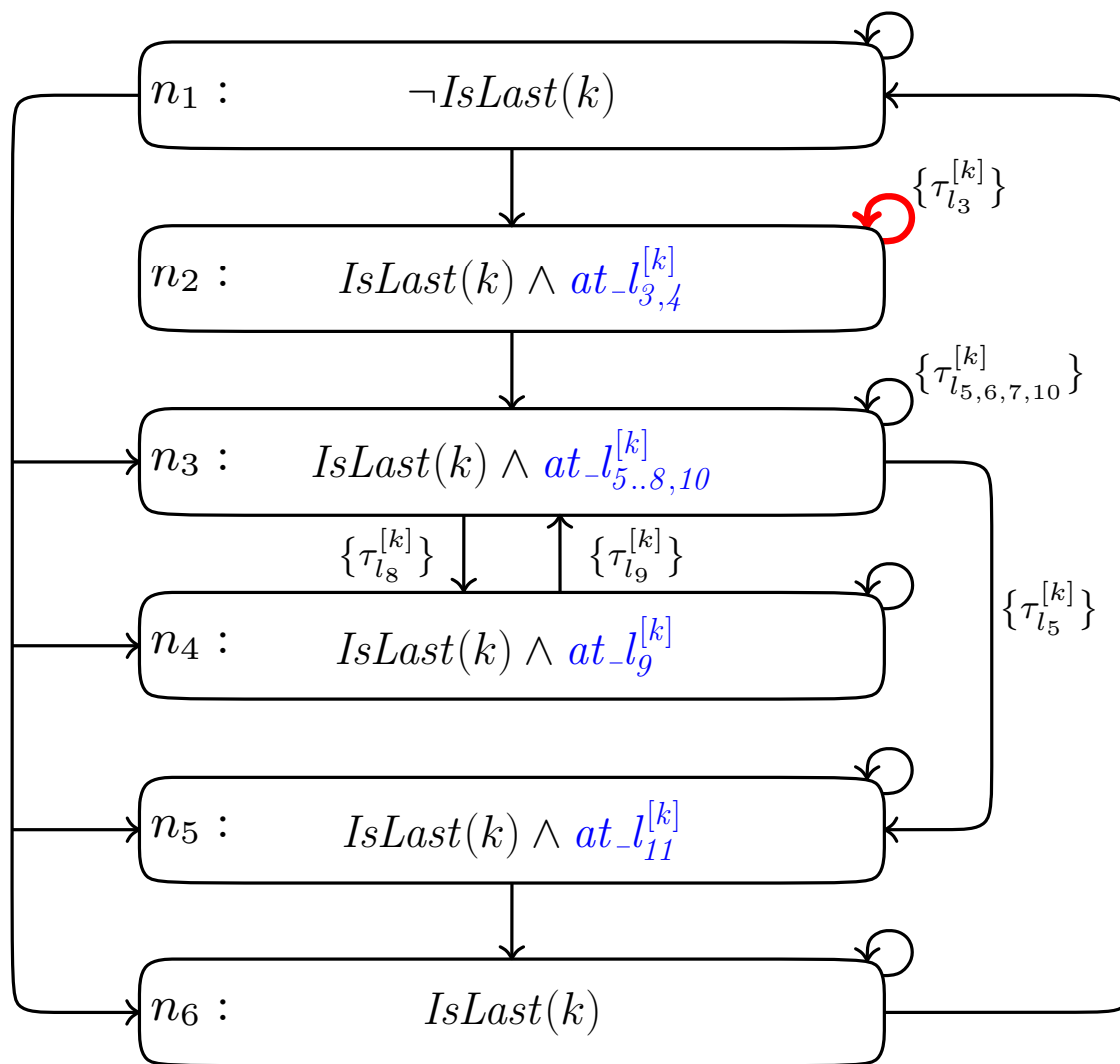
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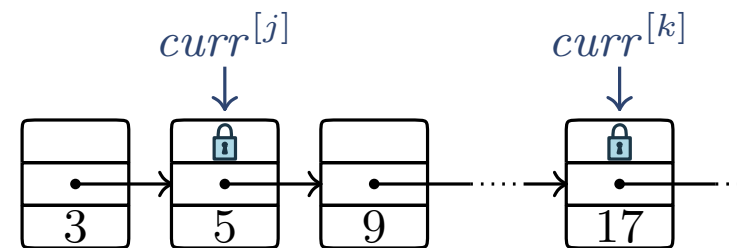
Reason about:
thread identifiers

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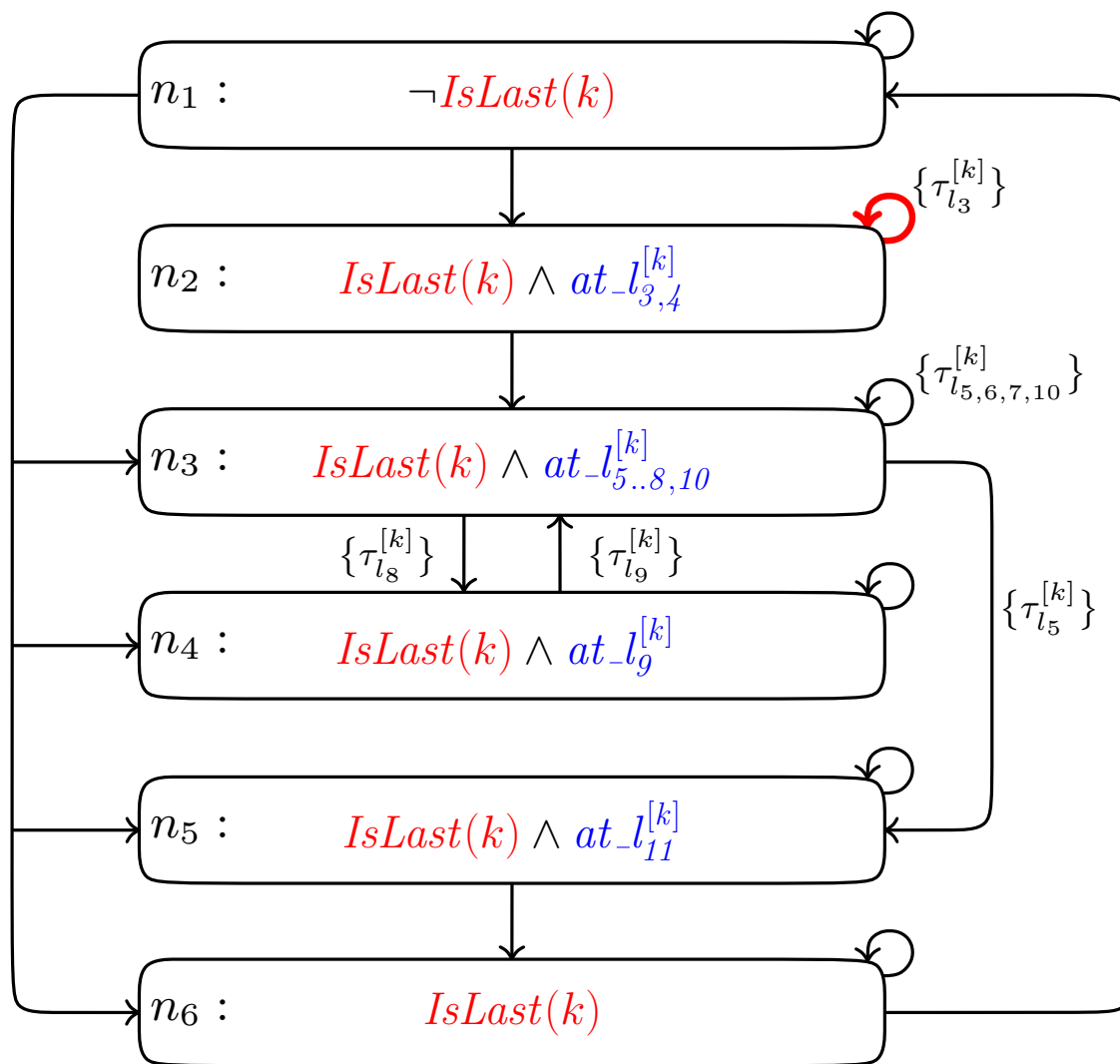
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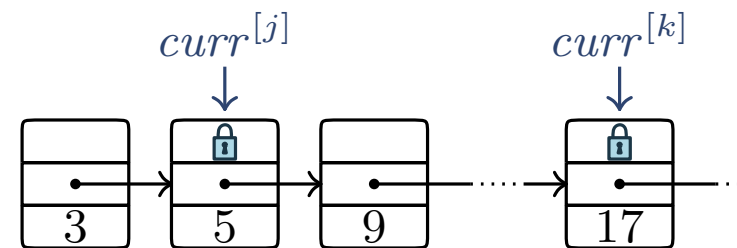
Reason about:
program position

$$\left(\begin{array}{l} IsLast(k) \wedge j \neq k \wedge \\ at_{l_{3,4}}^{[k]} \wedge at_{l_9}^{[j]} \wedge \\ curr^{[j]}.lockid = \emptyset \end{array} \right) \wedge curr^{[j]}.lock(j) \rightarrow \left(\begin{array}{l} IsLast(k') \wedge at'_{l_{3,4}}^{[k']} \wedge \\ at'_{l_{10}}^{[j']} \wedge pres(V - curr^{[j]}) \wedge \\ curr'^{[j']}.lockid = j' \end{array} \right)$$

Verification Diagram for "Last Terminates"



$$\mathcal{S}[N] \models \psi(k)$$



Reason about:
reachability, cells, memory

$$\left(\text{IsLast}(k) \wedge j \neq k \wedge \begin{array}{l} at_l_{3,4}^{[k]} \wedge at_l_9^{[j]} \wedge \\ curr^{[j]}.lockid = \emptyset \end{array} \right) \wedge curr^{[j]}.lock(j) \rightarrow \left(\text{IsLast}(k') \wedge at_l_{3,4}^{[k']} \wedge \begin{array}{l} at_l_{10}^{[j']} \wedge pres(V - curr^{[j]}) \wedge \\ curr'^{[j']}.lockid = j' \end{array} \right)$$

Our Contribution

- ▶ **TLL3**, a theory for concurrent linked lists
- ▶ We show TLL3 **decidable**
- ▶ We propose a combination based **decision procedure**

TLL3: A Theory for Single-Linked Concurrent Lists

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- ▶ TLL3 is a union of other theories

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Σ_{addr}

TLL3: A Theory for Single-Linked Concurrent Lists

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$$\Sigma_{\text{addr}} \cup \Sigma_{\text{elem}}$$

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TLL3: A Theory for Single-Linked Concurrent Lists

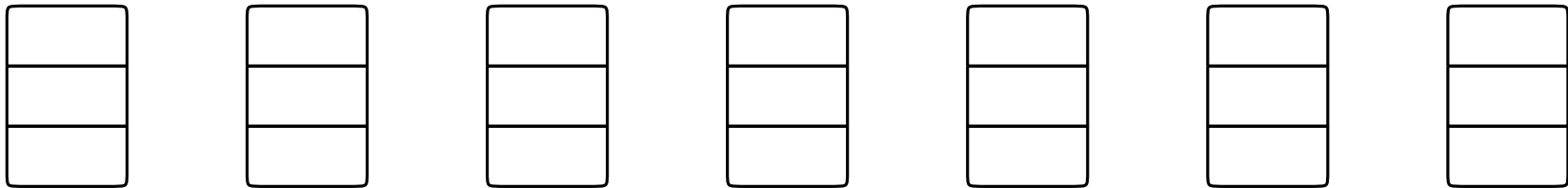
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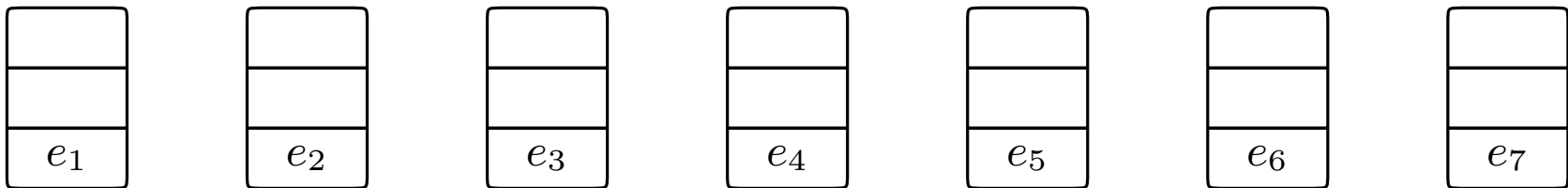
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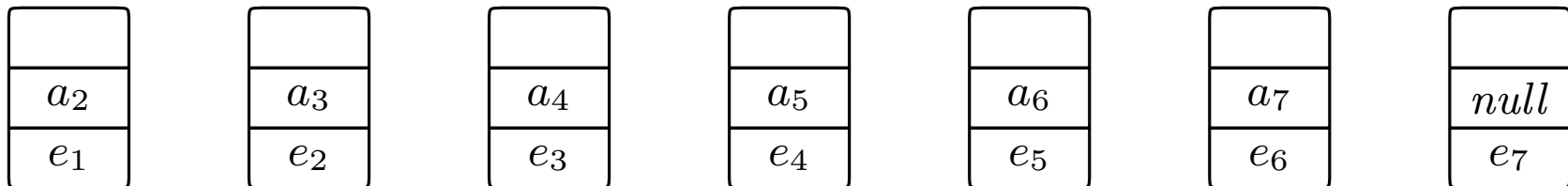
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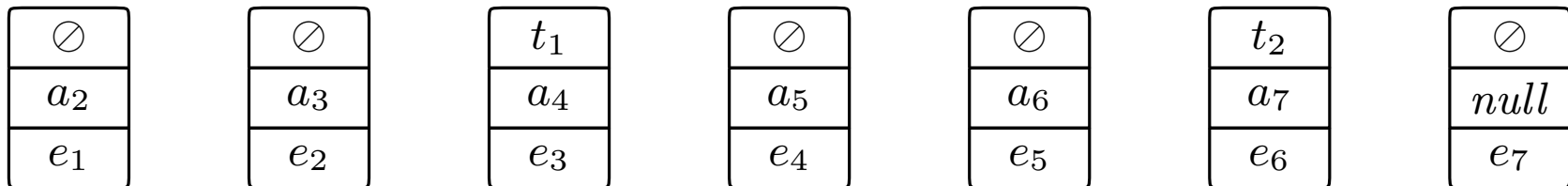
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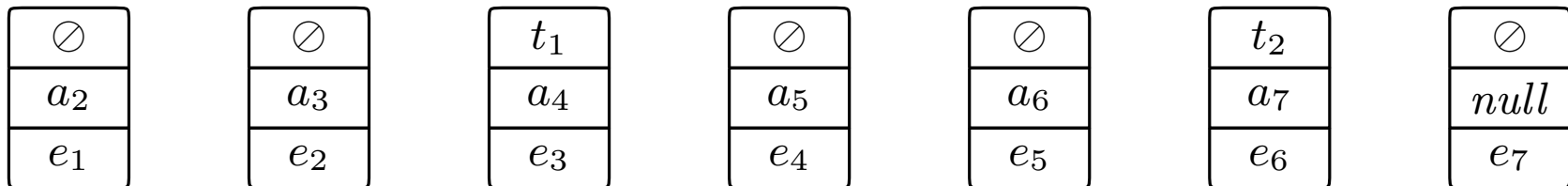
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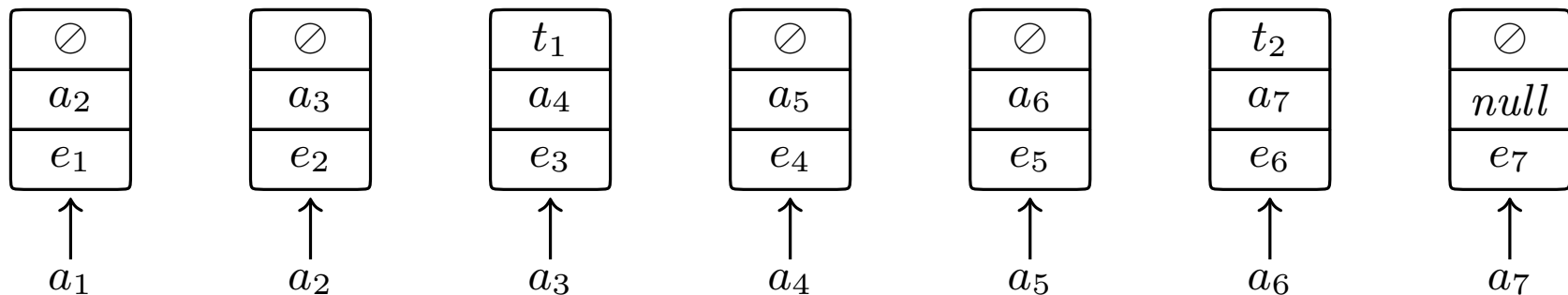
$$\Sigma_{\text{addr}} \cup \Sigma_{\text{elem}} \cup \Sigma_{\text{thid}} \cup \Sigma_{\text{cell}} \cup \Sigma_{\text{mem}}$$



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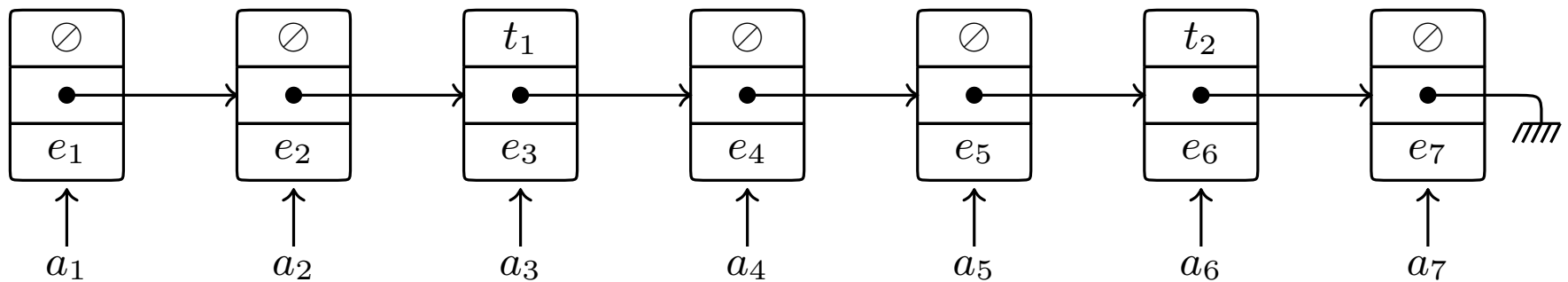
$$\Sigma_{\text{addr}} \cup \Sigma_{\text{elem}} \cup \Sigma_{\text{thid}} \cup \Sigma_{\text{cell}} \cup \Sigma_{\text{mem}}$$



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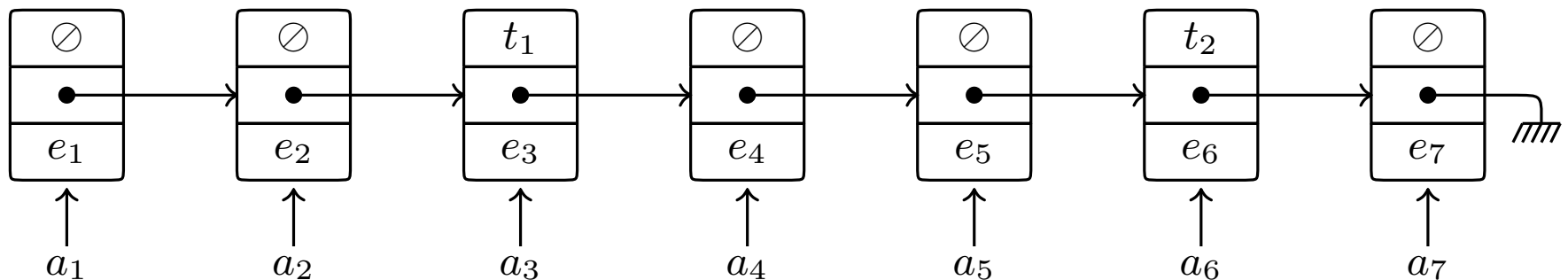
$$\Sigma_{\text{addr}} \cup \Sigma_{\text{elem}} \cup \Sigma_{\text{thid}} \cup \Sigma_{\text{cell}} \cup \Sigma_{\text{mem}}$$



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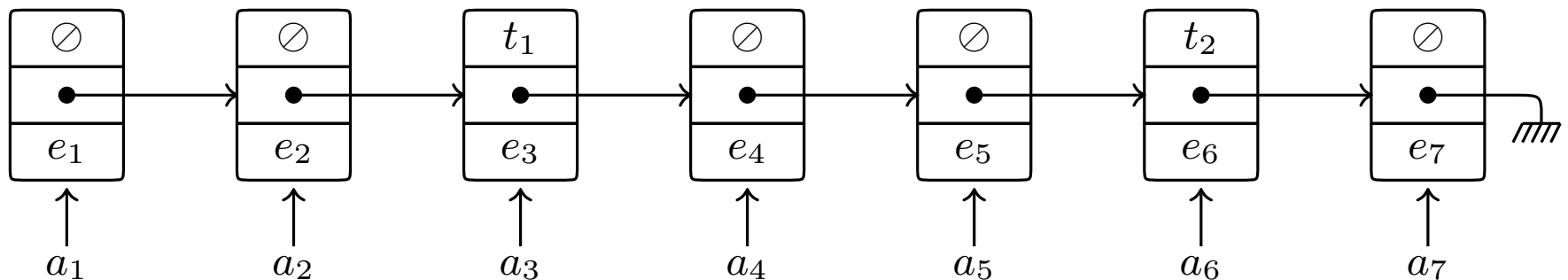
$$\Sigma_{\text{addr}} \cup \Sigma_{\text{elem}} \cup \Sigma_{\text{thid}} \cup \Sigma_{\text{cell}} \cup \Sigma_{\text{mem}} \cup \Sigma_{\text{set}}$$



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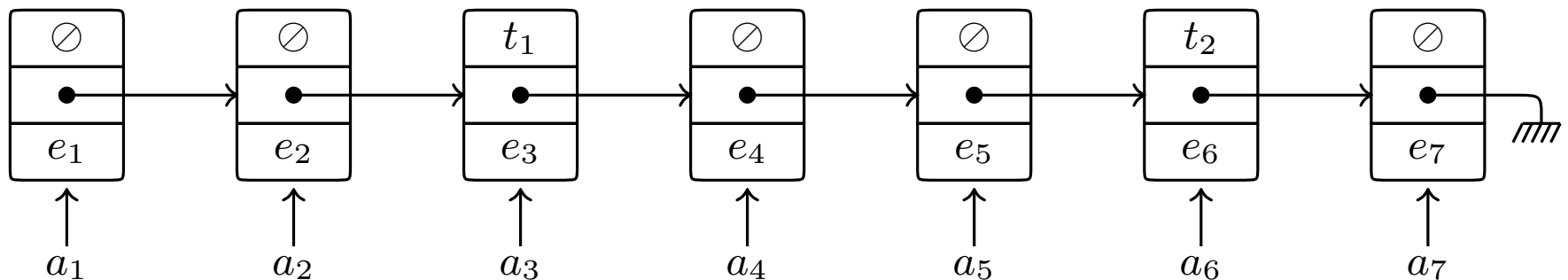
$$\Sigma_{\text{addr}} \cup \Sigma_{\text{elem}} \cup \Sigma_{\text{thid}} \cup \Sigma_{\text{cell}} \cup \Sigma_{\text{mem}} \cup \Sigma_{\text{set}} \cup \Sigma_{\text{setth}}$$



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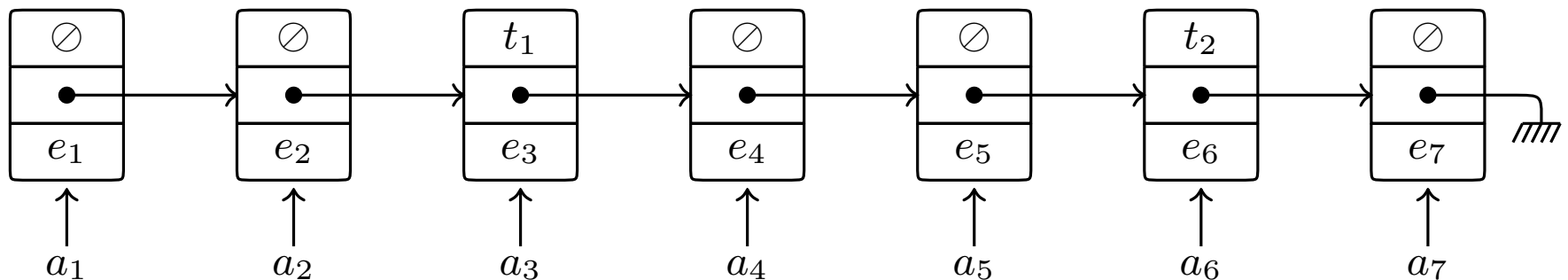


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path = a non-repeating sequence of addresses



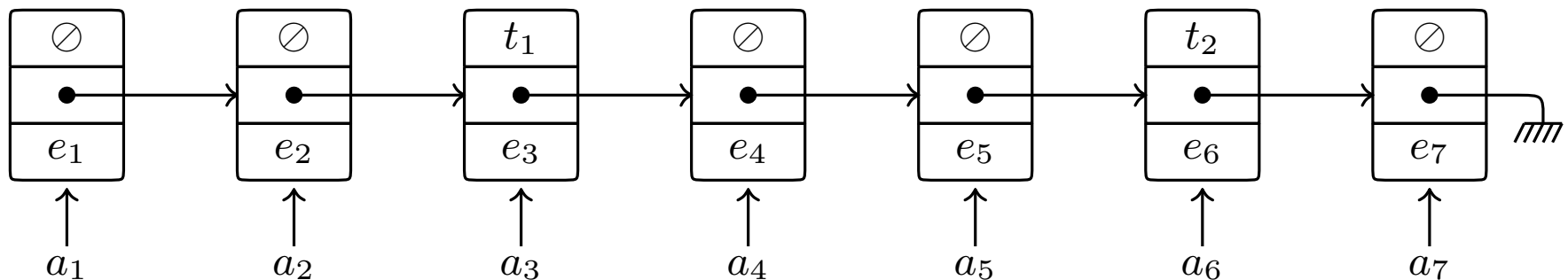
TLL3: A Theory for Single-Linked Concurrent Lists

- ▶ TLL3 is a union of other theories

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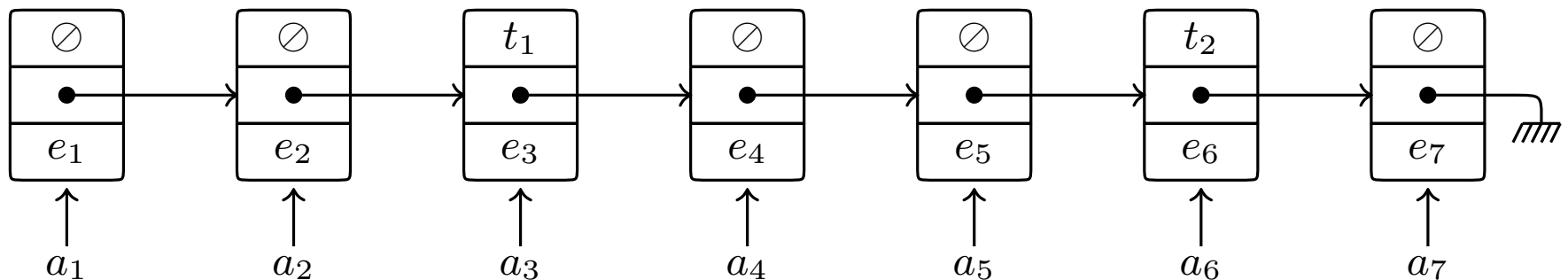
$$[a_1, a_2, a_3]$$



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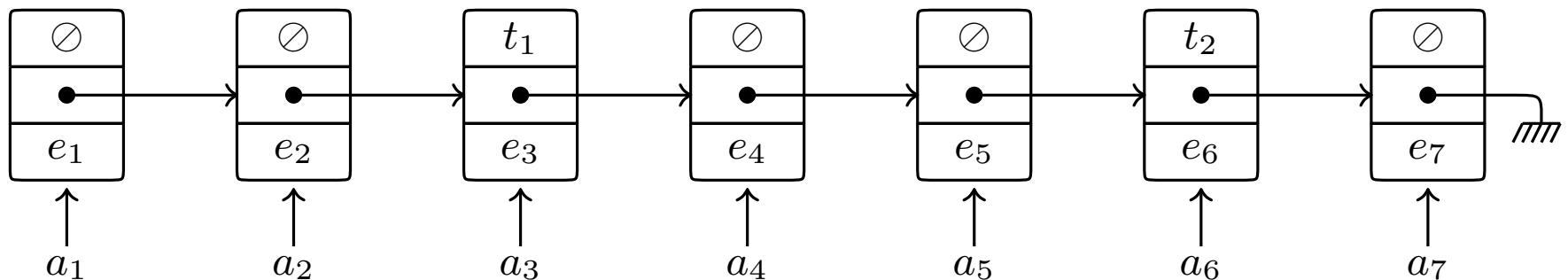


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append([a_1, a_2], [a_3, a_4, a_5], [a_1, a_2, a_3, a_4, a_5])

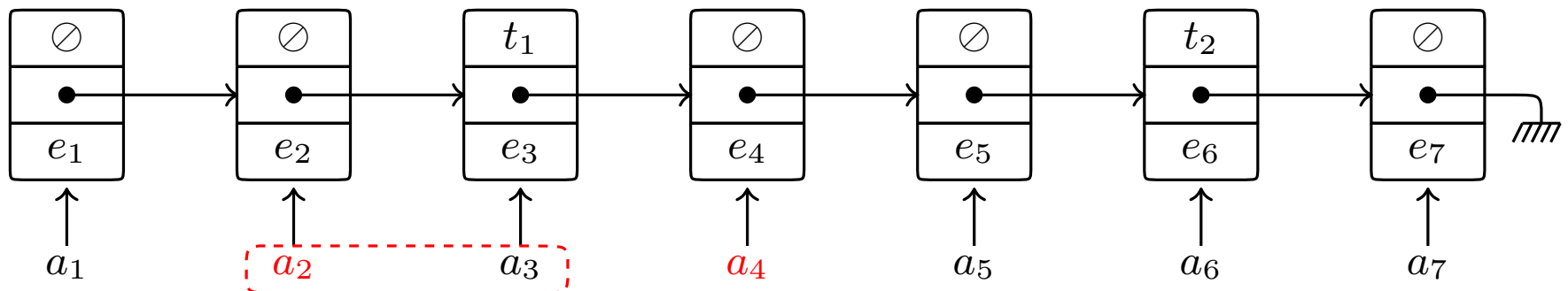


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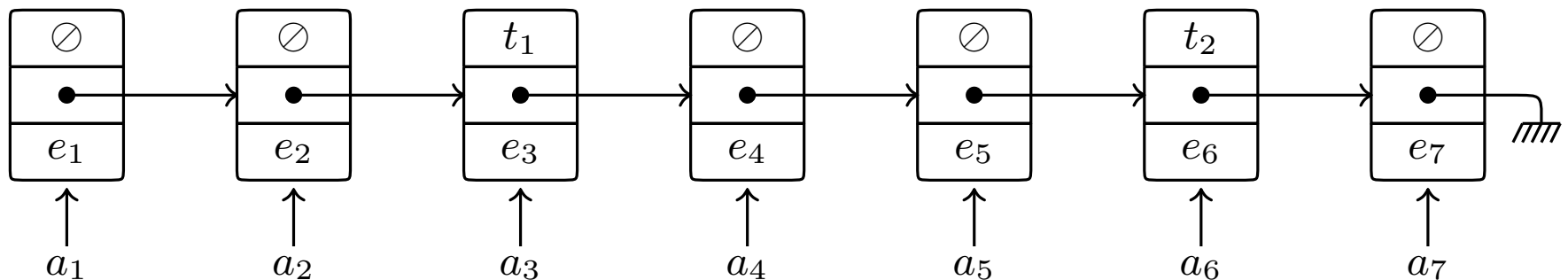
$$\text{reach}(m, a_2, a_4, [a_2, a_3])$$



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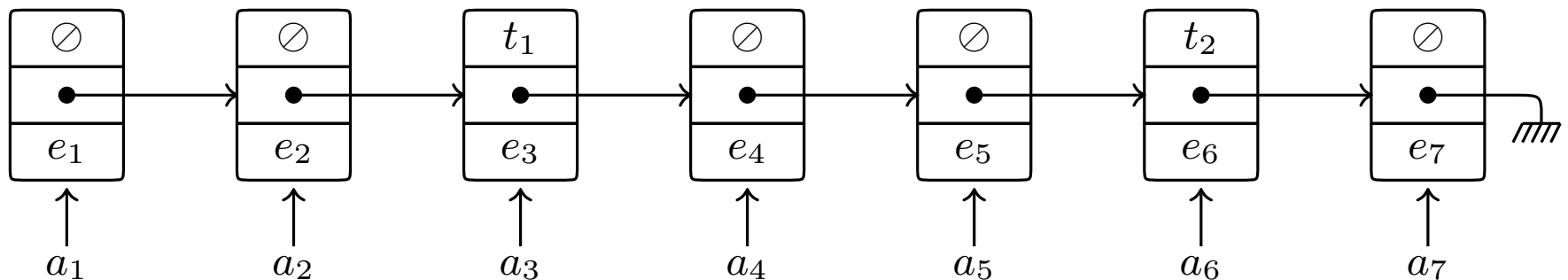
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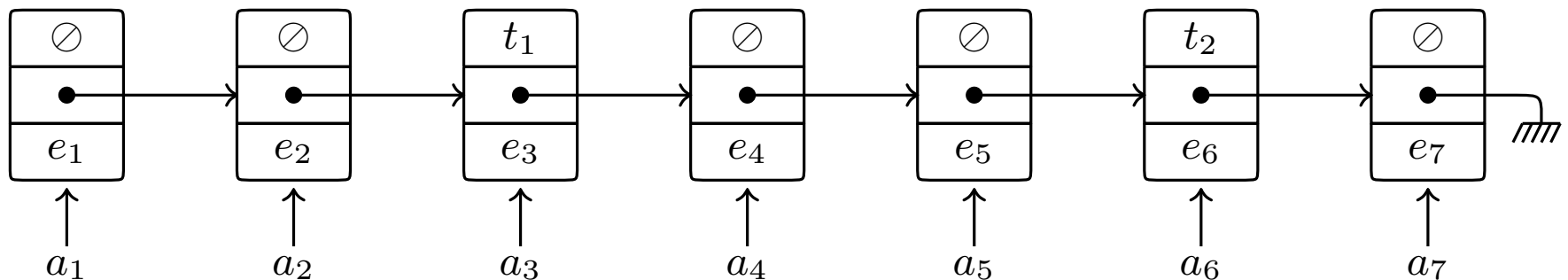


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$$\text{path2set}([a_2, a_3]) = \{a_2, a_3\}$$

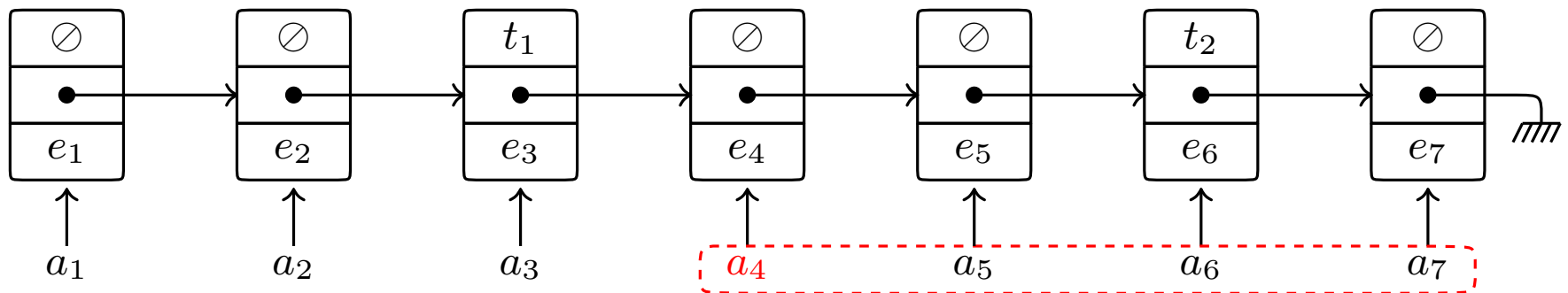


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$$\text{addr2set}(m, a_4) = \{a_4, a_5, a_6, a_7\}$$

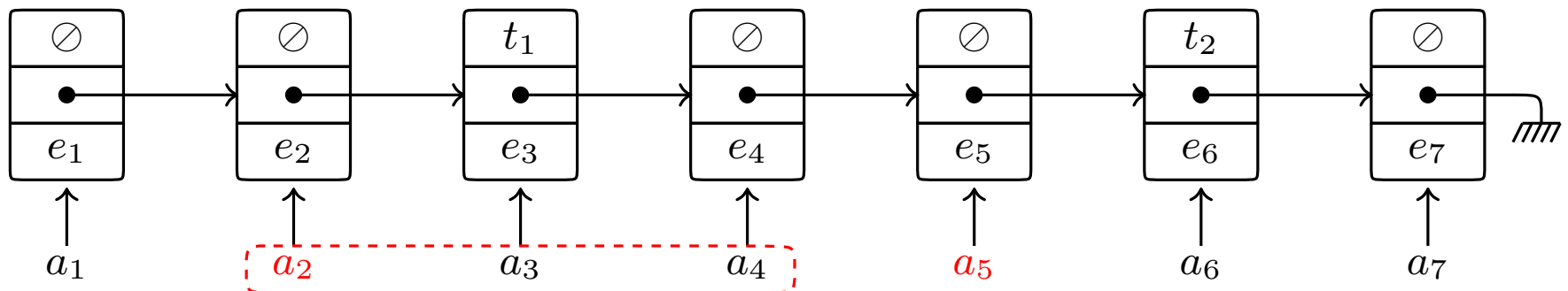


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$$\text{getp}(m, a_2, a_5) = [a_2, a_3, a_4]$$

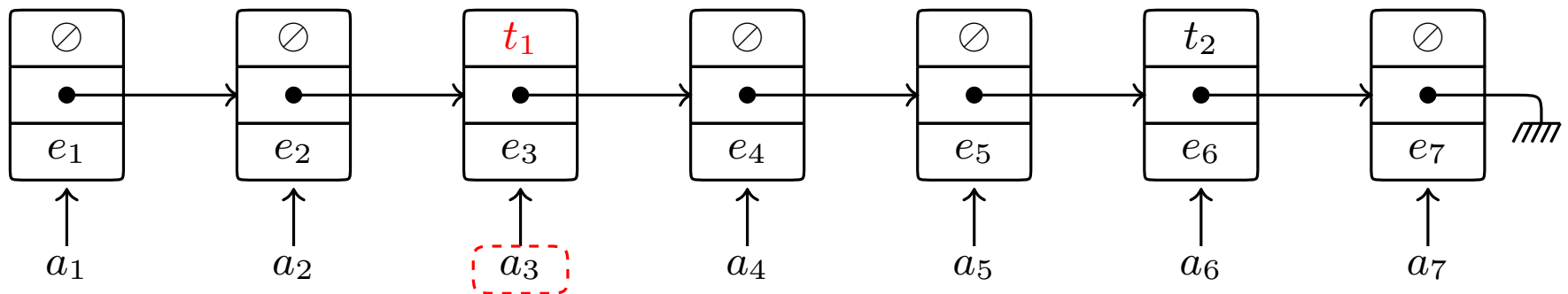


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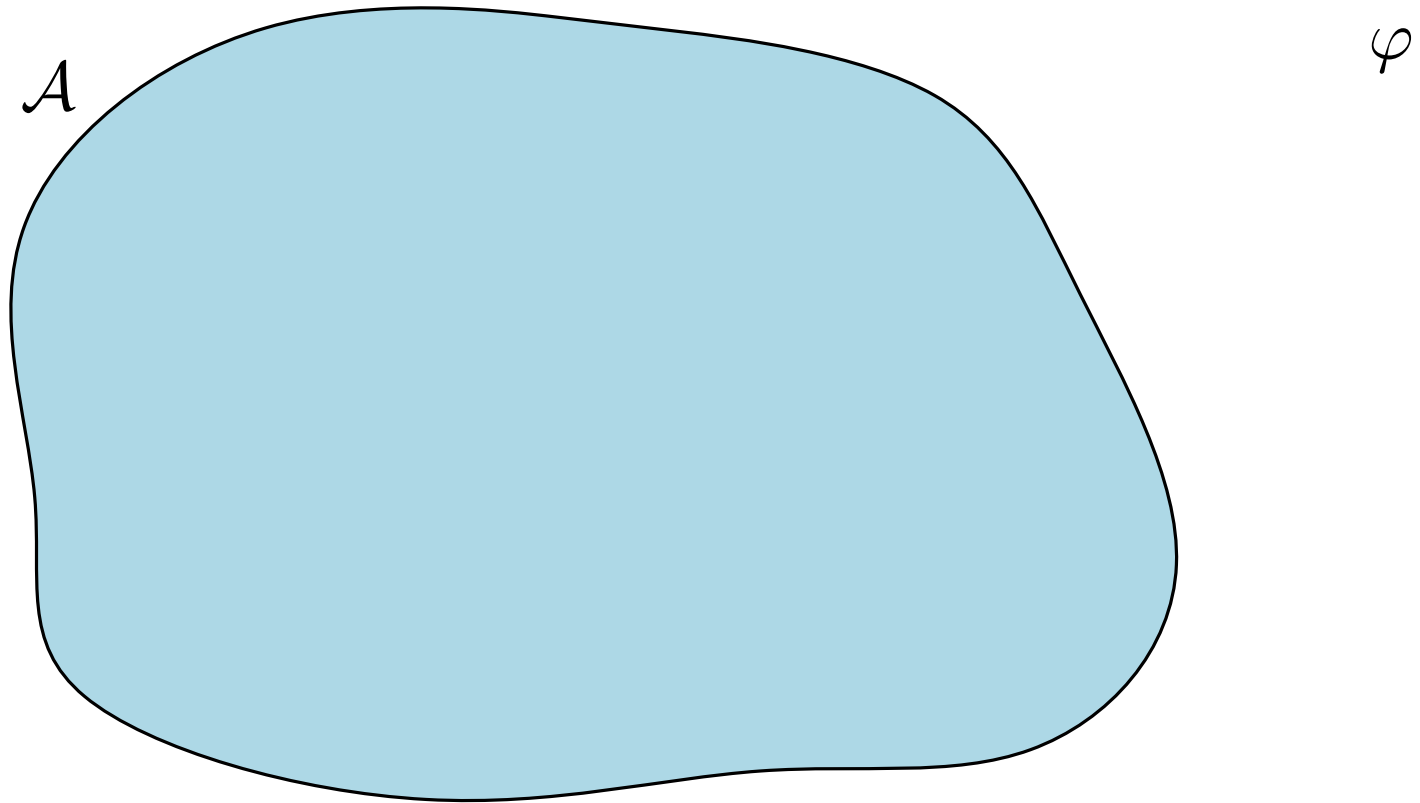
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$$\text{firstlocked}(m, [a_1, a_2, a_3, a_4, a_5, a_6, a_7]) = a_3$$

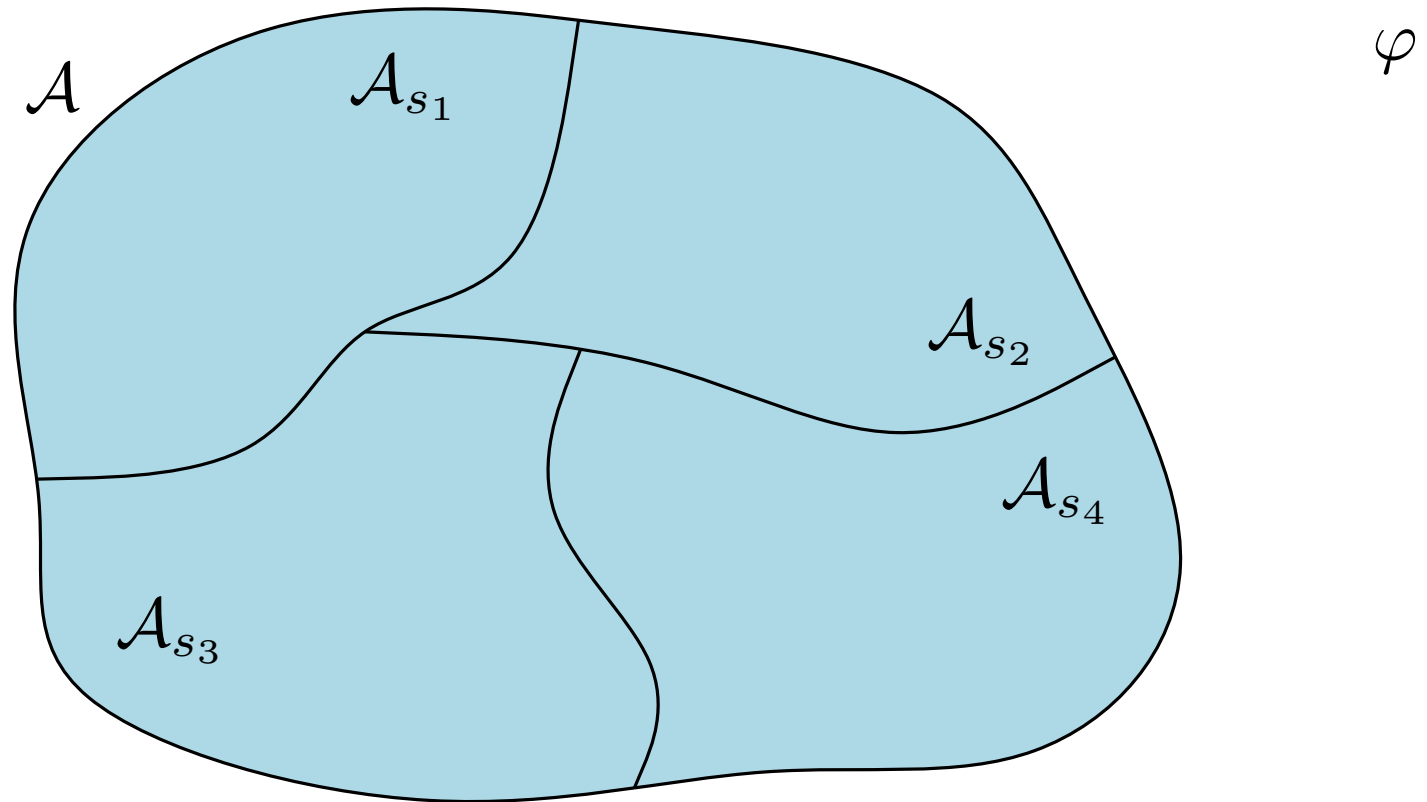


Decidability by Small Model Property (SMP)

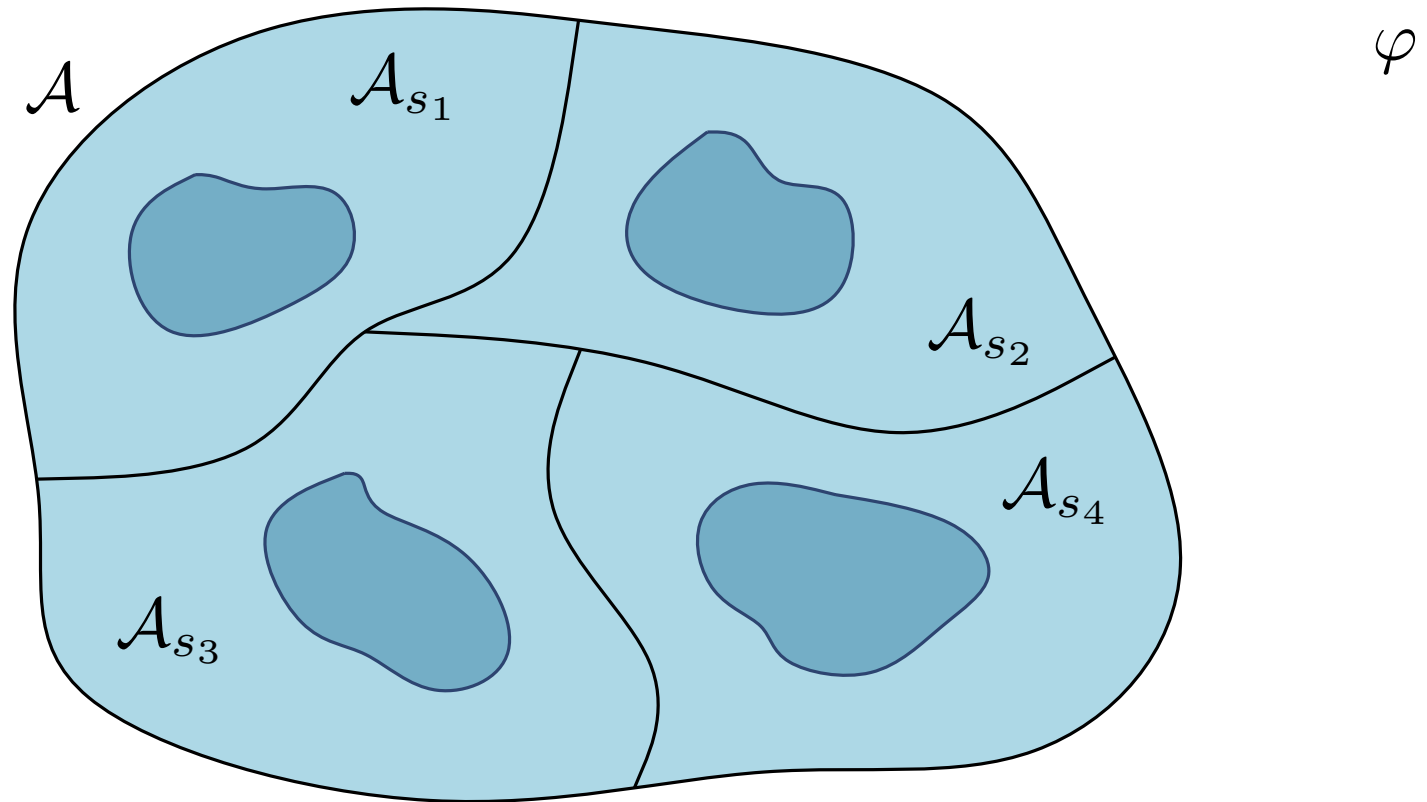
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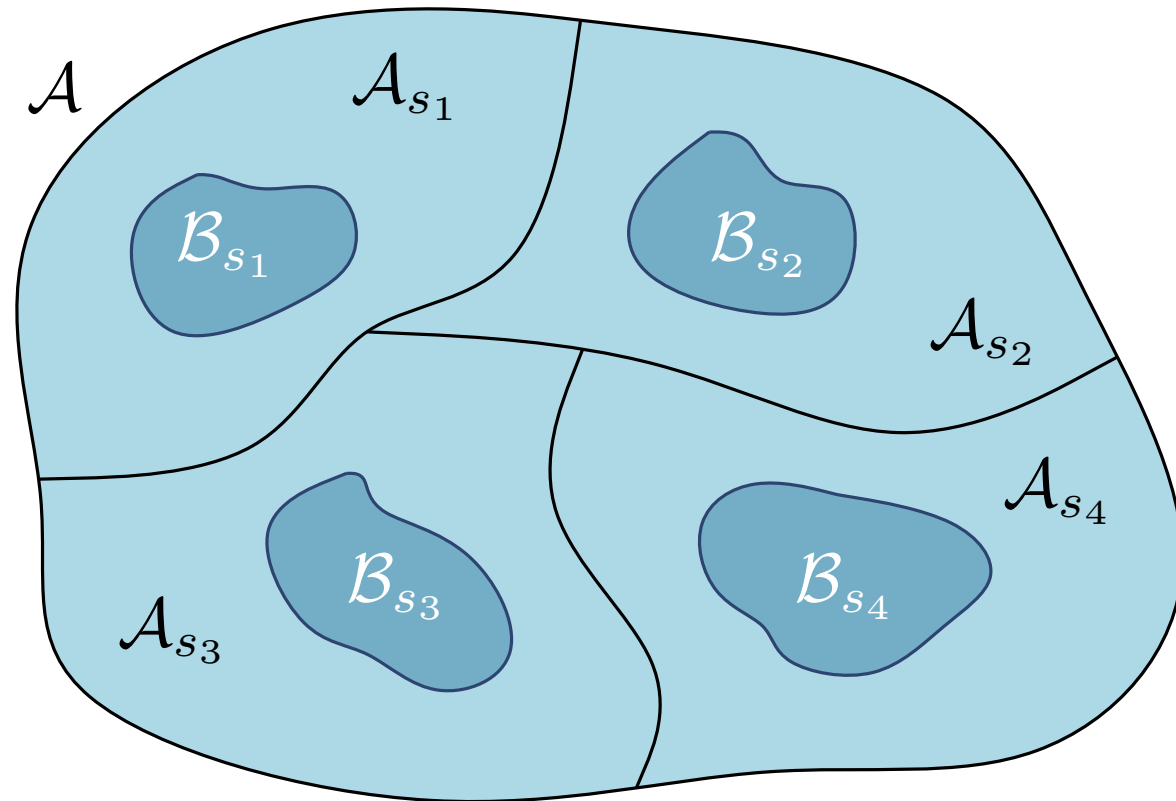
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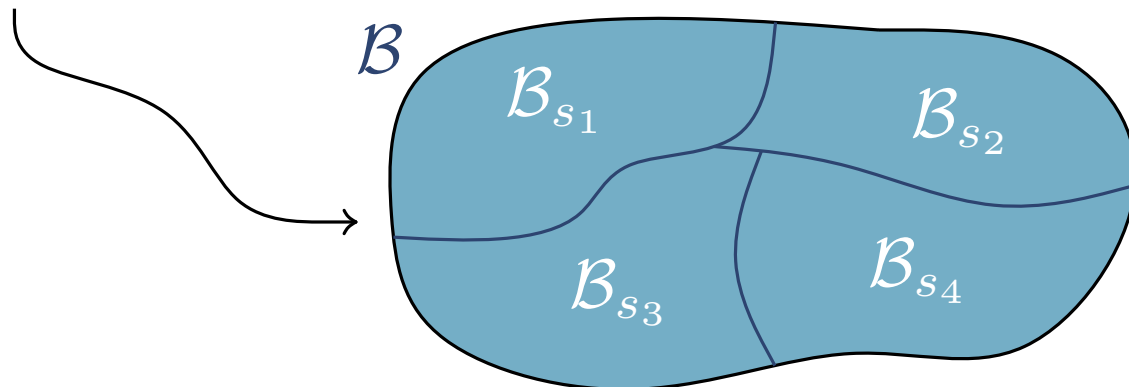


Decidability by Small Model Property (SMP)



φ

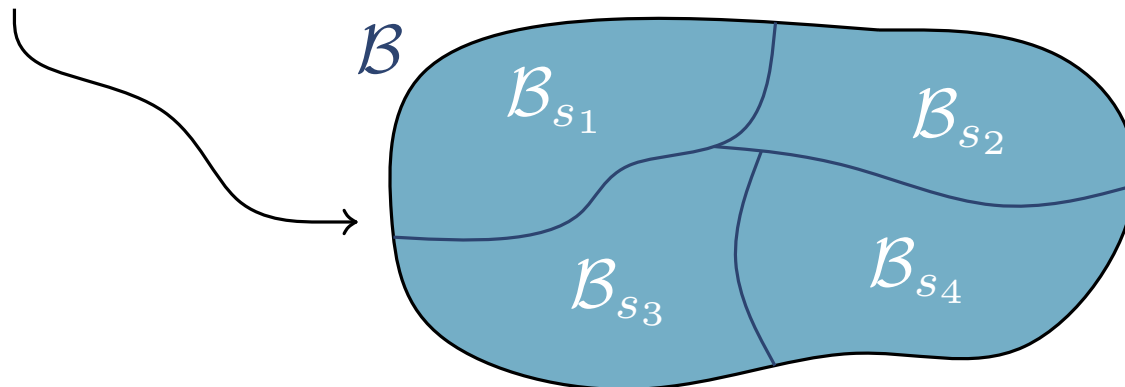
Finite elements



Decidability by Small Model Property (SMP)

- ▶ Let Γ be a conjunction of TLL3-literals

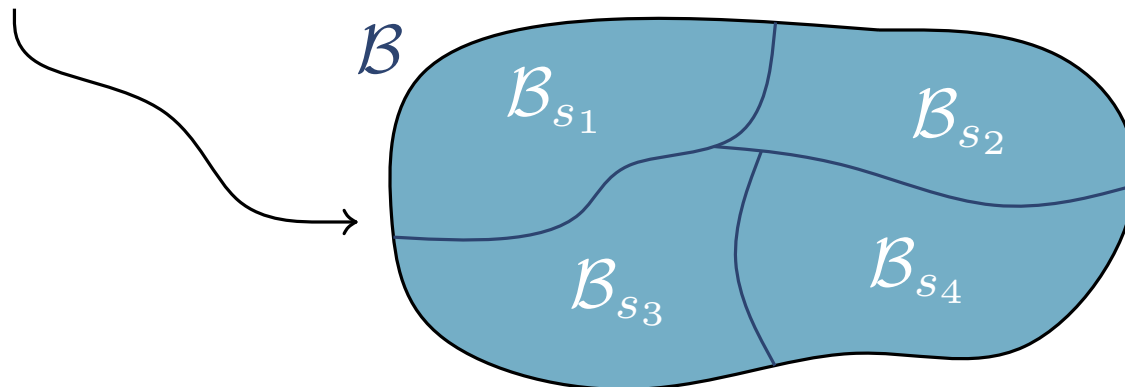
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- ▶ If Γ is satisfied in an arbitrary TLL3 interpretation \mathcal{A}

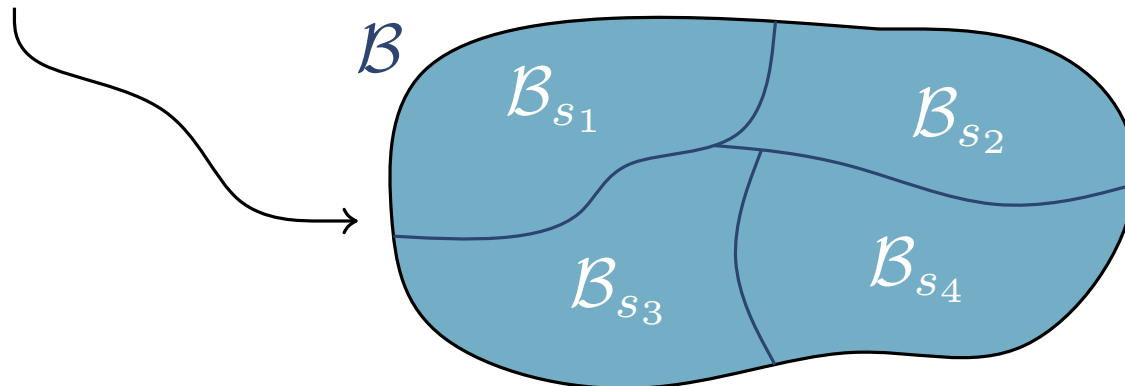
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Decidability by Small Model Property (SMP)

- ▶ Let Γ be a conjunction of TLL3-literals
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- ▶ We construct a new finite interpretation \mathcal{B} **bounded by Γ**

Finite elements

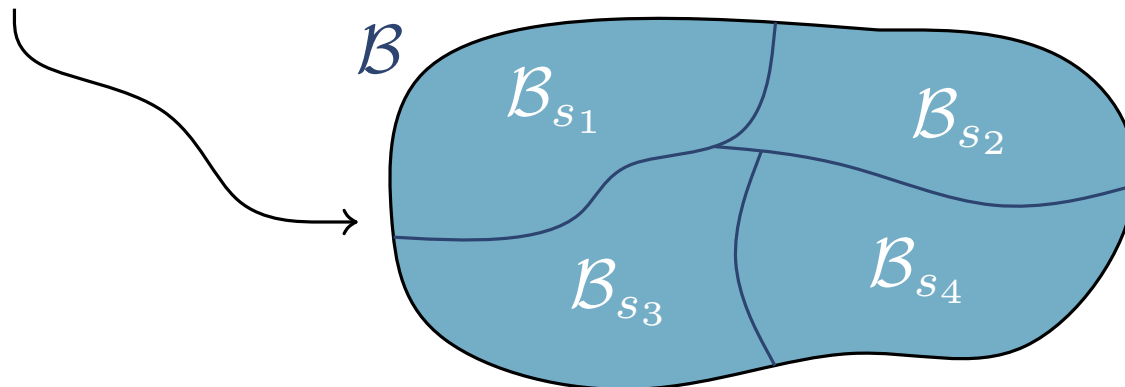


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TLL3 is **decidable** by enumerating all possible elements

Finite elements



A Decision Procedure for TLL3 (main idea)

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- ▶ We use a **many-sorted variant of Nelson-Oppen**

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Stable Infinite & Politeness

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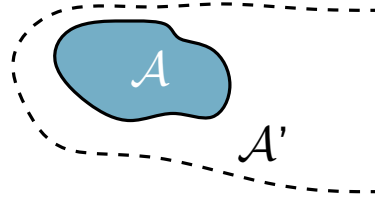
▶ **Stable infinite**

▶ **Polite** with respect to sorts s_1, \dots, s_n

Stable Infinite & Politeness

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for all QF-formula φ , exists an infinite interpretation \mathcal{A}'

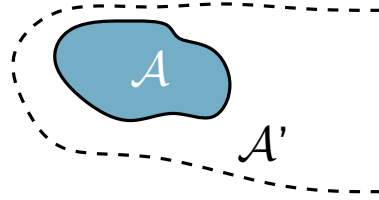


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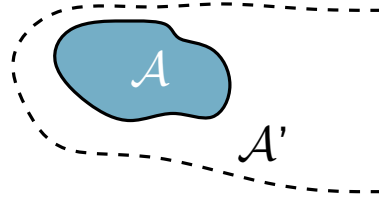
 - ▶ **Smooth**

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Stable Infinite & Politeness

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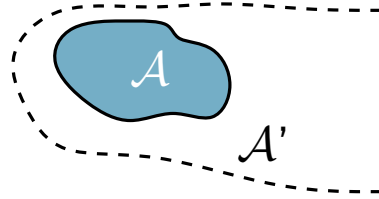
if \mathcal{A} is a model of φ , with domains $\mathcal{A}_{s_1} \cdots \mathcal{A}_{s_n}$

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Stable Infinite & Politeness

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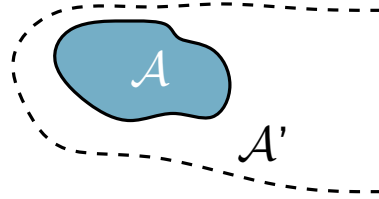
$$\begin{array}{ccc} |\mathcal{A}_{s_1}| & \cdots & |\mathcal{A}_{s_n}| \\ // \wedge & & // \wedge \\ k_1 & \cdots & k_n \end{array}$$

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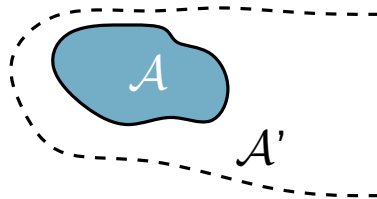
$$\begin{array}{ccc} |\mathcal{A}_{s_1}| & \cdots & |\mathcal{A}_{s_n}| \\ // \wedge & & // \wedge \\ k_1 & \cdots & k_n \\ || & & || \\ |\mathcal{B}_{s_1}| & \cdots & |\mathcal{B}_{s_n}| \end{array}$$

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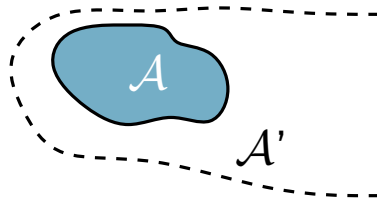
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f

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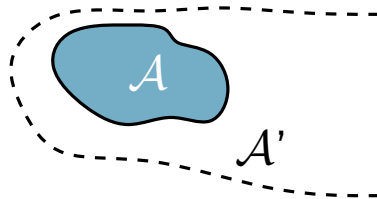
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$$\varphi \quad f$$

Stable Infinite & Politeness

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for all QF-formula φ , exists an infinite interpretation \mathcal{A}'



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► Finite witnessable

$$\varphi \xrightarrow{f} (\exists \bar{v})\psi \quad \text{s.t. } [\bar{v} = V_\varphi \setminus V_\psi]$$

if ψ is **satisfiable**, then exists \mathcal{B} with **one variable per value**

Checklist

- ▶ We use a **many-sorted variant of Nelson-Oppen**

To apply it, we require:

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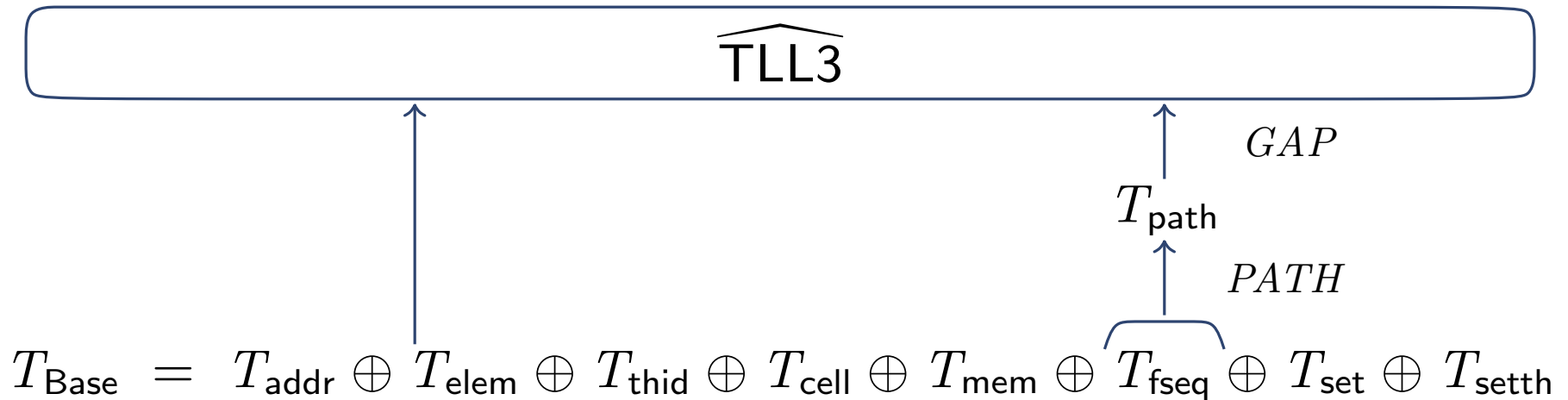
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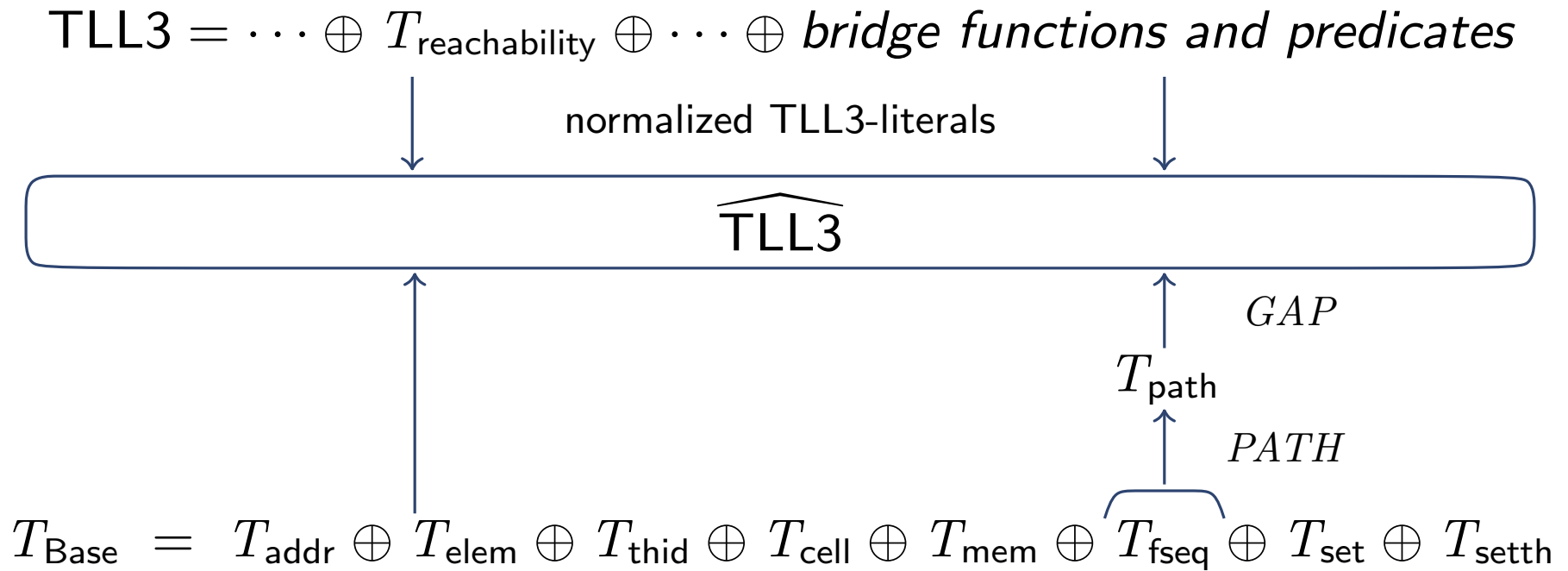
$$T_{\text{Base}} = T_{\text{addr}} \oplus T_{\text{elem}} \oplus T_{\text{thid}} \oplus T_{\text{cell}} \oplus T_{\text{mem}} \oplus \underbrace{T_{\text{fseq}} \oplus T_{\text{set}} \oplus T_{\text{setth}}}_{T_{\text{path}} \text{ PATH}}$$

A Combination-based Decision Procedure for TLL3

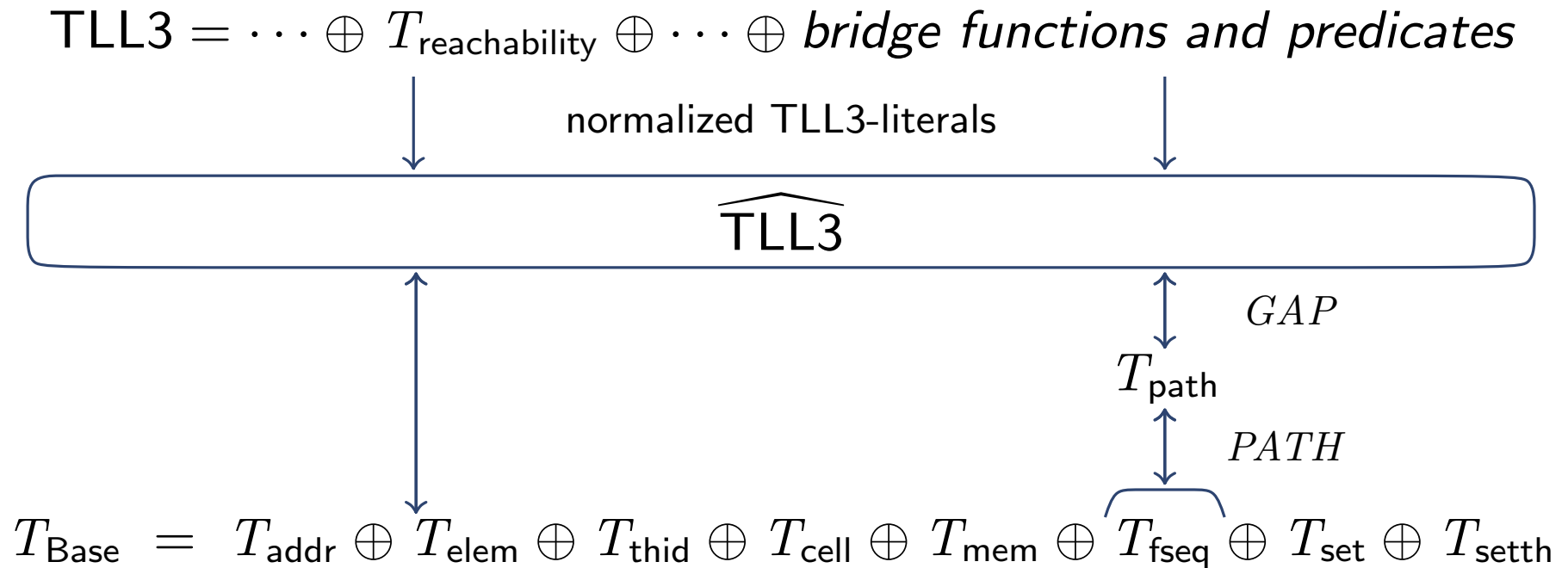
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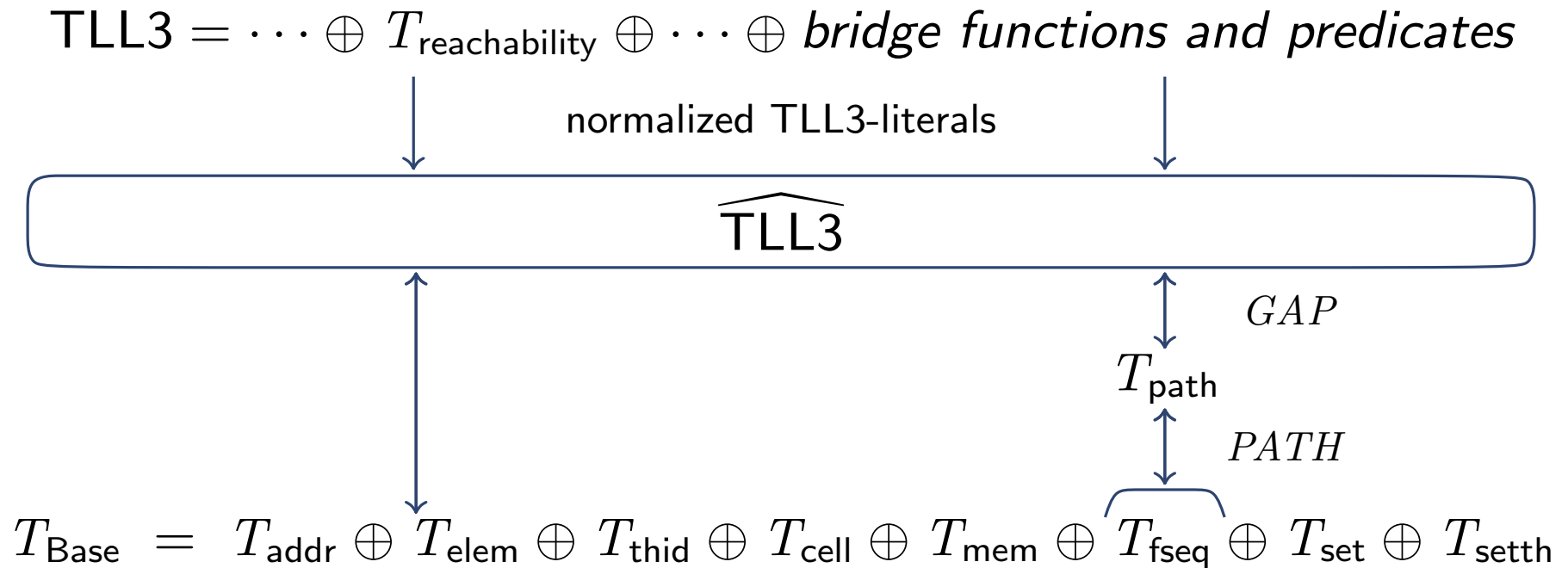


A Combination-based Decision Procedure for TLL3



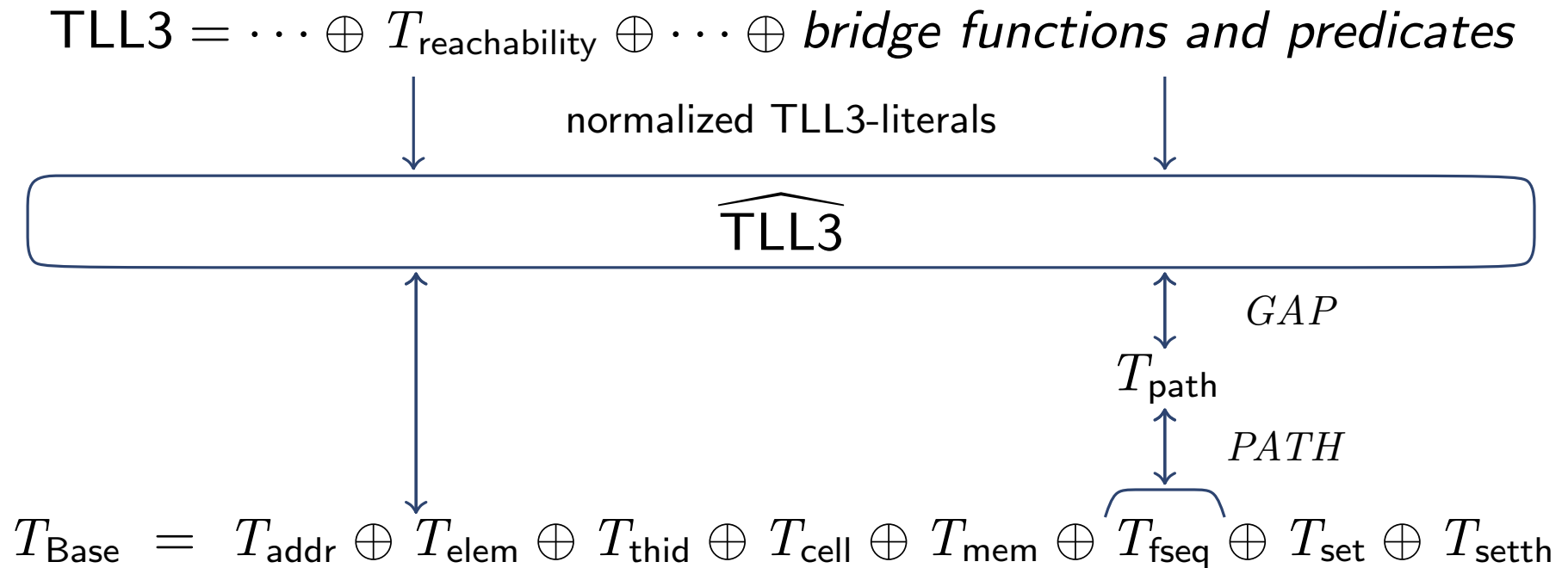
- **Unfolding** of definitions in $PATH$ and GAP

A Combination-based Decision Procedure for TLL3



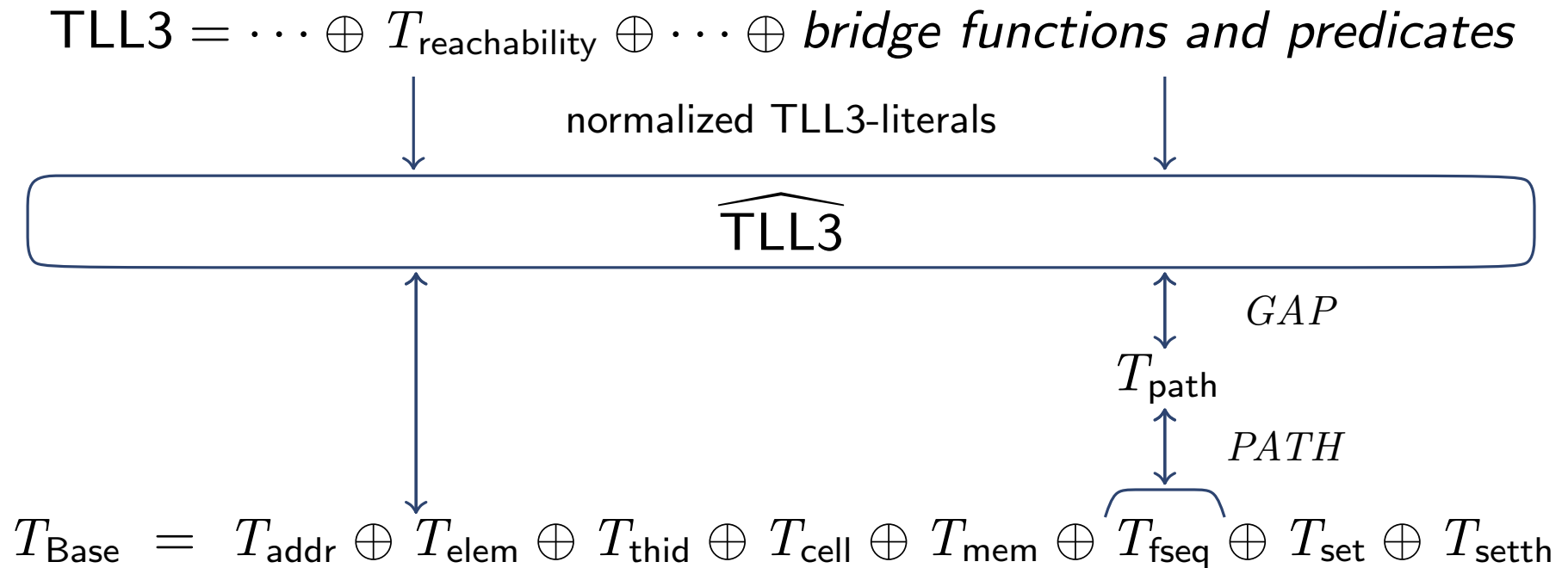
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- ▶ T_{cell} , T_{mem} , T_{set} , T_{setth} and T_{fseq} are all **stable infinite**
- ▶ These are **smooth** with respect to sorts *addr*, *elem* and *thid*

Conclusions

- ▶ We defined **TLL3**, a theory for concurrent single-linked lists
- ▶ We proved TLL3 **decidable**, by *Small Model Property*
- ▶ We provide a **combination-based decision procedure** for TLL3
- ▶ A step towards the assisted verification of **temporal properties** over **concurrent data-types**: VD + DP
- ▶ **Current and future** work:
 - parametrized verification diagrams, DP for concurrent skiplists, concurrent hash-maps, concurrent Schorr-Waite
- ▶ Many possible **collaborations**:
 - DPs as combination, SMTs, implementation