# Mode-based Reactive Synthesis\*

Matías Brizzio<sup>1,2</sup>, Felipe Gorostiaga<sup>1</sup>, César Sánchez<sup>1</sup>, Renzo Degiovanni<sup>3</sup>

 <sup>1</sup> IMDEA Software Institute, Spain
 <sup>2</sup> Universidad Politécnica de Madrid, Spain
 <sup>3</sup> Luxembourg Institute of Science and Technology (LIST) {first.last}@{imdea.org | list.lu}

**Abstract.** Reactive synthesis aims to automatically generate systems from high-level formal specifications, but its inherent complexity limits its scalability to real-world scenarios. Scalability can be improved by decomposing the specification into independent parts for parallel synthesis, but the dependency between variables limits this approach.

At the same time, specifications used in Requirements Engineering (RE) often include high-level state machine descriptions, known as modes, which structure the specification.

This paper introduces a novel method for the sequential decomposition of reactive synthesis problems based on modes. Our approach automatically uses modes to break down a specification into smaller sub-specifications, synthesizes each independently, and then integrates the solutions into a cohesive global model. We present an algorithm that exploits mode transitions and ensures consistency across synthesized components leveraging off-the-shelf reactive synthesis tools.

We prove the correctness of our approach and show empirically that our method significantly improves scalability when decomposing realworld specifications, outperforming state-of-the-art monolithic tools. As the first sequential decomposition approach, our method offers a promising alternative for scalable reactive synthesis.

#### 1 Introduction

Reactive systems [50], which continuously interact with their environment, are essential in domains like cyber-physical and embedded systems. These systems are crucial for tasks such as model checking [19], property monitoring [8], and model-based testing [31]. Linear-Time Temporal Logic (LTL) [59] is commonly used to specify properties of reactive systems, typically in an assume-quarantee

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format  $(A \to G)$ . Here, Assumptions (A) describe the uncontrollable environment and Guarantees (G) define the desired system behavior. This separation into *environment-controlled* and *system-controlled* variables facilitates effective analysis and synthesis [11].

Reactive synthesis automates the construction of a *controller* from a specification, ensuring that for all valid environment inputs, the controller behaves as required. Despite progress in the field, synthesizing controllers for complex specifications remains computationally challenging. Even with efficient LTL fragments like GR(1) [58,10], deciding realizability and generating a controller can lead to exponential blow-ups [40] in the reduction from the specification to GR(1). Several decomposition techniques have been proposed [28,45,7,20,55] which try to perform synthesis independently for different variables, but these methods face difficulties when sub-specifications share *controllable variables*. Requirements Engineering (RE) methodologies provide useful mechanisms to modularize and specify the system behavior. A typical way to organize system requirements is through the use of high-level state machine descriptions in which the states, referred to as *modes*, encapsulate specific system behavior under particular situations and transitions represent how the system execution evolves and react to environmental events [39,62,70,24].

Mode-based synthesis leverages these modes to synthesize complex systems by decomposing global specifications into sub-specifications for individual modes. However, the only previous approach [13] requires manual intervention, limiting its automation. In this work we present a fully automatic mode-based synthesis process, eliminating the need for manual engineering input. Our approach takes as input the system's global specification, a description of its modes, and optionally, the possible transitions between them. Through a process we call sequential decomposition, the synthesis problem is addressed incrementally: each sub-problem is solved independently, focusing on one mode at a time. A key aspect of our method is the treatment of initial conditions for each mode. Initial conditions ensure coherence between sub-specifications and the global specification guaranteeing the correct connection among modes. By maintaining alignment with global requirements, our method guarantees consistency across mode transitions, avoiding conflicts and ensuring the system's correctness.

We formalize mode projection and mode-based synthesis, proving consistency between modes and the global system. Crucially, our approach computes a set of initial conditions that ensure that (1) each sub-specification is realizable, (2) the initial conditions of each mode support the other modes. This allows to conclude that the global specification is also realizable. The resulting structured controllers enhance transparency and interpretability while improving synthesis scalability, as shown empirically against state-of-the-art monolithic tools. The paper is organized as follows: Section 2 covers preliminaries, Section 3 details our approach, Section 4 presents empirical results, and Section 5 concludes.

**Related Work.** Reactive synthesis [60,10] for LTL is 2EXPTIME-complete [60]. The use of tractable fragments, such as GR(1), reduces synthesis complexity

to polynomial time [10]. However, significant challenges remain, particularly in constructing deterministic automata for large Safety-LTL formulas [77]. Compositional approaches improve scalability by decomposing synthesis tasks [66,60]. Dureja and Rozier [26] reduce model-checking tasks via dependency analysis, while Finkbeiner et al. [30] extend this idea to synthesis by focusing on controllable variables. However, these simultaneous decomposition methods, which address the synthesis problem in parallel, struggle when requirements share many controlled variables, limiting their applicability [41,30,54]. In RE, highlevel state machines or modes, are commonly used to structure system specifications [35,70,71,63,4]. Languages like EARS [53], SCR [38,37], TLSF [42], SPIDER [43], NASA's FRET [33], and Spectra [51] leverage modes for organizing computation and requirements. This aligns with IEEE standard 29148, which notes that "some systems behave quite differently depending on the mode of operation. For example, a control system may have different features depending on its mode: training, normal, or emergency." [1]. State-based techniques like Statecharts [36], Broy's hierarchical service modeling [15], and the SCR method further formalize mode-based specifications. Feature modeling [25,67] and safety analysis methods like FTA and FMEA [46,2] also utilize modes. However, translating these mode-based specifications into LTL can increase complexity, hindering simultaneous methods. For example, NASA's FRET, which heavily uses state variables (modes), poses challenges for decomposition tools, leading to issues like false positives [54] and goal-conflicts [21,16,12,22,52,11]. This motivates the need for more efficient synthesis approaches in real-world applications. Recent RE research explores using Large Language Models (LLMs) for requirements specification [73,5,57,76,75,72]. LLMs, like GPT series [14,56], LaMDA [68], LLaMa [69], PaLM [18], and BERT [23], can assist in articulating mode-based requirements, potentially simplifying translation to LTL [47,3]. Related to us, Balkan et al. [6] use modes in a GR(1) subfragment for control design of continuous systems, focusing on quantitative performance. This contrasts with our focus on discrete systems and logical correctness in reactive synthesis. More directly, Brizzio et al. [13] proposed a mode-based decomposition for a fragment of safety, but require manual specification of initial conditions, which is tedious, error-prone, and deviates from standard RE practices. In contrast, our novel mode-based synthesis automatically generates initial conditions, ensuring consistency and eliminating manual intervention. Unlike simultaneous decomposition, we employ a sequential approach. By addressing one sub-problem at a time and leveraging natural mode transitions, our sequential method simplifies synthesis, reduces potential conflicts, and inherently ensures consistency. To the best of our knowledge, this is the first fully automated sequential mode-based synthesis method.

#### 2 Preliminaries

LTL is a logical formalism widely used to specify reactive systems [59,49]. Given a set of propositional variables AP, LTL formulas are defined using standard logical

connectives and the temporal operators  $\bigcirc$  (next) and  $\mathcal{U}$  (until) as follows:

$$\varphi ::= \mathtt{true} \, \big| \, a \, \big| \, \varphi \vee \varphi \, \big| \, \neg \varphi \, \big| \, \bigcirc \varphi \, \big| \, \varphi \, \mathcal{U} \, \varphi$$

where  $a \in \mathsf{AP}$ . Common operators, such as false,  $\land$  (and),  $\square$  (always), and  $\rightarrow$  (implies), can be derived:  $\phi \land \psi \equiv \neg(\neg \phi \lor \neg \psi)$ ,  $\square \phi \equiv \neg(\mathsf{true} \ \mathcal{U} \neg \phi)$ , and  $\phi \rightarrow \psi \equiv \neg \phi \lor \psi$ . Given an LTL formula  $\varphi$ ,  $Vars(\varphi) \subseteq \mathsf{AP}$  denotes the set of atomic propositions used in  $\varphi$ . The semantics of LTL associate traces  $\sigma \in \Sigma^{\omega}$  with formulae as follows (we omit the Boolean operators which are standard):

```
 \begin{split} \sigma &\vDash a & \text{iff} \ \ a \in \sigma(0) \\ \sigma &\vDash \bigcirc \varphi & \text{iff} \ \ \sigma^1 \vDash \varphi \\ \sigma &\vDash \varphi_1 \, \mathcal{U} \, \varphi_2 \text{ iff} \ \ \text{for some} \ i \geq 0 \ \ \sigma^i \vDash \varphi_2, \ \text{and for all} \ 0 \leq j < i, \sigma^j \vDash \varphi_1 \end{split}
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where  $\sigma(i)$  is the *i*-th letter of  $\sigma$ , and  $\sigma^i \in \Sigma^\omega$  is its suffix starting at the *i*-th position. Given  $L \subseteq \Sigma^\omega$  and a formula  $\varphi$ , we use  $L \vDash \varphi$  if for all  $\sigma \in L$ ,  $\sigma \vDash \varphi$ .

A Syntactic Fragment of Safety-LTL. We focus on a fragment of LTL (Safety-LTL) [77] commonly used in requirement engineering. LTL<sub>G</sub> consists of formulas of the form  $\psi \wedge \Box \varphi$ , where  $\psi$  is propositional and  $\varphi \in LTL_X$  (which uses only the  $\bigcirc$  operator). LTL<sub>G</sub> is widely used in industrial safety specifications [17,29,34]. Specifically, we work with  $\mathsf{GX}_0$  [13], a sub-fragment of Safety-LTL defined as  $\alpha \to (\beta \wedge \Box \psi)$ , where  $\alpha, \beta$ , and  $\psi$  are conjunctions in LTL<sub>X</sub>. This fragment extends  $LTL_G$ , still expresses safety properties, and is supported by tools like Strix [55]. A reactive specification  $\varphi = (A, G)$  consists of  $A = (\theta_e, \varphi_e)$ and  $G = (\theta_s, \varphi_s)$ , where  $\theta_{\{e,s\}}$  represent initial conditions for the environment and system, respectively, and  $\varphi_{\{e,s\}}$  are the assumptions and guarantees. Similar to previous works that restrict either the environment's or the system's specifications to simpler LTL fragments for efficiency [30,54,74] we simplify our approach. We use propositional formulas over  $Vars(\varphi)$  for A to ensure a consistent environment during decomposition, avoiding false-negatives [54], and employ  $G \in LTL_X$  for guarantees. Thus, the intended meaning of  $\varphi$  is the  $\mathsf{GX}_0$  formula:  $(\theta_e \to (\theta_s \land \Box(\varphi_e \to \varphi_s))).$ 

Reactive Synthesis. Reactive LTL synthesis [60] is the problem of automatically constructing a system that reacts to the environment guaranteeing an LTL specification  $\varphi$ . The propositions  $Vars(\varphi)$  are partitioned into  $\mathcal{X} \cup \mathcal{Y}$ , where  $\mathcal{X}$  are environment-controlled variables and  $\mathcal{Y}$  are system-controlled variables. A system strategy for  $\varphi$  is a function  $\rho: (2^{\mathcal{X}})^+ \to 2^{\mathcal{Y}}$  mapping finite sequences of  $\mathcal{X}$  valuations to  $\mathcal{Y}$  valuations. Given an infinite sequence  $X = X_1, X_2, \ldots \in (2^{\mathcal{X}})^{\omega}$ , the play induced by strategy  $\rho$  is the infinite sequence  $\sigma_{\rho,X} = (X_1 \cup \rho(X_1))(X_2 \cup \rho(X_1, X_2)) \ldots$  We use  $\mathcal{L}(\rho) = \{\sigma_{\rho,X} \mid X \in (2^{\mathcal{X}})^{\omega}\}$  for the set of plays played according to  $\rho$ . A play  $\sigma$  is winning if  $\sigma \models \varphi$ . A strategy is winning if  $\mathcal{L}(\rho) \models \varphi$ . Realizability is the problem of deciding if a specification has a winning strategy, and synthesis is the problem of computing one.

## 3 Mode-Based Synthesis for $\mathsf{GX}_0$

We now describe our mode-based reactive synthesis method for  $\mathsf{GX}_0$  specifications, beginning with some preliminary definitions. A mode m for a  $\mathsf{GX}_0$  formula  $\varphi$  is a predicate over  $Vars(\varphi)$  describing a set of system states. A set of modes  $M = \{m_0, \ldots, m_k\}$  is valid for  $\varphi$  if (1) modes are mutually exclusive  $(m_i, m_j \in M \text{ with } i \neq j, m_i \land m_j \text{ is unsatisfiable})$  and (2) they cover all possible states  $(\bigvee_{i=0}^k m_i \text{ is valid})$ . A mode-transition from m to a different mode n in a trace  $\sigma$  occurs at index i if  $\sigma(i) \vDash m$  and  $\sigma(i+1) \vDash n$ . A mode transition from m to n occurs under a strategy  $\rho$  if it occurs in some trace  $\sigma \in \mathcal{L}(\rho)$ .

**Definition 1 (Mode-Graph).** A mode-graph is a directed graph  $\mathcal{G} = (M, \prec)$ , where  $M = \{m_0, m_1, \ldots, m_k\}$  is a set of modes, and  $\prec \subseteq M \times M$  is irreflexive.

The intended meaning of  $\prec$  is to restrict the search of strategies to those where mode-transitions are included in  $\prec$ . In a *complete mode graph*,  $\prec = \{(m,n) \mid m,n \in M, m \neq n\}$  all possible mode transitions are included. In RE, it is common to specify modes and mode-transitions as part of the requirements, as discussed in Sections 1 and 2.

#### 3.1 Basic Mode-Based Synthesis with Initial Conditions

We first define a function reduce that, given a mode  $m \in M$ , and a formula  $\psi$ , it returns a reduced version of it which is specific to mode m:

```
\mathsf{reduce}(\psi, m) = \begin{cases} \mathsf{true}, & \text{if } (m \land \neg \psi) \vDash \mathsf{false} \ (1) \\ \mathsf{false}, & \text{if } (m \land \psi) \vDash \mathsf{false} \ (2) \\ \mathsf{Simpl}(\mathsf{reduce}(\psi_1, m) \bullet \mathsf{reduce}(\psi_2, m)), & \text{if neither } (1) \text{ nor } (2), \\ & \text{and } \psi = \psi_1 \bullet \psi_2 \\ & \text{for } \bullet \in \{\land, \lor, \rightarrow\} \end{cases} \mathsf{Simpl}(\neg \mathsf{reduce}(\psi', m)), & \text{if } \psi = \neg \psi' \\ \square \mathsf{Simpl}(\mathsf{reduce}(\psi', m)), & \text{if } \psi = \square \psi' \\ \bigcirc \mathsf{Simpl}(\mathsf{reduce}(\psi', m)), & \text{if } \psi = \bigcirc \psi' \\ \psi, & \text{otherwise} \end{cases}
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where  $\mathsf{Simpl}(\psi)$  is a function that applies standard Boolean simplifications, like  $(x \land \mathsf{true}) \mapsto x, (x \land \mathsf{false}) \mapsto \mathsf{false}, \text{ etc.}$ 

Example 1. Consider two modes  $m_0$  and  $m_1$  and the following  $\mathsf{GX}_0$  specification  $\varphi = ((\mathsf{true}, \mathsf{true}), (\mathsf{true}, \varphi_s))^{-1}$ , which, in this example, simplifies to  $\varphi = \Box \varphi_s$ , since all other components are  $\mathsf{true}$ . In this case,  $\varphi_s$  is given by:

$$G_1: \Box(e_1 \to (m_0 \to \bigcirc s_1)), \quad G_2: \Box(m_1 \to \bigcirc(\neg s_3 \lor m_0)), \quad G_3: \Box(\neg m_1 \to s_3)$$

<sup>&</sup>lt;sup>1</sup>For readability, throughout the paper, we assume that any unspecified components of  $\varphi$  are implicitly set to **true**, unless explicitly stated otherwise.

Since we only have  $\varphi_s$ , applying reduce  $(\varphi, m_0)$  projects<sup>2</sup>  $\varphi$  on mode  $m_0$ , resulting in a new set of guarantees  $G_1 : \Box (e_1 \to \bigcirc s_1)$ ,  $G_2 : \Box$  true and  $G_3 : \Box s_3$ . Particularly, in this example, the result of this projection represents the guarantees that remain relevant in mode  $m_0$ , forming a mode-specific specification. We denote this reduced version as  $\varphi \downarrow_{m_0}$ .

The key-stone of mode-based synthesis is that one can focus on simplified specifications for each of the given modes independently—which improves the scalability of off-the-shelf reactive synthesis tools. However, during an execution, a system can transition between different modes. In these transitions, the system may leave the satisfaction of sub-formulas pending for the arriving modes. We call these sub-formulas *pending obligations*, as they represent temporal constraints that must be ensured in future modes. The following definition captures those obligations that a mode must ensure are carried forward during transitions.

**Definition 2 (Pending Obligations).** Given a  $GX_0$  specification  $\varphi$  and a mode m, the set of (potential) pending obligations for m in  $\varphi$  is  $\mathcal{O}_{\varphi}^p(m) = \{\psi \mid \bigcirc \psi \in subformulas(\varphi_{\downarrow_m})\}$ .

Cumulative Obligations. Cumulative obligations, denoted  $\mathcal{O}_{\varphi}^{c}(m)$ , represent the obligations a mode m in a specification  $\varphi$  inherits from its predecessors during transitions. They ensure that all pending requirements are satisfied throughout the system. A straightforward, though imprecise, method to compute them is by aggregating obligations from all immediate predecessors. Particularly, for a given specification  $\varphi$  and a mode  $m_j : \mathcal{O}_{\varphi}^{c}(m_j) = \bigcup_{m_i \prec m_j} \mathcal{O}_{\varphi}^{p}(m_i)$ . The concept of cumulative obligations is illustrated in the following example.

Example 2. Consider a mode-graph with  $M = \{m_0, m_1, m_2\}$  and  $m_0 \prec m_1$ ,  $m_1 \prec m_2$  and  $m_2 \prec m_0$ . Let  $e_1$  and  $e_2$  be environment-controlled variables, with the system controlling  $\{s_1, s_2, s_3, s_4, m_0, m_1, m_2\}$ . The specification  $\varphi$  has the following guarantees  $\varphi_s$ :

$$\begin{array}{ll} \textbf{\textit{G}}_{\textbf{1}}: \Box(e_1 \rightarrow (m_0 \rightarrow (\bigcirc m_1 \land \bigcirc \bigcirc s_2))) & \textbf{\textit{G}}_{\textbf{2}}: \Box(e_2 \rightarrow (\neg m_2 \rightarrow \bigcirc \bigcirc (\neg s_3 \lor s_1))) \\ \textbf{\textit{G}}_{\textbf{3}}: \Box(\neg e_1 \rightarrow (m_1 \rightarrow \bigcirc (m_2 \land s_4))) & \textbf{\textit{G}}_{\textbf{4}}: \Box(m_2 \rightarrow \bigcirc (m_0 \land s_1)) \end{array}$$

The reduced specifications for modes  $m_0$ ,  $m_1$ , and  $m_2$  are  $\varphi_{\downarrow m_0} = G_1 \wedge G_2$ ,  $\varphi_{\downarrow m_1} = G_2 \wedge G_3$ , and  $\varphi_{\downarrow m_2} = G_4$ . When the system transitions from  $m_0$  to  $m_1$  while  $e_1$  holds, mode  $m_1$  must fulfill the pending obligation  $\bigcirc s_2$ . The exact obligations, however, depend on the order of events. If  $e_2$  occurs before  $e_1$ , the system must satisfy both  $\neg s_3 \vee s_1$  and  $\bigcirc s_2$ . On the other hand, if  $e_1$  and  $e_2$  occur simultaneously, the system must ensure  $\bigcirc(\neg s_3 \vee s_1)$  along with  $\bigcirc s_2$ . If the system is in mode  $m_1$  and  $e_1$  does not hold, the system transitions to  $m_2$  leaving the pending obligation  $m_2 \wedge s_4$ . The pending obligations for  $m_0$ ,  $m_1$  and  $m_2$  are:

$$\mathcal{O}^p_{\varphi}(m_0) = \{ m_1, \bigcirc s_2, s_2, \bigcirc (\neg s_3 \vee s_1), \neg s_3 \vee s_1 \}$$
 
$$\mathcal{O}^p_{\varphi}(m_1) = \{ \bigcirc (\neg s_3 \vee s_1), \neg s_3 \vee s_1, m_2 \wedge s_4 \}$$
 
$$\mathcal{O}^p_{\varphi}(m_2) = \{ m_0 \wedge s_1 \}$$

<sup>&</sup>lt;sup>2</sup>Throughout the paper, we use "reduced spec." and "projection" interchangeably.

Algorithm 1: Fixpoint Algorithm for Cumulative Obligations.

```
1 Input: \varphi = (A, G), M = \{m_0, \dots, m_k\} \ \mathcal{G} = (M, \prec)
2 for m_j \in M do
3 \left| \begin{array}{c} \mathcal{O}_{\varphi}^c[m_j] \leftarrow \bigcup_{m_i \prec m_j} \mathcal{O}_{\varphi}^p(m_i) \\ \text{4 } changed \leftarrow \text{true} \end{array} \right|
5 while changed do
6 \left| \begin{array}{c} changed \leftarrow \text{false} \\ \text{7} \quad \text{for } (m_i, m_j) \in \prec \text{do} \\ \text{8} \quad \left| \begin{array}{c} \text{for } u \in \bigcup_{\emptyset^k p \in \mathcal{O}_{\varphi}^c[m_i]} \\ \text{9} \quad \left| \begin{array}{c} \text{if } u \notin \mathcal{O}_{\varphi}^c[m_j] \text{ then} \\ \text{10} \quad \left| \begin{array}{c} \mathcal{O}_{\varphi}^c[m_j] \leftarrow \mathcal{O}_{\varphi}^c[m_j] \cup \{u\} \ ; \ changed \leftarrow \text{true} \\ \text{11 } \text{return } \mathcal{O}_{\varphi}^c \end{array} \right|
```

From this, the *cumulative obligations* would be:  $\mathcal{O}_{\varphi}^{c}(m_{0}) = \mathcal{O}_{\varphi}^{p}(m_{2}); \mathcal{O}_{\varphi}^{c}(m_{1}) = \mathcal{O}_{\varphi}^{p}(m_{0}); \mathcal{O}_{\varphi}^{c}(m_{2}) = \mathcal{O}_{\varphi}^{p}(m_{1}).$ 

However, as mentioned before, this simple aggregation is *imprecise* and can lead to incorrect results. It fails to capture interactions between obligations arising from mode transitions in execution paths as demonstrated in the following example.

Example 3. Consider a specification with the following guarantees:

$$G_1 : \Box(m_0 \to \bigcirc m_1)$$
  $G_2 : \Box(m_1 \to \bigcirc m_0)$   
 $G_3 : \Box(m_0 \to \bigcirc \bigcirc p)$   $G_4 : \Box(m_0 \to p)$ 

Here,  $\mathcal{O}^p_{\varphi}(m_0)$  includes  $\{m_1, \bigcirc p, p\}$ , while  $\mathcal{O}^p_{\varphi}(m_1)$  only contains  $\{m_0\}$  because  $\varphi_{\downarrow m_1}$  retains only  $\mathbf{G_2}$ . If we propagate obligations naively without accounting for mode interactions,  $\mathcal{O}^c_{\varphi}(m_0)$  might incorrectly focus on satisfying only  $m_0$ , neglecting the obligation to satisfy p. This error occurs because  $\mathbf{G_2}$  forces  $m_1$  to transition back to  $m_0$  after just one step, leaving p—which originates from the obligation  $\bigcirc p$  that  $m_0$  transferred to  $m_1$ —unsatisfied. Such propagation would compromise the correctness of the specification leading to false negatives.

Particularly, cumulative obligations encode the temporal dependencies between modes, ensuring that obligations inherited from predecessors are correctly maintained throughout transitions. Unlike pending obligations, which define future requirements for a specific mode, cumulative obligations ensure that a mode is prepared to handle any inherited constraints. Alg. 1 systematically computes all cumulative obligations that a mode may need to satisfy, ensuring the global specification is correctly enforced across transitions. This algorithm iterates until a stable set of obligations is computed. Given a valid set of modes  $M = \{m_0, m_1, \ldots, m_k\}$ , the cumulative obligations  $\mathcal{O}_{\varphi}^c(m)$  for each  $m \in M$  are the conditions that m may be forced to satisfy based on the pending obligations

inherited from all its predecessors. Each  $\mathcal{O}_{\varphi}^{c}(m)$  is computed using Alg. 1. Revisiting Ex. 3, Alg.1 iteratively determines that when transitioning from  $m_1$  to  $m_0$ ,  $m_0$  must satisfy both p and itself, resulting in  $\mathcal{O}_{\varphi}^{c}(m_0) = \{p, m_0\}$ .

We refer to the set  $\mathcal{O} = \bigcup_{m \in M} \mathcal{O}_{\varphi}^{c}(m)$  as the universe of obligations across different modes. To systematically explore the universe of obligations, we introduce obligation variables that encode each element within this universe, encoding whether the corresponding obligation is considered. We introduce a fresh variable  $v(\varphi)$  for each formula  $\varphi \in \mathcal{O}$ . We use  $v(\varphi)$  for the variable corresponding to  $\varphi$ . While Alg. 1 performs the correct propagation of obligations, the set of obligations computed is a superset of the obligations that a mode may be requested to fulfill. Asking an instance of a mode to satisfy a larger subset of the cumulative obligations makes the instance more difficult to be realizable, while it helps predecessor instances to be realizable. The concept of initial condition captures this notion of combination of obligations.

**Definition 3 (Initial Conditions).** Given a mode  $m \in M$  and a specification  $\varphi = ((\theta_e, \varphi_e), (\theta_s, \varphi_s))$ , the set of initial conditions  $\mathcal{I}_{\varphi}(m)$  is the set of all possible conjunctions of subsets of cumulative obligations:

$$\mathcal{I}_{\varphi}(m) = \left\{ \bigwedge_{\phi \in \mathcal{S}} \phi \wedge m \; \middle| \; \mathcal{S} \subseteq \mathcal{O}_{\varphi}^{c}(m) \right\}.$$

Additionally, for the **initial mode**  $m_0$ , the original system constraint  $\theta_s$  from  $\varphi$  is always included in its initial conditions

$$\mathcal{I}_{\varphi}(m_0) = \left\{ \bigwedge_{\phi \in \mathcal{S}} \phi \wedge (\theta_s \wedge m_0) \mid \mathcal{S} \subseteq \mathcal{O}_{\varphi}^c(m_0) \right\}.$$

Example 4. Consider  $\mathcal{O}_{\varphi}^{c}(m_0) = \{\bigcirc q, \bigcirc r\}$  and  $\mathcal{O}_{\varphi}^{c}(m_1) = \{s\}$ . The initial conditions are:  $\mathcal{I}_{\varphi}(m_0) = \{\bigcirc q \land \bigcirc r, \bigcirc q, \bigcirc r, m_0\}$  and  $\mathcal{I}_{\varphi}(m_1) = \{s, m_1\}$ . <sup>3</sup>

Mode-Based Synthesis For clarity, in the following definitions we abuse notation by modifying the order of elements in the specification without altering its intrinsic meaning (see Sec. 2). We denote a specification as  $(\theta_e, \theta_s, \varphi_m^R)$  where  $\varphi_m^R$  represents the reduction of both assumptions and guarantees onto mode m. This reduced specification introduces fresh variables to ensure proper transitions between modes, a process handled by Alg. 2. Given a set of valid modes  $M = \{m_0, \ldots, m_k\}$ , a mode-graph  $\mathcal{G} = (M, \prec)$ , and a  $\mathsf{GX}_0$  specification  $\varphi = (A, G)$ , the mode-based synthesis method constructs a set of mode-specific projections, where each projection for a mode  $m_i$  is defined as:  $\Pi_{m_i} = (\theta_{e_i}, \theta_{s_i}, \varphi_{m_i}^R)$ . We denote the full set of projections as:  $\Pi = \{\Pi_{m_0}, \Pi_{m_1}, \ldots, \Pi_{m_k}\}$ . If each projection is realizable the resulting strategies can be composed into a strategy satisfying

<sup>&</sup>lt;sup>3</sup>Notice that, each  $x \in \mathcal{I}_{\varphi}(m)$  is conjoined with m. For readability, it is omitted from the text.

```
env boolean reset, start;
  sys Int(1..20) counter; sys boolean trigger;
   // Start and reset are not initially pressed
  asm (!reset and !start);
   // Only reset or start can be active at a time
  asm G !(reset and start);
      Counter is initially at the lowest value -- theta_s
  gar counter=1;
      Everything beyond this point corresponds to varphi_s
   // Restart signal always reset the count
10
  gar G (reset -> next(counter=1) );
  // Always stay at the same number or increase it
gar G ((counter=1 and start) -> next(counter=2 or reset));
gar G ((counter=2 and !reset) -> next(counter=3 or reset));
  gar G ((counter=19 and !reset) -> next(counter=20 or reset));
// Only trigger a signal if the bound is reached.
gar G (counter=20 <-> trigger);
   ^{\prime}/ Reach the limit and start again
  gar G (counter=20 -> next(counter=1));
```

Fig. 1: Counter machine example written in Spectra.

the original specification. When referring to a specific component of a projection, we use  $\Pi_{m_i} \cdot \theta_{e_i}$ ,  $\Pi_{m_i} \cdot \theta_{s_i}$ , and  $\Pi_{m_i} \cdot \varphi_{m_i}^R$  to extract the corresponding initial conditions or the reduced specification, respectively. Importantly, only the projection for the initial mode  $m_0$  includes  $\theta_e$ , while all other projections replace it with true. This follows from the definition of a  $\mathsf{GX}_0$  specification (see Sec. 2), where environment assumptions are expressed as propositional formulas. Since the environment does not maintain state across modes, there is no requirement for  $\theta_e$  to persist beyond the initial mode. Instead,  $\theta_e$  in  $m_0$  encodes the initial values of the environment, ensuring consistency with the original specification  $\varphi$ . After the initial mode,  $\theta_e$  is universally quantified, effectively becoming true. It is worth bearing in mind that if the mode-transition relation ≺ is not explicitly provided by the engineer, the process assumes a complete graph over M, meaning all modes are initially considered reachable from one another. The synthesis procedure will then refine the mode transitions based on realizability constraints. This formulation ensures that the synthesized controller correctly handles mode transitions while maintaining consistency with the original specification. In particular, this process must satisfy the following objectives:

- (1) Ensure that each projection  $(\theta_{e_i}, \theta_{s_i}, \varphi_{m_i}^R) \in \Pi$  is realizable. Even though the local strategy generated is infinite, it could decide to jump into a successor node and move into a winning sink state.
- (2) For each  $m_i \prec m_j$  the successor mode  $m_j$  must satisfy the obligations inherited from its predecessor  $m_i$ , denoted as  $\mathcal{O}^p_{\varphi}(m_i) \cap \mathcal{O}^c_{\varphi}(m_j)$ . The connection between projections is managed through the initial condition  $\theta_{s_j} \in \mathcal{I}_{\varphi}(m_j)$  of the successor  $m_j$ . Formally, for all  $m_i \prec m_j$ ,  $\theta_{s_j} \Longrightarrow \bigwedge(\mathcal{O}^p_{\varphi}(m_i) \cap \mathcal{O}^c_{\varphi}(m_j))$ . This ensures that a transition from  $m_i$  to  $m_j$  is valid only if  $m_j$  satisfies the pending obligations of mode  $m_i$ , ensuring the correct connection and composition of projections for sequential decomposition.

Alg. 2 adapted from [13], outlines the mode-based synthesis approach. The algorithm generates a projection  $\Pi_{m_i}$  for each mode  $m_i$ , ensuring consistency

### Algorithm 2: Sequential Decomposition.

```
1 Input: \varphi = ((\theta_e, \varphi_e), (\theta_s, \varphi_s)), \mathcal{I}, M' \subseteq M, \mathcal{G} = (M, \prec), \mathcal{O}
  2 \mathcal{O}_{\text{sorted}} = \text{sortBySize}(\mathcal{O},\downarrow) \; ; \; \theta'_s \leftarrow \texttt{true}
  3 for \psi \in \theta_s do
               \psi' \leftarrow \psi[f \backslash v(f)] for all f \in \mathcal{O}_{\text{sorted}}
               \theta_s' \leftarrow \theta_s' \wedge \psi'
  6 for m_i \in M' do
               \varphi_s' \leftarrow \varphi_s \downarrow_{m_i} ; \varphi_{m_i}^R \leftarrow \mathsf{true}
   7
               for \psi \in \varphi'_s do
                    \psi' \leftarrow \psi[f \setminus v(f)] \text{ for all } f \in \mathcal{O}_{\text{sorted}}
\varphi_{m_i}^R \leftarrow \varphi_{m_i}^R \wedge (\neg done \rightarrow \psi')
10
               for \bigcirc^k p \in \mathcal{O}_{sorted} do
11
                      \text{if } k = 1 \text{ then } \varphi_{m_i}^R \leftarrow \varphi_{m_i}^R \wedge \left( (\neg done \wedge v(\bigcirc p)) \rightarrow \bigcirc (\neg done \rightarrow p) \right); 
12
                    else \varphi_{m_i}^R \leftarrow \varphi_{m_i}^R \wedge ((\neg done \wedge v(\bigcirc^k p)) \rightarrow \bigcirc(\neg done \rightarrow v(\bigcirc^{k-1} p)));
13
               for (m_i, m_j) \in \prec, and \bigcirc^k p \in \mathcal{O}_{sorted} and c_j \in \mathcal{I} do 
 \mid if (c_j \land \neg v(\bigcirc^{k-1} p)) is sat then \varphi_{m_i}^R \leftarrow \varphi_{m_i}^R \land (jump_j \rightarrow \neg v(\bigcirc^{k-1} p));
14
               \varphi_{m_i}^R \leftarrow \varphi_{m_i}^R \wedge ((\bigvee_{(m_i, m_j) \in \prec} jump_j) \rightarrow \bigcirc done)
16
               \varphi^R_{m_i} \leftarrow \varphi^R_{m_i} \land ((\neg \bigvee_{(m_i, m_i) \in \prec} jump_j) \rightarrow (\neg done \rightarrow \bigcirc \neg done))
17
               \varphi_{m_i}^R \leftarrow (\varphi_e \downarrow_{m_i} \rightarrow \varphi_{m_i}^R)
18
               \Pi[i] \leftarrow (\theta_e, \theta_s', \varphi_{m_i}^R)
19
20 return \Pi
```

Here,  $\varphi[\phi \setminus \psi]$  is the formula obtained by replacing occurrences of  $\phi$  in  $\varphi$ , by  $\psi$ . If M' is a singleton set, Alg. 2 returns a projection specialized for that mode only.

with **manually provided** initial conditions (as required in point (2)). To demonstrate the method, consider the specification  $\varphi$  in Fig. 1. Due to its complexity and the number of variables, traditional synthesis tools often struggle with this kind of specifications. Assume that each mode corresponds to a *unique* counter value. For instance, in mode  $m_{20}$  (i.e., counter=20), the approach projects the global specification onto  $m_{20}$  relying on manually specified initial conditions.

Alg. 2 ensures that the projection  $\Pi_{m_{20}}$ , along with others, is consistent with the global specification  $\varphi$ . A reduced version of this projection is shown in Fig. 2. Transitions between modes are modeled using fresh variables jump (indicating a transition to a successor mode) and done (indicating that the game for the current mode is completed because a jump transition was taken). A transition is allowed only if the initial condition of the successor mode satisfies the obligations at the time of the jump. This guarantees that if the set of reduced specifications is realizable then the strategies for the different modes can be properly connected. Although Alg. 2 significantly improves synthesis speed, it introduces two limitations: (1) Each mode is associated with a single initial condition, and (2) these initial conditions must be manually specified. To address (1), Alg. 2 can be modified to iterate over a sequence of sets of possible initial conditions candidates (initial-universe)  $\mathcal{I}_{\varphi}^{u} = [\{\theta_{s_i}^1, \ldots, \theta_{s_i}^k\}, \{\theta_{s_j}^1, \ldots, \theta_{s_j}^k\}, \ldots]$ , updating the jump variable to jump i for each different initial condition required.

```
env boolean reset, start;
sys Int(20) counter;
sys Int(1) jump;
sys boolean trigger, done, o_1;
//varphi_e and theta_s
asm G !(reset and start);
gar (counter=20 and ! done);
*/ varphi_s
gar G (!done -> trigger);
gar G (!done -> counter=20->o_1);
gar G (!done -> reset -> o_1);
gar G (!done and o_1->next(done));
gar G (jump=1 -> next(done));
gar G (done -> next(done));
```

Fig. 2: Reduced specification  $\Pi_{m_{20}}$ .

However, the second problem remains a significant challenge due to the considerable manual effort required to provide the initial conditions. To address this, we propose here a method for automatic mode-based synthesis that computes these initial conditions automatically. Alg 2 then generates projections using these automatically computed initial conditions  $\mathcal{I}_{\varphi}$ , ensuring realizability and consistency.

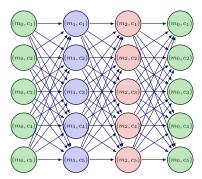
#### 3.2 A Fixpoint Search Method for Initial Conditions

We now propose an automatic mode-based synthesis method that eliminates the need for manually specifying initial conditions. The key challenge is to discover a set of feasible initial conditions for each mode, ensuring that all projections remain realizable. For this, we consider multiple instances of a mode  $m_j$ , each corresponding to an initial condition  $c_k$ . An instance of a mode with an initial condition supports its predecessors allowing them to jump, and at the same time it uses its successors to attempt its own realizability. The core task is to verify the realizability of each projection  $\Pi_{m_i} = (\theta_{e_i}, \theta_{s_i}, \varphi_{m_i}^R)$ . Since we have already established that  $\theta_e$  appears only in the initial mode and is implicitly **true** in all others, this holds throughout the following definitions.

If an instance is found to be unrealizable with a given collection of successor instances it must be removed and its predecessors realizability need to be reestablished. We will provide a fixpoint search technique that iteratively refines the space of possible initial conditions until a stable solution is found. The following definition captures the set of possible instantiations.

**Definition 4 (Mode Instance Graph).** Given a set of modes M, a specification  $\varphi$ , a set of initial conditions  $\mathcal{I}_{\varphi}(m)$  for each mode  $m \in M$ , and a mode graph  $\mathcal{G} = (M, \prec)$ , a Mode Instance Graph (MIG) is a graph  $(V, \mapsto)$ , where:

```
- V \subseteq \{(m, c) \mid m \in M \text{ and } c \in \mathcal{I}_{\varphi}(m)\} \\ - \mapsto = \{((m_i, c_j), (m_k, c_l)) \mid (m_i, c_j) \in V, (m_k, c_l) \in V \text{ and } m_i \prec m_k)\}
```



In a mode instance graph, each mode is instantiated with possibly many initial conditions, which are connected to every instance of its successor nodes. For example, consider a mode-graph with three modes  $m_0, m_1$ , and  $m_2$  and  $m_0 \prec m_1 \prec m_2 \prec m_0$ . Assume each mode has five possible initial conditions, denoted as  $(m_i, c_k)$ . The universe of exploration in the diagram on the left illustrates the MIG for this simplified example. Our approach systematically evaluates each configuration to ensure

that all projections meet the realizability requirements. Informally, our method iterates through the MIG to find a *proof of realizability* for the specification  $\varphi$ , pruning the search space as unfeasible nodes are found. Formally:

**Definition 5 (Realizability Proof).** Given a  $GX_0$  specification defined as follows:  $\varphi = ((\theta_e, \varphi_e), (\theta_s, \varphi_s))$ , a Mode Instance Graph MIG =  $(V, \mapsto)$ . A proof of realizability is a subgraph  $\mathcal{R} = (V', \mapsto')$ , where  $V' \subseteq V$  and  $\mapsto' \subseteq \mapsto$  such that for every projection  $\Pi_{m_i} = (\theta_{e_i}, c_k, \varphi_{m_i}^R)$  generated from  $(m_i, c_k) \in V'$ , the following holds:

$$(\Pi_{m_i}.\theta_{e_i} \to (\Pi_{m_i}.c_k \wedge \square(\Pi_{m_i}.\varphi_{m_i}^R)))$$

where

$$\Pi_{m_i}. heta_{e_i} = egin{cases} heta_e & \textit{if } m_i = m_0, \ ext{true} & \textit{otherwise}. \end{cases}$$

A realizability proof guarantees the realizability of  $\varphi$  (Thm. 2). Alg. 3 systematically searches for subsets of the initial conditions, one for each node, aiming to find a realizability proof. The search begins with a candidate proof by creating the MIG that contains all modes and their initial conditions. The algorithm proceeds in rounds, where at each round all remaining instance modes are checked for realizability. At each round unrealizable instances are removed. The process iterates until a fixpoint is reached, that is when a full round is passed with all remaining instances are realizable. The resulting graph is a realizability proof  $\mathcal R$  (or a graph that does not contain any instance of the initial mode). A modification of Alg. 3, allows us to bypass many realizability checks by inferring results

## **Algorithm 3:** Search for Init. Cond.

```
1 Input: \varphi = ((\theta_e, \varphi_e), (\theta_s, \varphi_s)), M = \{m_0, \dots, m_n\}, \mathcal{G} = (M, \prec),
  2 \mathcal{O}_{\varphi}^c \leftarrow \mathsf{Alg}\ 1(\varphi, M, \mathcal{G})\ ; \ \mathcal{O} \leftarrow \bigcup_{m \in M} \mathcal{O}_{\varphi}^c(m)
  3 for m_i \in M do \mathcal{I}_{\varphi}^u[i] \leftarrow \mathcal{I}_{\varphi}(m_i);
  4 \mathrm{MIG} \leftarrow \mathsf{createMIG}(\mathcal{G}, \mathcal{I}_{\varphi}^u)
  5 do
  6
             initial \leftarrow \texttt{false}; finished \leftarrow \texttt{true}
  7
             for m_i \in M do
                 foreach candidate c \in \mathcal{I}_{\varphi}^{u}[i] do
   8
                      if m_i = m_0 then
   9
                         (\theta_{e_i}, \theta_{s_i}, \varphi_{m_i}^R) \leftarrow \mathsf{Alg} \ 2(((\theta_e, \varphi_e), (c, \varphi_s)), \mathcal{I}_{\varphi}^u, \{m_i\}, \mathcal{G}, \mathcal{O}) \ ;
                       else (\theta_{e_i}, \theta_{s_i}, \varphi_{m_i}^R) \leftarrow \text{Alg } 2(((\text{true}, \varphi_e), (c, \varphi_s)), \mathcal{I}_{\varphi}^u, \{m_i\}, \mathcal{G}, \mathcal{O}) ;
10
                       if realizable(\theta_{e_i}, \theta_{s_i}, \varphi_{m_i}^R) then
11
                        | if m_i = m_0 then initial \leftarrow \text{true};
12
                      else
13
14
                            finished \leftarrow \texttt{false}
                            MIG \leftarrow MIG \setminus (m_i, c)
15
                            \mathcal{I}_{\varphi}^{u}[i] \leftarrow \mathcal{I}_{\varphi}^{u}[i] \setminus c
16
       while \neg finished \land \neg initial;
18 return MIG
```

based on previous checks in the same iteration, using memoization [9]. For instance, if a mode with candidate initial condition  $p \wedge q$  is realizable, the same mode will also be realizable for initial conditions p, q and true. Conversely, if p is unrealizable, all initial conditions that imply p are also unrealizable. For example, in our case study (Fig. 1), we defined two modes,  $m_0$  that spans from 1 to 10, while  $m_1$  spans from 11 to 20. Our algorithm efficiently computes the initial conditions as follows: for  $m_0$ , it deduces the initial condition  $\theta_0 = \text{counter=1}$ , and for  $m_1$ , it derives the initial condition  $\theta_1 = \text{counter=11}$ . These results align with those manually derived in [13].

#### 3.3 Correctness

This section establishes the soundness of the mode-based synthesis algorithm. We begin by stating a previously established result.

**Theorem 1 (Soundness of Alg. 2 [13]).** Let  $\varphi$  be a  $GX_0$  specification,  $\mathcal{G} = (M, \prec)$  a mode-graph, and  $\mathcal{I}_{\varphi}(m)$  the set of initial conditions for each mode  $m \in M$ . If every mode projection  $\Pi_m$  is realizable, then the original specification  $\varphi$  is realizable.

The following theorem formalizes the soundness of Alg. 3.

Theorem 2 (Soundness of Alg. 3). Let  $\varphi = ((\theta_e, \varphi_e), (\theta_s, \varphi_s))$  be a  $GX_0$  specification. If Alg. 3 returns a realizability proof  $\mathcal{R}$ , then  $\varphi$  is realizable.

Proof (sketch). We proceed by induction on the length of the environment sequence  $X_1, \ldots, X_T$  in a trace  $\sigma$ . We define a function  $\gamma(\sigma, t)$  that returns the mode active at time t in the trace  $\sigma$ . That is, at every step t, the system is in mode  $m_t = \gamma(\sigma, t)$ . The algorithm constructs a realizability proof  $\mathcal{R}$ , where each mode  $m_j$  is associated with a projection  $\Pi_{m_j} = (\theta_{e_j}, \theta_{s_j}, \varphi_{m_j}^R)$  that is realizable. The set  $\mathcal{I}_{\varphi}(m_j)$  ensures that  $\theta_{s_j}$  is a valid initial condition, satisfying all required obligations. The proof proceeds as follows:

- Base case: The system starts in mode  $m_0 = \gamma(\sigma, 0)$  with the initial environment valuation  $X_1$ . The algorithm constructs the realizability proof  $\mathcal{R}$  by iterating over all candidate initial conditions  $\theta_{s_0} \in \mathcal{I}_{\varphi}(m_0)$ , checking the realizability of  $\Pi_{m_0} = (\theta_e, \theta_{s_0}, \varphi_{m_0}^R)$ . If  $\theta_{s_0}$  is realizable, the algorithm keeps  $(m_0, \theta_{s_0})$  in  $\mathcal{R}$ . Since the algorithm removes all unrealizable candidates, the existence of  $(m_0, \theta_{s_0}) \in \mathcal{R}$  guarantees that the system has a valid strategy satisfying  $\Pi_{m_0}$  at t = 0. Therefore, the base case holds.
- **Inductive step:** Assume that for all previous steps t < T, the system was realizable under its respective projections, i.e., for all  $X_1, \ldots, X_t$ , the corresponding mode projections  $\Pi_{\gamma(\sigma,t)}$  were realizable. Now consider the next time step T, and let  $m_j = \gamma(\sigma, T)$  be the mode active at T. The algorithm ensures that the realizability of  $\Pi_{m_j}$  holds for some initial condition candidate  $\theta_{s_j} \in \mathcal{I}_{\varphi}(m_j)$ . There are two possible cases:

- 1. The system remains in mode  $m_j$ . Since the system does not transition, the active mode remains  $m_j = \gamma(\sigma, T)$ . By the inductive hypothesis, the game has been realizable up to step T-1. By the algorithm's construction,  $(m_j, c_k) \in \mathcal{R}$  only if  $\Pi_{m_j}$  was realizable for some  $c_k \in \mathcal{I}_{\varphi}(m_j)$ . This means that there exists a valid strategy for  $m_j$  that satisfies  $\Pi_{m_j}$  at time T. If the system were not realizable at T, then the environment would have a winning strategy leading to an unrealizable state. However, such a strategy would necessarily include the initial state, contradicting our hypothesis that the game was realizable up to T-1. Therefore, staying in the same mode ensures that the system remains realizable, ensuring that the specification is not violated at step T.
- 2. The system transitions to a new mode  $m_k$ . The algorithm only allows a transition from  $m_j$  to  $m_k$ , i.e.,  $\gamma(\sigma, T-1) \neq \gamma(\sigma, T)$ , if there exists an initial condition  $\theta_{s_k} \in \mathcal{I}_{\varphi}(m_k)$  that satisfies the pending obligations from  $m_j$ , i.e.,  $\mathcal{O}_{\varphi}^p(m_j) \cap \mathcal{O}_{\varphi}^c(m_k)$ . If no such initial condition existed, the system would remain in  $m_j$ , contradicting the assumption that a transition to  $m_k$  occurred. As stated in Def. 3,  $\mathcal{I}_{\varphi}(m_k)$  includes all possible obligations that  $m_k$  may inherit and since the algorithm removes all unrealizable candidates from  $\mathcal{I}_{\varphi}(m_k)$ , any transition to  $m_k$  necessarily maintains realizability.

At each step, the system either (1) remains in the same mode with a winning strategy or (2) transitions to a realizable mode, ensuring correctness throughout the execution. Hence,  $\varphi$  is realizable.

Thm. 2 establishes soundness but not completeness. Our approach forces the system to choose the next mode based solely on the current history, disregarding the next environment input. This can lead to false negatives. Consider the specification  $\square(m_0 \to \bigcirc(e \lor m_1))$ , where  $m_0, m_1$  are modes and e is an environment input. This requires that if the system is in  $m_0$ , then in the next step, either e is true or the system transitions to  $m_1$ . Our approach must decide whether to transition to  $m_1$  before observing e. However, a winning strategy might depend on e: stay in  $m_0$  if e is true, and transition to  $m_1$  otherwise. For instance, if e alternates, a winning strategy is to stay in  $m_0$  while e is true and move to  $m_1$  when e is false. Our method, however, must choose between always staying in  $m_0$  (violating the spec when e is false) or always transitioning to  $m_1$  (unnecessarily when e is true), incorrectly reporting unrealizability. This illustrates that premature decision-making can lead to false negatives. To mitigate incompleteness, we define a sufficient condition: mode-determinism, ensuring that the current variable valuation uniquely determines the next mode without violating the specification. For a  $\mathsf{GX}_0$  formula  $\Box \psi^4$  with variables  $\overline{z} = \mathit{Vars}(\psi) = \mathcal{X} \cup \mathcal{Y}$ , let  $\varphi(\overline{z}, \overline{z}')$  be the relation between pre- and post-state variables satisfying  $\varphi$ .

**Definition 6 (Mode-deterministic).** A specification  $\Box \psi$  with  $\overline{z} = Vars(\psi)$  is mode-deterministic if there are no modes  $m_1$ ,  $m_2$  and  $m_3$  (with  $m_2 \neq m_3$ )

<sup>&</sup>lt;sup>4</sup>Nested  $\bigcirc$  operators in  $\psi$  are handled by introducing fresh variables  $v_{\bigcirc\alpha} \iff \bigcirc\alpha$ .

$$s.t.$$
:

$$\exists \overline{z}, \overline{z}_2, \overline{z}_3. (m_1(\overline{z}) \wedge (\psi(\overline{z}, \overline{z}_2) \wedge m_2(\overline{z}_2)) \wedge (\psi(\overline{z}, \overline{z}_3) \wedge m_3(\overline{z}_3))).$$

This condition ensures that no state in  $m_1$  can transition simultaneously to  $m_2$  and  $m_3$ . Mode-determinism can be verified using  $k^3$  SAT queries, where k is the number of modes. For a mode-deterministic specification  $\varphi$  the feasible mode jumps  $J(\varphi) \subseteq M \times M$  are defined as:  $(m_i, m_j) \in J$  if:  $\exists \overline{z}, \overline{z}'. (m_i(\overline{z}) \wedge \psi(\overline{z}, \overline{z}') \wedge m_j(\overline{z}'))$ . This captures possible mode transitions within a  $\varphi$  trace.

**Theorem 3.** Let  $\varphi$  be a mode-deterministic specification and let  $(M, \prec)$  be a mode-graph with  $J(\varphi) \subseteq \prec$ . If the mode-based synthesis algorithm returns unrealizable, then  $\varphi$  is unrealizable.

It is always possible to choose a proper  $\prec$  either by computing  $J(\varphi)$  or by using the complete mode-graph.

## 4 Empirical Evaluation

We evaluate our approach against the following research questions:

- RQ1: How effectively does our method compute initial conditions?
- RQ2: Does our mode-based technique improve controller synthesis time compared to traditional methods?
- **RQ3:** How well does our heuristic prune the search space?

Specifications. For our evaluation, we use benchmarks from [13] alongside new specifications from recent work [61,52], written in languages like Spectra and FRET. Additionally, we adapt each specification to mode-based determinism and we introduce goal-conflicts to make them unrealizable to test whether our method correctly identifies those scenarios. Table 1 summarizes the specifications, including

#A - #G	#M
2-(n+5)	2,4,7
2-7	3
3-4	3
3-15	4
1-9	3
1-187	3
2-14	4
2-(2n + 5)	2,4,7
	2-(n+5) 2-7 3-4 3-15 1-9 1-187 2-14

Table 1: Specs and Modes.

assumptions (#A), guarantees (#G), and modes (#M).

**Setting and Evaluation.** We implemented our approach in Java, leveraging the widely-used OwL library [44] for parsing and manipulating LTL formulas. For verifying realizability, we used Strix [55]. The experiments were conducted on a cluster featuring Intel Xeon processors clocked at 2.6GHz and equipped with 16GB of RAM, running GNU/Linux. Our tool, case studies, and instructions for replicating the experiments are in our replication package <sup>5</sup>.

<sup>&</sup>lt;sup>5</sup>https://sites.google.com/view/mode-decomposition

Specifications		Time (s)		#Strix		Detailed time (fp)		
Name.	#M	Strix	fp	fph	fp	fph	Proj.	Real.
counter-10	2	8	0.56	0.49	2	2	0.41	0.15
counter-14	7	timeout	1.23	1.31	7	7	0.82	0.41
counter-20	4	timeout	1.01	1.06	4	3	0.80	0.27
lift-15	3	timeout	4.03	4.00	5	4	3.44	0.59
sis-1500	3	timeout	46.40	43.00	3	2	42.84	3.92
double-counter-10	2	6	0.80	0.73	3	3	0.60	0.20
double-counter-14	7	94	3.23	2.96	13	10	2.23	1.00
double-counter-20	4	timeout	2.62	2.56	7	5	2.14	0.48
cruise-fse	4	timeout	65.37	63.89	11	9	45.86	19.51
altlayer	3	timeout	18.13	18.52	4	4	15.83	2.30
fret-lift	4	timeout	27.48	27.12	4	4	21.80	5.68
thermostat-80	3	60	29.64	29.66	3	2	27.40	2.24
thermostat-150	3	ERROR	60.53	55.41	3	2	52.68	7.85

Table 2: All experimental results (Realizable cases).

Effectiveness of Initial Condition Computation. We evaluated three approaches: monolithic synthesis (Strix), our fixpoint method (fp), and our memoization fixpoint method (fph). Addressing RQ1 (effectiveness of initial condition computation), both fp and fph successfully generated initial conditions for all tested specifications, resulting in successful synthesis in all realizable cases and consistently outperforming Strix (see Table 2). Moreover, in mode-deterministic unrealizable cases, our method correctly concluded unrealizability (by generating empty solutions), whereas Strix timed out in most of the cases (see Table 3).

Effectiveness of Search Space Pruning. Addressing RQ3, memoization's impact varied by specification structure. It modestly reduced realizability checks for specifications with mutually exclusive candidates (e.g., SCR). However, for larger search spaces with well-defined lattice structures, it significantly pruned the search space and improved execution time. Further evaluation using randomly generated conjunctive formulas (Spot [27]) as initial candidates confirmed that solver calls could be reduced by up to 50%. These results highlight memoization's potential, particularly in non-mutually exclusive specifications, with similar trends in unrealizable cases (Table 3), reinforcing approach's robustness.

Impact on Synthesis Time. Addressing RQ2 (synthesis time improvement), our comparative analysis revealed substantial differences. The monolithic method (Strix) consistently timed out (10 minutes) on larger instances, often failing due to formula size. In contrast, our methods, fp and fph, completed synthesis well within the time limit, reducing synthesis time by over 90%. Critically, as noted

Specifications		Time (s)			#S1	#Strix		Detailed time (fph)	
Name	#M	Strix	fp	fph	fp	fph	Proj.	Real.	
counter-10	2	8	0.61	0.59	3	2	0.39	0.20	
counter-14	7	timeout	4.33	2.66	21	16	1.69	0.98	
counter-20	4	timeout	2.24	4.60	11	10	3.28	1.33	
lift-15	3	timeout	6.92	10.93	9	7	9.50	1.44	
sis-1500	3	timeout	44.72	58.19	3	3	53.58	4.62	
double-counter-10	2	13	1.28	1.83	7	7	1.14	0.61	
double-counter-14	7	114	2.52	3.18	13	10	2.32	0.84	
double-counter-20	4	timeout	6.83	9.60	23	19	7.75	1.84	
cruise-fse	4	timeout	99.83	134.72	23	18	101.42	33.30	
altlayer	3	timeout	65.40	65.45	11	10	57.77	7.68	
fret-lift	4	timeout	79.80	81.80	10	10	64.18	17.61	
thermostat-80	3	74	110.67	100.84	9	7	94.20	6.64	
thermostat-150	3	ERROR	193.17	185.69	9	6	173.87	11.83	

Table 3: All experimental results (Unrealizable cases). All cases executed using mode-determinism.

by Mavridou et al. [54], state-of-the-art simultaneous decomposition tools [41,30] failed on all our specifications due to the frequent use of state variables/modes and many shared controllable variables. Our sequential mode-based approaches, however, excelled in these environments, demonstrating their adaptability where simultaneous methods fall short. This dramatic reduction in synthesis time shows that our method complements existing approaches by enabling synthesis for a broader class of complex real-world specifications.

# 5 Conclusion and Future Work

This paper presents a novel, fully automated mode-based reactive synthesis method. Taking an LTL  $(GX_0)$  specification and a set of modes (with optional transitions) as input, our iterative algorithm efficiently searches for initial condition combinations that realize the overall specification or concludes unrealizability. This automatic search for suitable initial conditions is a key feature of our approach, simplifying the synthesis process for engineers.

A current limitation is that completeness for unrealizability requires mode-based determinism and a subsuming mode-graph, which we plan to address in future work. However, our method achieves significantly faster synthesis compared to monolithic methods, enabling more effective derivation of controllers for complex real-world specifications, as supported by a thorough empirical evaluation. Future work will also explore controller explainability, extend to richer LTL fragments such as Safety-LTL, and investigate mode-synthesis with theories [64,32,65,48].

#### References

- 1. Iso/iec/ieee international standard systems and software engineering life cycle processes –requirements engineering. ISO/IEC/IEEE 29148:2011(E) pp. 1–94 (2011). https://doi.org/10.1109/IEEESTD.2011.6146379
- 2. Arabian-Hoseynabadi, H., Oraee, H., Tavner, P.: Failure modes and effects analysis (fmea) for wind turbines. International journal of electrical power & energy systems **32**(7), 817–824 (2010)
- 3. Arora, C., Grundy, J., Abdelrazek, M.: Advancing requirements engineering through generative AI: assessing the role of llms. CoRR abs/2310.13976 (2023). https://doi.org/10.48550/ARXIV.2310.13976, https://doi.org/10.48550/arXiv.2310.13976
- 4. Arora, C., Sabetzadeh, M., Briand, L., Zimmer, F.: Extracting domain models from natural-language requirements: approach and industrial evaluation. In: Proceedings of the ACM/IEEE 19th International Conference on Model Driven Engineering Languages and Systems. p. 250–260. MODELS '16, Association for Computing Machinery, New York, NY, USA (2016). https://doi.org/10.1145/2976767.2976769, https://doi.org/10.1145/2976767.2976769
- 5. Arvidsson, S., Axell, J.: Prompt engineering guidelines for llms in requirements engineering (2023)
- Balkan, A., Vardi, M., Tabuada, P.: Mode-target games: Reactive synthesis for control applications. IEEE Transactions on Automatic Control 63(1), 196–202 (2017)
- 7. Bansal, S., Li, Y., Tabajara, L.M., Vardi, M.Y.: Hybrid compositional reasoning for reactive synthesis from finite-horizon specifications. In: AAAI'20
- 8. Bauer, A., Leucker, M., Schallhart, C.: Runtime verification for LTL and TLTL. ACM Trans. Softw. Eng. Methodol. **20**(4), 14:1–14:64 (2011). https://doi.org/10.1145/2000799.2000800, https://doi.org/10.1145/2000799.2000800
- Beame, P., Impagliazzo, R., Pitassi, T., Segerlind, N.: Memoization and dpll: formula caching proof systems. In: 18th IEEE Annual Conference on Computational Complexity, 2003. Proceedings. pp. 248–259 (2003). https://doi.org/10.1109/CCC.2003.1214425
- Bloem, R., Jobstmann, B., Piterman, N., Pnueli, A., Sa'ar, Y.: Synthesis of reactive(1) designs. JCSS 78(3), 911–938 (2012)
- Brizzio, M.: Resolving goal-conflicts and scaling synthesis through mode-based decomposition. In: Proceedings of the 2024 IEEE/ACM 46th International Conference on Software Engineering: Companion Proceedings. pp. 207–211. ICSE-Companion '24, Association for Computing Machinery, New York, NY, USA (2024). https://doi.org/10.1145/3639478.3639801
   https://doi.org/10.1145/3639478.3639801
- 12. Brizzio, M., Cordy, M., Papadakis, M., Sánchez, C., Aguirre, N., Degiovanni, R.: Automated Repair of Unrealisable LTL Specifications Guided by Model Counting. In: Proc. of GECCO'23. pp. 1499–1507. ACM (2023), https://doi.acm.org/10.1145/3583131.3590454
- 13. Brizzio, M., Sánchez, C.: Efficient reactive synthesis using mode decomposition. In: Proc. of ICTAC 2023. LNCS, vol. 14446, pp. 256–275. Springer (2023), https://doi.org/10.1007/978-3-031-47963-2\_16
- 14. Brown, T.B., Mann, B., Ryder, N., Subbiah, M., Kaplan, J., Dhariwal, P., Neelakantan, A., Shyam, P., Sastry, G., Askell, A., Agarwal, S., Herbert-Voss, A.,

- Krueger, G., Henighan, T., Child, R., Ramesh, A., Ziegler, D.M., Wu, J., Winter, C., Hesse, C., Chen, M., Sigler, E., Litwin, M., Gray, S., Chess, B., Clark, J., Berner, C., McCandlish, S., Radford, A., Sutskever, I., Amodei, D.: Language Models are Few-Shot Learners. In: NeurIPS (2020)
- 15. Broy, M.: Multifunctional software systems: Structured modeling and specification of functional requirements. Science of Computer Programming **75**(12), 1193–1214 (2010). https://doi.org/https://doi.org/10.1016/j.scico.2010.06.007, https://www.sciencedirect.com/science/article/pii/S016764231000119X
- Carvalho, L., Degiovanni, R., Brizzio, M., Cordy, M., Aguirre, N., Traon, Y.L., Papadakis, M.: ACoRe: Automated Goal-Conflict Resolution. In: Proc. of FASE 2023. LNCS, vol. 13991, pp. 3–25. Springer (2023), https://doi.org/10.1007/ 978-3-031-30826-0\_1
- 17. Chatterjee, K., Henzinger, T.A., Otop, J., Pavlogiannis, A.: Distributed synthesis for ltl fragments. In: 2013 Formal Methods in Computer-Aided Design. pp. 18–25. IEEE (2013)
- 18. Chowdhery, A., Narang, S., Devlin, J., Bosma, M., Mishra, G., Roberts, A., Barham, P., Chung, H.W., Sutton, C., Gehrmann, S., others: Palm: Scaling language modeling with pathways. ArXiv abs/2204.02311 (2022)
- 19. Clarke, E., Grumberg, O., Peled, D.: Model Checking. The Cyber-Physical Systems Series, MIT Press (1999), https://books.google.es/books?id=Nmc4wEaLXFEC
- 20. De Giacomo, G., Favorito, M.: Compositional approach to translate LTLf/LDLf into deterministic finite automata. In: Proc. of ICAPS'21. pp. 122–130 (2021)
- Degiovanni, R., Molina, F., Regis, G., Aguirre, N.: A genetic algorithm for goal-conflict identification. In: Proc. of ASE 2018. pp. 520–531 (2018), https://doi.org/10.1145/3238147.3238220
- 22. Degiovanni, R., Ricci, N., Alrajeh, D., Castro, P.F., Aguirre, N.: Goal-conflict detection based on temporal satisfiability checking. In: Proc. of ASE 2016. pp. 507–518 (2016), http://doi.acm.org/10.1145/2970276.2970349
- 23. Devlin, J., Chang, M.W., Lee, K., Toutanova, K.: Bert: Pre-training of deep bidirectional transformers for language understanding (2019)
- Dietrich, D., Atlee, J.M.: A mode-based pattern for feature requirements, and a generic feature interface. In: 2013 21st IEEE International Requirements Engineering Conference (RE). pp. 82–91 (2013). https://doi.org/10.1109/RE.2013.6636708
- 25. Dietrich, D., Atlee, J.M.: A mode-based pattern for feature requirements, and a generic feature interface. 2013 21st IEEE International Requirements Engineering Conference (RE) pp. 82-91 (2013), https://api.semanticscholar.org/CorpusID:29015370
- 26. Dureja, R., Rozier, K.Y.: More scalable LTL model checking via discovering design-space dependencies  $(D^3)$ . In: Proc. of TACAS'18. pp. 309–327. Springer (2018)
- Duret-Lutz, A.: Manipulating LTL formulas using Spot 1.0. In: Proceedings of the 11th International Symposium on Automated Technology for Verification and Analysis (ATVA'13). Lecture Notes in Computer Science, vol. 8172, pp. 442–445. Springer, Hanoi, Vietnam (Oct 2013)
- 28. Esparza, J., Křetínský, J.: From LTL to deterministic automata: A safraless compositional approach. In: Proc. of CAV'14. pp. 192–208. Springer (2014)
- 29. Filippidis, I., Murray, R.M.: Layering assume-guarantee contracts for hierarchical system design. Proceedings of the IEEE 106(9), 1616-1654 (2018)
- 30. Finkbeiner, B., Geier, G., Passing, N.: Specification decomposition for reactive synthesis. ISSE (2022)

- 31. Fraser, G., Wotawa, F., Ammann, P.: Testing with model checkers: a survey. Softw. Test., Verif. Reliab. 19(3), 215–261 (2009). https://doi.org/10.1002/stvr.402, https://doi.org/10.1002/stvr.402
- 32. Gacek, A., Katis, A., Whalen, M.W., Backes, J., Cofer, D.D.: Towards realizability checking of contracts using theories. In: Havelund, K., Holzmann, G.J., Joshi, R. (eds.) NASA Formal Methods 7th International Symposium, NFM 2015, Pasadena, CA, USA, April 27-29, 2015, Proceedings. Lecture Notes in Computer Science, vol. 9058, pp. 173–187. Springer (2015). https://doi.org/10.1007/978-3-319-17524-9 13
- Giannakopoulou, D., Mavridou, A., Rhein, J., Pressburger, T., Schumann, J., Shi,
   N.: Formal requirements elicitation with fret. In: International Working Conference
   on Requirements Engineering: Foundation for Software Quality (REFSQ-2020).
   No. ARC-E-DAA-TN77785 (2020)
- 34. Golia, P., Roy, S., Meel, K.S.: Synthesis with explicit dependencies. In: 2023 Design, Automation & Test in Europe Conference & Exhibition (DATE). pp. 1–6. IEEE (2023)
- 35. Großer, K., Rukavitsyna, M., Jürjens, J.: A comparative evaluation of requirement template systems. In: Schneider, K., Dalpiaz, F., Horkoff, J. (eds.) 31st IEEE International Requirements Engineering Conference, RE 2023, Hannover, Germany, September 4-8, 2023. pp. 41–52. IEEE (2023). https://doi.org/10.1109/RE57278.2023.00014 https://doi.org/10.1109/RE57278.2023.00014
- 36. Harel, D.: Statecharts: a visual formalism for complex SVStems. Science of Computer Programming **8**(3), 231 - 274(1987).https://doi.org/https://doi.org/10.1016/0167-6423(87)90035-9, https: //www.sciencedirect.com/science/article/pii/0167642387900359
- 37. Heitmeyer, C.: Requirements models for critical systems. In: Software and Systems Safety, pp. 158–181. IOS Press (2011)
- 38. Heitmeyer, C., Labaw, B., Kiskis, D.: Consistency checking of SCR-style requirements specifications. In: Proc. of RE'95. pp. 56-63. IEEE (1995)
- 39. Heitmeyer, C.L., Archer, M., Bharadwaj, R., Jeffords, R.D.: Tools for constructing requirements specifications: the SCR toolset at the age of nine. Comput. Syst. Sci. Eng. **20**(1) (2005)
- Hermo, M., Lucio, P., Sánchez, C.: Tableaux for realizability of safety specifications. In: Proc. of the 25th International Symposium on Formal Methods (FM'23).
   LNCS, vol. 14000, pp. 495–513 (2023). https://doi.org/10.1007/978-3-031-27481-7\_28
   https://doi.org/10.1007/978-3-031-27481-7\_28
- 41. Iannopollo, A., Tripakis, S., Vincentelli, A.: Specification decomposition for synthesis from libraries of LTL assume/guarantee contracts. In: DATE. IEEE (2018)
- 42. Jacobs, S., Klein, F., Schirmer, S.: A high-level LTL synthesis format: TLSF v1.1. EPTCS 229, 112–132 (11 2016)
- 43. Konrad, S., Cheng, B.: Facilitating the construction of specification pattern-based properties. In: 13th IEEE International Conference on Requirements Engineering (RE'05). pp. 329–338 (2005). https://doi.org/10.1109/RE.2005.29
- 44. Kretínský, J., Meggendorfer, T., Sickert, S.: Owl: A library for ω-words, automata, and LTL. In: Lahiri, S.K., Wang, C. (eds.) Automated Technology for Verification and Analysis 16th International Symposium, ATVA 2018, Los Angeles, CA, USA, October 7-10, 2018, Proceedings. Lecture Notes in Computer Science, vol. 11138, pp. 543–550. Springer (2018). https://doi.org/10.1007/978-3-030-01090-4\_34, https://doi.org/10.1007/978-3-030-01090-4\_34

- 45. Kupferman, O., Piterman, N., Vardi, M.Y.: Safraless compositional synthesis. In: Proc. of CAV'06. pp. 31–44. Springer (2006)
- 46. Leveson, N.G.: Safeware: system safety and computers. ACM (1995)
- 47. Liu, J.X., Yang, Z., Idrees, I., Liang, S., Schornstein, B., Tellex, S., Shah, A.: Lang2ltl: Translating natural language commands to temporal robot task specification. In: Conference on Robbot Learning (2023)
- 48. Maderbacher, B., Bloem, R.: Reactive synthesis modulo theories using abstraction refinement. In: # PLACEHOLDER\_PARENT\_METADATA\_VALUE#.pp. 315–324. TU Wien Academic Press (2022)
- 49. Manna, Z., Pnueli, A.: The Temporal Logic of Reactive and Concurrent Systems. Springer-Verlag New York, Inc., New York, NY, USA (1992)
- 50. Manna, Z., Pnueli, A.: Temporal verification of reactive systems: safety. Springer-Verlag, New York, NY, USA (1995). https://doi.org/10.1007/978-1-4612-4222-2
- 51. Maoz, S., Ringert, J.O.: Spectra: a specification language for reactive systems. Software and Systems Modeling **20**(5), 1553–1586 (2021). https://doi.org/10.1007/s10270-021-00868-z, https://doi.org/10.1007/s10270-021-00868-z
- 52. Maoz, S., Shalom, R.: Unrealizable cores for reactive systems specifications: artifact. In: Proceedings of the 43rd International Conference on Software Engineering: Companion Proceedings. p. 217–218. ICSE '21, IEEE Press (2021). https://doi.org/10.1109/ICSE-Companion52605.2021.00097, https://doi.org/10.1109/ICSE-Companion52605.2021.00097
- 53. Mavin, A., Wilkinson, P., Harwood, A., Novak, M.: Easy approach to requirements syntax (ears). In: 2009 17th IEEE International Requirements Engineering Conference. pp. 317–322 (2009). https://doi.org/10.1109/RE.2009.9
- 54. Mavridou, A., Katis, A., Giannakopoulou, D., Kooi, D., Pressburger, T., Whalen, M.W.: From partial to global assume-guarantee contracts: Compositional realizability analysis in FRET. In: Huisman, M., Pasareanu, C.S., Zhan, N. (eds.) Formal Methods 24th International Symposium, FM 2021, Virtual Event, November 20-26, 2021, Proceedings. Lecture Notes in Computer Science, vol. 13047, pp. 503–523. Springer (2021). https://doi.org/10.1007/978-3-030-90870-6\_27, https://doi.org/10.1007/978-3-030-90870-6\_27
- 55. Meyer, P.J., Sickert, S., Luttenberger, M.: Strix: Explicit reactive synthesis strikes back! In: Proc. of CAV'18 (Part I). pp. 578–586. Springer (2018)
- 56. OpenAI: Gpt-4 technical report (2023)
- 57. Ouyang, L., Wu, J., Jiang, X., Almeida, D., Wainwright, C.L., Mishkin, P., Zhang, C., Agarwal, S., Slama, K., Ray, A., Schulman, J., Hilton, J., Kelton, F., Miller, L., Simens, M., Askell, A., Welinder, P., Christiano, P.F., Leike, J., Lowe, R.: Training language models to follow instructions with human feedback. CoRR abs/2203.02155 (2022)
- 58. Piterman, N., Pnueli, A., Sa'ar, Y.: Synthesis of reactive (1) designs. In: Proc. of VMCAI'06. pp. 364–380. Springer (2006)
- 59. Pnueli, A.: The temporal logic of programs. In: SFCS'77. pp. 46-57. IEEE (1977)
- 60. Pnueli, A., Rosner, R.: On the synthesis of a reactive module. In: Proc. of the 16th ACM SIGPLAN-SIGACT Symp. on Principles of Programming Languages (POPL'89). pp. 179–190. ACM, New York, NY, USA (1989). https://doi.org/10.1145/75277.75293, http://doi.acm.org/10.1145/75277.75293
- 61. Pressburger, T., Katis, A., Dutle, A., Mavridou, A.: Authoring, analyzing, and monitoring requirements for a lift-plus-cruise aircraft. In: Ferrari, A., Penzenstadler, B. (eds.) Requirements Engineering: Foundation for Software Quality -

- 29th International Working Conference, REFSQ 2023, Barcelona, Spain, April 17-20, 2023, Proceedings. Lecture Notes in Computer Science, vol. 13975, pp. 295–308. Springer (2023). https://doi.org/10.1007/978-3-031-29786-1\_21, https://doi.org/10.1007/978-3-031-29786-1\_21
- 62. Pudlitz, F., Brokhausen, F., Vogelsang, A.: Extraction of system states from natural language requirements. In: 2019 IEEE 27th International Requirements Engineering Conference (RE). pp. 211–222 (2019). https://doi.org/10.1109/RE.2019.00031
- 63. Pudlitz, F., Brokhausen, F., Vogelsang, A.: Extraction of system states from natural language requirements. In: 2019 IEEE 27th International Requirements Engineering Conference (RE). pp. 211–222 (2019). https://doi.org/10.1109/RE.2019.00031
- 64. Rodríguez, A., Sánchez, C.: Boolean abstractions for realizability modulo theories. In: Enea, C., Lal, A. (eds.) Computer Aided Verification 35th International Conference, CAV 2023, Paris, France, July 17-22, 2023, Proceedings, Part III. Lecture Notes in Computer Science, vol. 13966, pp. 305-328. Springer (2023). https://doi.org/10.1007/978-3-031-37709-9\_15, https://doi.org/10.1007/978-3-031-37709-9\_15
- 65. Rodríguez, A., Sánchez, C.: Adaptive reactive synthesis for ltl and ltlf modulo theories. In: Proceedings of the Thirty-Eighth AAAI Conference on Artificial Intelligence and Thirty-Sixth Conference on Innovative Applications of Artificial Intelligence and Fourteenth Symposium on Educational Advances in Artificial Intelligence. AAAI'24/IAAI'24/EAAI'24, AAAI Press (2024). https://doi.org/10.1609/aaai.v38i9.28939
- 66. de Roever, W.P., Langmaack, H., Pnueli, A. (eds.): Compositionality: The Significant Difference. Springer (1998). https://doi.org/10.1007/3-540-49213-5
- 67. Shaker, P., Atlee, J.M., Wang, S.: A feature-oriented requirements modelling language. In: 2012 20th IEEE International Requirements Engineering Conference (RE). pp. 151–160. IEEE (2012)
- 68. Thoppilan, R., Freitas, D.D., Hall, J., Shazeer, N., Kulshreshtha, A., Cheng, H.T., Jin, A., Bos, T., Baker, L., Du, Y., Li, Y., Lee, H., Zheng, H.S., Ghafouri, A., Menegali, M., Huang, Y., Krikun, M., Lepikhin, D., Qin, J., Chen, D., Xu, Y., Chen, Z., Roberts, A., Bosma, M., Zhao, V., Zhou, Y., Chang, C.C., Krivokon, I., Rusch, W., Pickett, M., Srinivasan, P., Man, L., Meier-Hellstern, K., Morris, M.R., Doshi, T., Santos, R.D., Duke, T., Soraker, J., Zevenbergen, B., Prabhakaran, V., Diaz, M., Hutchinson, B., Olson, K., Molina, A., Hoffman-John, E., Lee, J., Aroyo, L., Rajakumar, R., Butryna, A., Lamm, M., Kuzmina, V., Fenton, J., Cohen, A., Bernstein, R., Kurzweil, R., Aguera-Arcas, B., Cui, C., Croak, M., Chi, E., Le, Q.: Lamda: Language models for dialog applications (2022)
- 69. Touvron, H., Martin, L., Stone, K., Albert, P., Almahairi, A., Babaei, Y., Bashlykov, N., Batra, S., Bhargava, P., Bhosale, S., et al.: Llama 2: Open foundation and fine-tuned chat models. arXiv preprint arXiv:2307.09288 (2023)
- 70. Vogelsang, A., Femmer, H., Winkler, C.: Systematic elicitation of mode models for multifunctional systems. In: 2015 IEEE 23rd International Requirements Engineering Conference (RE). pp. 305–314. IEEE (2015)
- Vogelsang, A., Femmer, H., Winkler, C.: Take care of your modes! an investigation of defects in automotive requirements. In: Requirements Engineering: Foundation for Software Quality: 22nd International Working Conference, REFSQ 2016, Gothenburg, Sweden, March 14-17, 2016, Proceedings 22. pp. 161–167. Springer (2016)

- Wei, J., Tay, Y., Bommasani, R., Raffel, C., Zoph, B., Borgeaud, S., Yogatama,
   D., Bosma, M., Zhou, D., Metzler, D., Chi, E.H., Hashimoto, T., Vinyals, O.,
   Liang, P., Dean, J., Fedus, W.: Emergent abilities of large language models. CoRR
   abs/2206.07682 (2022)
- 73. Yang, J., Jin, H., Tang, R., Han, X., Feng, Q., Jiang, H., Yin, B., Hu, X.: Harnessing the power of llms in practice: A survey on chatgpt and beyond (2023)
- Ye, X., Ruess, H.: Efficient reactive synthesis. arXiv preprint arXiv:2404.17834 (2024)
- 75. Zeng, A., Liu, X., Du, Z., Wang, Z., Lai, H., Ding, M., Yang, Z., Xu, Y., Zheng, W., Xia, X., Tam, W.L., Ma, Z., Xue, Y., Zhai, J., Chen, W., Liu, Z., Zhang, P., Dong, Y., Tang, J.: Glm-130b: An Open Bilingual Pre-trained Model. ICLR 2023 poster (2023)
- Zhang, S., Roller, S., Goyal, N., Artetxe, M., Chen, M., Chen, S., Dewan, C., Diab, M., Li, X., Lin, X.V., Mihaylov, T., Ott, M., Shleifer, S., Shuster, K., Simig, D., Koura, P.S., Sridhar, A., Wang, T., Zettlemoyer, L.: Opt: Open Pre-trained Transformer Language Models. ArXiv abs/2205.01068 (2022)
- 77. Zhu, S., Tabajara, L.M., Li, J., Pu, G., Vardi, M.Y.: A symbolic approach to safety LTL synthesis. In: Proc. of HVC. pp. 147–162. Springer (2017)